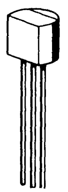
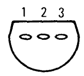
2N5460 (SILICON)
thru
2N5465

P-channel depletion mode (Type A) junction field-effect transistors designed for use in general-purpose amplifier applications.



CASE 29
(TO-92)



STYLE 7:
PIN 1 SOURCE
2 DRAIN
3. GATE

MAXIMUM RATINGS

Rating	Symbol	2N5460 2N5461 2N5462	2N5463 2N5464 2N5465	Unit
Drain-Gate Voltage	V_{DG}	40	60	Vdc
Reverse Gate-Source Voltage	$V_{GS(r)}$	40	60	Vdc
Forward Gate Current	$I_{G(f)}$	10		mAdc
Total Device Dissipation @ T_A = 25°C Derate above 25°C	P_D (1)	310 2.82		mW mW/°C
Storage Temperature Range	T_{stg} (1)	-65 to +150		°C
Operating Junction Temperature Range	T_J (1)	-65 to +135		°C

(1) Continuous package improvements have enhanced these guaranteed Maximum Ratings as follows. P_D = 1.0 W @ T_C = 25°C, Derate above 25°C – 8.0 mW/°C, T_J = -65 to +150°C, θ_{JC} = 125°C/W.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Gate-Source Breakdown Voltage (I_G = 10 μAdc, V_{DS} = 0)	2N5460, 2N5461, 2N5462	$V_{(BR)GSS}$	40	-	-	Vdc
	2N5463, 2N5464, 2N5465		60	-	-	
Gate-Source Cutoff Voltage (V_{DS} = 15 Vdc, I_D = 1.0 μAdc)	2N5460, 2N5463	$V_{GS(off)}$	0.75	-	6.0	Vdc
	2N5461, 2N5464		1.0	-	7.5	
	2N5462, 2N5465		1.8	-	9.0	
Gate Reverse Current (V_{GS} = 20 Vdc, V_{DS} = 0)	2N5460, 2N5461, 2N5462	I_{GSS}	-	-	5.0	nAdc
(V_{GS} = 30 Vdc, V_{DS} = 0)	2N5463, 2N5464, 2N5465		-	-	5.0	
(V_{GS} = 20 Vdc, V_{DS} = 0, T_A = 100°C)	2N5460, 2N5461, 2N5462		-	-	1.0	μAdc
(V_{GS} = 30 Vdc, V_{DS} = 0, T_A = 100°C)	2N5463, 2N5464, 2N5465		-	-	1.0	
ON CHARACTERISTICS						
Zero-Gate Voltage Drain Current (V_{DS} = 15 Vdc, V_{GS} = 0)	2N5460, 2N5463	I_{DSS}	1.0	-	5.0	mAdc
	2N5461, 2N5464		2.0	-	9.0	
	2N5462, 2N5465		4.0	-	16	
Gate-Source Voltage (V_{DS} = 15 Vdc, I_D = 0.1 mAdc)	2N5460, 2N5463	V_{GS}	0.5	-	4.0	Vdc
(V_{DS} = 15 Vdc, I_D = 0.2 mAdc)	2N5461, 2N5464		0.8	-	4.5	
(V_{DS} = 15 Vdc, I_D = 0.4 mAdc)	2N5462, 2N5465		1.5	-	6.0	
SMALL-SIGNAL CHARACTERISTICS						
Forward Transadmittance (V_{DS} = 15 Vdc, V_{GS} = 0, f = 1.0 kHz)	2N5460, 2N5463	$\lvert y_{fs} \rvert$	1000	-	4000	μmhos
	2N5461, 2N5464		1500	-	5000	
	2N5462, 2N5465		2000	-	6000	
Output Admittance (V_{DS} = 15 Vdc, V_{GS} = 0, f = 1.0 kHz)		$\lvert y_{os} \rvert$	-	-	75	μmhos
Input Capacitance (V_{DS} = 15 Vdc, V_{GS} = 0, f = 1.0 MHz)		C_{iss}	-	5.0	7.0	pF
Reverse Transfer Capacitance (V_{DS} = 15 Vdc, V_{GS} = 0, f = 1.0 MHz)		C_{rss}	-	1.0	2.0	pF
Common-Source Noise Figure (V_{DS} = 15 Vdc, V_{GS} = 0, R_G = 1.0 Megohm, f = 100 Hz, BW = 1.0 Hz)		NF	-	1.0	2.5	dB
Equivalent Short-Circuit Input Noise Voltage (V_{DS} = 15 Vdc, V_{GS} = 0, f = 100 Hz, BW = 1.0 Hz)		e_n	-	60	115	nV/√Hz

DRAIN CURRENT versus GATE SOURCE VOLTAGE

FIGURE 1 – 2N5460 and 2N5463

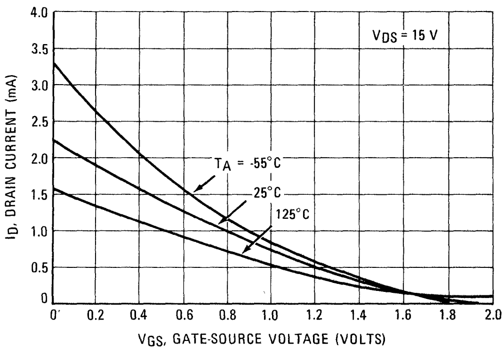


FIGURE 2 – 2N5461 and 2N5464

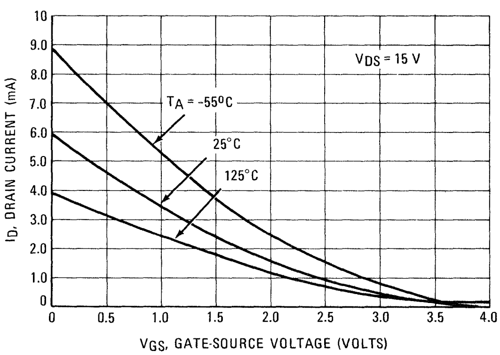
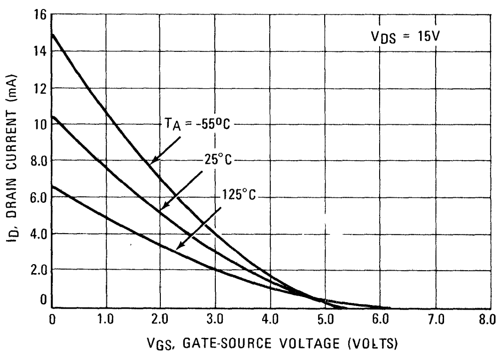


FIGURE 3 – 2N5462 and 2N5465



FORWARD TRANSFER ADMITTANCE versus DRAIN CURRENT

FIGURE 4 – 2N5460 and 2N5463

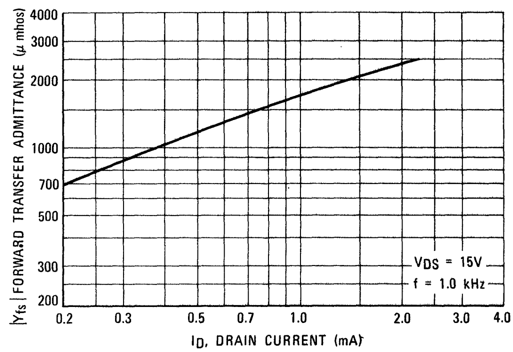


FIGURE 5 – 2N5461 and 2N5464

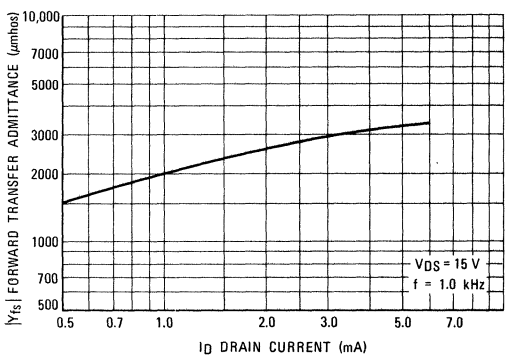


FIGURE 6 – 2N5462 and 2N5465

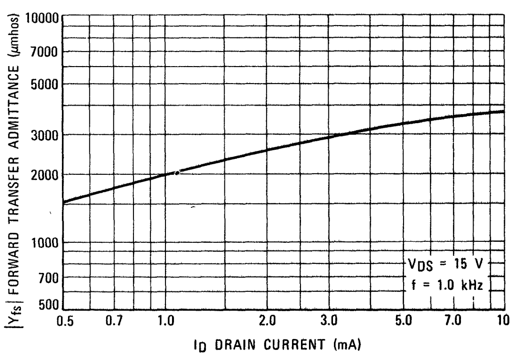


FIGURE 7 – OUTPUT RESISTANCE VERSUS DRAIN CURRENT

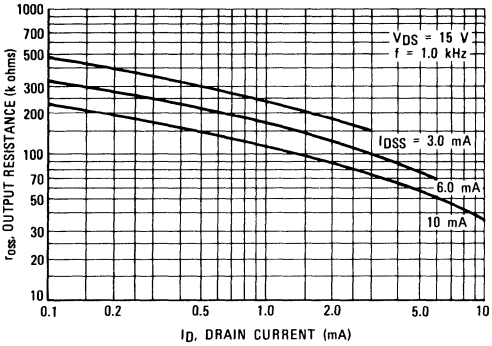


FIGURE 8 – CAPACITANCE VERSUS DRAIN-SOURCE VOLTAGE

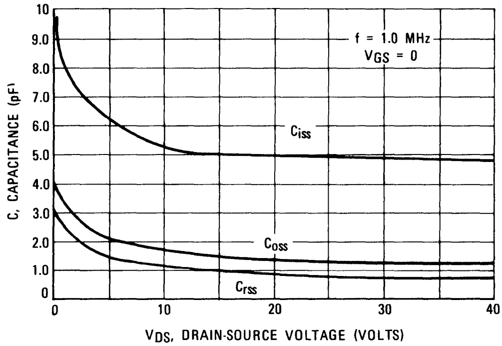


FIGURE 9 – NOISE FIGURE VERSUS FREQUENCY

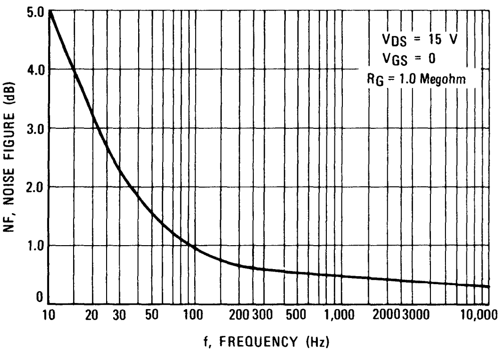


FIGURE 10 – NOISE FIGURE VERSUS SOURCE RESISTANCE

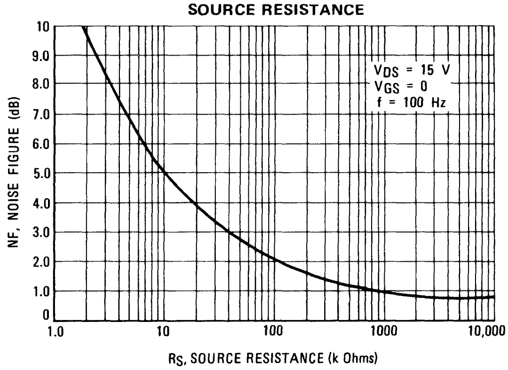
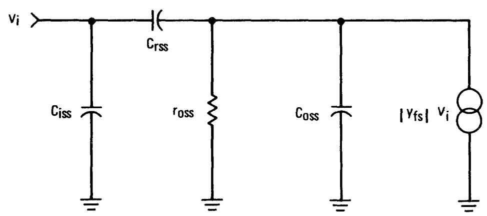


FIGURE 11 – EQUIVALENT LOW FREQUENCY CIRCUIT



Common Source
y Parameters for Frequencies
Below 30 MHz

$y_{is} = j\omega C_{iss}$

$y_{os} = j\omega C_{osp}{}^{*} + 1/r_{oss}$

$y_{fs} = |y_{fs}|$

$y_{rs} = -j\omega C_{rss}$

*C_{osp} is C_{oss} in parallel with Series Combination of C_{iss} and C_{rss}.

NOTE:

1. Graphical data is presented for dc conditions. Tabular data is given for pulsed conditions (Pulse Width = 630 ms, Duty Cycle = 10%).