

isc Silicon NPN Power Transistor

2N5466

DESCRIPTION

- Excellent Safe Operating Area
- Low Collector-Emitter Saturation Voltage
- The device employs the popular JEDEC TO-3
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

APPLICATIONS

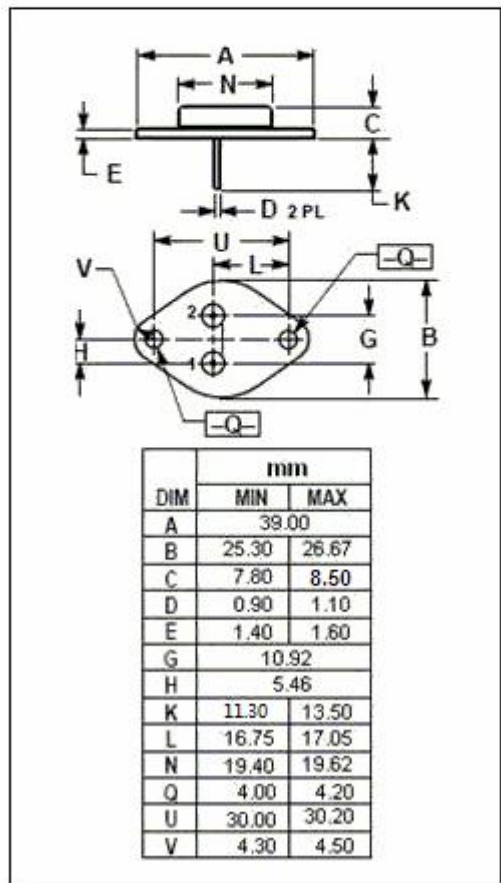
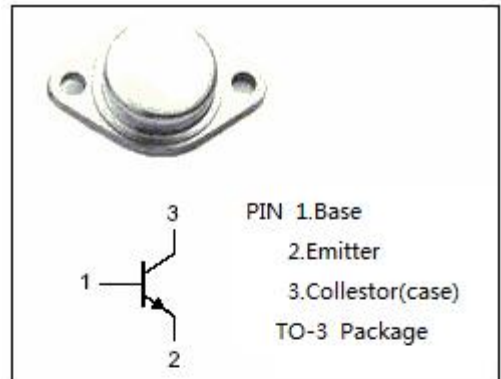
- High voltage high current power transistors

ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CB0}	Collector-Base Voltage	500	V
V _{CEO}	Collector-Emitter Voltage	400	V
V _{EBO}	Emitter-Base Voltage	7	V
I _c	Collector Current-Continuous	3	A
P _c	Collector Power Dissipation@T _c =25°C	140	W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-65~200	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case	1.48	°C/W



isc Silicon NPN Power Transistor**2N5466****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CEO(SUS)}^*$	Collector-Emitter Sustaining Voltage	$I_C=200\text{mA}; I_B=0$	400		V
I_{CBO}	Collector Cutoff Current	$V_{CB}=500\text{V}; I_B=0$		1	mA
I_{EBO}	Emitter Cutoff Current	$V_{EB}=5\text{V}; I_C=0$		0.1	mA
h_{FE-1}	DC Current Gain	$I_C=1\text{A}; V_{CE}=4\text{V}$	15	45	
h_{FE-2}	DC Current Gain	$I_C=2\text{A}; V_{CE}=4\text{V}$	8		
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=2\text{A}; I_B=0.4\text{A}$		2.0	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C=2\text{A}; I_B=0.4\text{A}$		2.0	V

*:Pulse test:Pulse width=300us,duty cycle \leq 2%**NOTICE:**

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