



isc Silicon NPN Power Transistor

2N5469

DESCRIPTION

- Excellent Safe Operating Area
- · Low Collector-Emitter Saturation Voltage
- The device employs the popular TO-66
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

APPLICATIONS

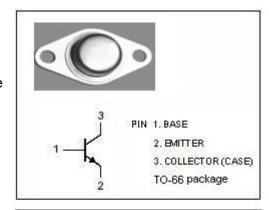
· High voltage high current power transistors

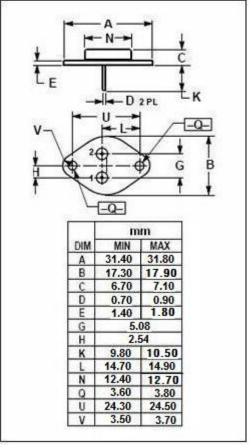
ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	700	V
V _{CEO}	Collector-Emitter Voltage	400	V
V _{EBO}	Emitter-Base Voltage	7	V
Ic	Collector Current-Continuous	3	А
Pc	Collector Power Dissipation@T _C =25°C	70	W
TJ	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature	-65~200	$^{\circ}$ C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance,Junction to Case	2.5	°C/W







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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CEO(SUS)} *	Collector-Emitter Sustaining Voltage	I _C =200mA; I _B = 0	400		V
I _{CBO}	Collector Cutoff Current	V _{CB} =700V;I _B = 0		1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} =5V; I _C = 0		0.1	mA
h _{FE}	DC Current Gain	Ic=3A; VcE= 5V	15	60	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 0.6A		5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 3A; I _B = 0.6A		1.5	V

^{*:}Pulse test:Pulse width=300us,duty cycle≤2%



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