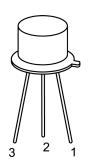
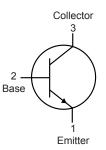
Bipolar Transistor



RoHS Compliant







Description:

A Silicon epitaxial NPN planer transistor in a TO-39 type package designed for use as drivers for high Power transistors in general purpose amplifier and switching circuits.

Maximum Ratings:

Characteristic	Symbol	Rating	Unit	
Collector Emitter Voltage	V _{CEO}	400		
Collector Base Voltage	$(I_E = 0), V_{CBO}$	100	V	
Emitter Base Voltage	(I _C = 0), V _{EBO}	4		
Collector Current	I _C	1	A	
Base Current	I _B	500	mA	
Total Device Dissipation	$(T_C = +25^{\circ}C), P_{tot}$	10	W	
Total Device Dissipation	$(T_A = +25^{\circ}C), P_{tot}$	1	VV	
Operating Junction Temperature	T_J	-65 to +200	°C	
Storage Temperature Range	T _{stg}	-65 to +200		
Thermal Resistance, Junction-to-Case	R _{thJC}	17.4	°C/W	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	175	C/VV	



Bipolar Transistor

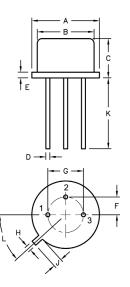


Electrical Characteristics: $(T_A = +25^{\circ}C \text{ unless otherwise specified})$

Parameter	Symbol	Test Conditions	Min	Max	Unit
	I _{CBO}	V _{CB} = 100V, I _E = 0		1	
Collector Cutoff Current	I _{CEO}	I_{CEO} $V_{CE} = 70V, I_{B} = 0$ I_{CEV} $V_{CE} = 100V, V_{BE} = -1.5V$		10	μΑ
Collector Cutoff Current	I _{CEV}				
		$V_{CE} = 100V, V_{BE} = -1.5V, T_{C} = +150^{\circ}C$		1	mA
Emitter Cutoff Current	I _{EBO}	$V_{EB} = 4V, I_{C} = 0$			μΑ
Collector-Emitter Sustaining Voltage	V _{CEO(SUS)}	I _C = 10mA, I _B = 0, Note 1	100	-	
	V _{CE(Sat)}	I _C = 250mA, I _B = 25mA, Note 1		0.6	
Collector-Emitter Saturation Voltage	, ,	$I_{\rm C}$ = 500mA, $I_{\rm B}$ = 50mA, Note 1		1	V
		I _C = 1A, I _B = 200mA, Note1	_	2	
Base-Emitter Voltage	V _{BE(on)}	$V_{CE} = 2V, I_{C} = 250 \text{mA}$		1	
DC Current Gain	h _{FE}	I _C = 250mA, V _{CE} = 2V, Note 1	40	150	
DC Current Gain		I _C = 1A, V _{CE} = 2V, Note 1	5		-
Transition Frequency	f _T	$V_{CE} = 10V, I_{C} = 100mA, f = 10MHz$	30	_	MHz
Collector-Base Capacitance	C _{cbo}	V _{CB} = 20V, I _E = 0, f = 1MHz	-	50	pF
Small-Signal Current Gain	h _{fe}	$V_{CE} = 1.5V, I_{C} = 200mA, f = 1kHz$	40	-	-

Note

1. Pulse Duration: 300µs, Duty Cycle ≦2%



Dimensions	Α	В	С	D	E	F	G	Н	J	K	L
Min.	8.5	7.74	6.09	0.4	-	2.41	4.82	0.71	0.73	12.7	42°
Max.	9.39	8.5	6.6	0.53	0.88	2.66	5.33	0.86	1.02	-	48°

Dimensions: Millimetres

Pin Configuration:

- 1. Emitter
- 2. Base 1
- 3. Base 2

Part Number Table

Description	Part Number				
Transistor, NPN, 1A, 100V, TO-39	2N5681				

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