

# 2N5692 thru 2N5696 (GERMANIUM)

## PNP GERMANIUM POWER SWITCHING TRANSISTORS

... designed for high-current, fast-switching applications requiring low saturation voltage and excellent safe operating area.

**40 AMPERE "ADE"**  
**POWER TRANSISTORS**  
**PNP GERMANIUM**

**50-140 VOLTS**  
**120 WATTS**

### MAXIMUM RATINGS

Rating	Symbol	2N5692	2N5693	2N5694	2N5695	2N5696	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	30	60	80	100	120	Vdc
Collector-Base Voltage	V <sub>CB</sub>	50	80	100	120	140	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	—	—	2.5	—	—	Vdc
Collector Current — Continuous	I <sub>C</sub>	—	—	40	—	—	Adc
— Peak		—	—	60	—	—	
Base Current — Continuous	I <sub>B</sub>	—	—	12	—	—	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	—	—	120	—	—	Watts W/°C
Operating and Storage Junction Temperature Range	T <sub>J,Tstg</sub>	—	—	—65 to +110	—	—	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	θ <sub>JC</sub>	0.7	°C/W

\*Indicates JEDEC Registered Data.

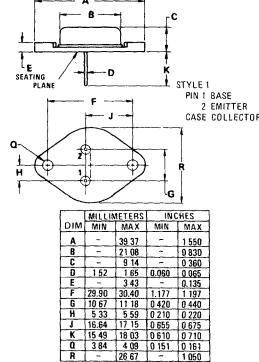
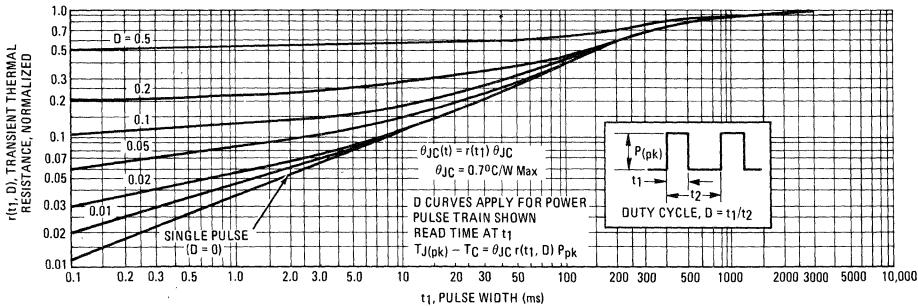


FIGURE 1 – THERMAL RESPONSE



# 2N5692 thru 2N5696 (continued)

ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$  unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>					
* Collector-Emitter Breakdown Voltage ( $I_C = 0.1 \text{ Adc}, I_B = 0$ )	2N5692 2N5693 2N5694 2N5695 2N5696	$BV_{CEO}$	30 60 80 100 120	— — — — —	Vdc
* Collector Cutoff Current ( $V_{CE} = 50 \text{ Vdc}$ ) $V_{BE(\text{off})} = 0.2 \text{ Vdc}$	2N5692	$I_{CEX1}$	—	10	mAdc
( $V_{CE} = 80 \text{ Vdc}$ )	2N5693		—	10	
( $V_{CE} = 100 \text{ Vdc}$ ) For	2N5694		—	10	
( $V_{CE} = 120 \text{ Vdc}$ ) All Types	2N5695 2N5696		—	10	
( $V_{CE} = 140 \text{ Vdc}$ )			—	10	
* Collector Cutoff Current ( $V_{CE} = 50 \text{ Vdc}$ ) $V_{BE(\text{off})} = 0.2 \text{ Vdc}, T_C = +85^\circ\text{C}$	2N5692	$I_{CEX2(1)}$	—	30	mAdc
( $V_{CE} = 80 \text{ Vdc}$ ) For	2N5693		—	30	
( $V_{CE} = 100 \text{ Vdc}$ ) All Types	2N5694 2N5695 2N5696		—	30	
* Collector-Emitter Sustaining Voltage (See Figure 3) ( $I_C = 10 \text{ Adc}$ )	2N5692 2N5693 2N5694 2N5695 2N5696	$V_{CEX(\text{sus})}$	50 80 100 120 140	— — — — —	Vdc
( $I_C = 40 \text{ Adc}$ )	2N5692 2N5693 2N5694 2N5695 2N5696		45 50 55 60 65	— — — — —	
* Collector Cutoff Current ( $V_{CB} = 2.0 \text{ Vdc}, I_E = 0$ )		$I_{CBO}$	—	200	$\mu\text{Adc}$
* Emitter Cutoff Current ( $V_{BE} = 2.5 \text{ Vdc}, I_C = 0$ )		$I_{EBO}$	—	500	mAdc
<b>ON CHARACTERISTICS (1)</b>					
* DC Current Gain ( $I_C = 25 \text{ Adc}, V_{CE} = 2.0 \text{ Vdc}$ ) ( $I_C = 40 \text{ Adc}, V_{CE} = 2.0 \text{ Vdc}$ )		$h_{FE}$	20 10	65 —	—
* Collector-Emitter Saturation Voltage ( $I_C = 60 \text{ Adc}, I_B = 12 \text{ Adc}$ )		$V_{CE(\text{sat})}$	—	0.75	Vdc
* Base-Emitter Saturation Voltage ( $I_C = 60 \text{ Adc}, I_B = 12 \text{ Adc}$ )		$V_{BE(\text{sat})}$	—	1.2	Vdc

## SMALL-SIGNAL CHARACTERISTICS

* Current-Gain-Bandwidth Product ( $I_C = 5.0 \text{ Adc}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ kHz}$ )	$f_T$	200	—	kHz
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## SWITCHING CHARACTERISTICS

* Rise Time		$t_r$	—	20	$\mu\text{s}$
* Storage Time	( $I_C = 25 \text{ Adc}, I_B1 = 2.5 \text{ Adc}, I_B2 = 2.5 \text{ Adc}$ (See Figure 2))	$t_s$	—	8.0	$\mu\text{s}$
* Fall Time		$t_f$	—	15	$\mu\text{s}$

\* Indicates JEDEC Registered Data.

(1) To avoid excessive heating of the collector junction, perform test with pulse method. ( $PW \leq 300 \mu\text{s}, DC \leq 2.0\%$ ).

FIGURE 2 – SWITCHING TIME TEST CIRCUIT

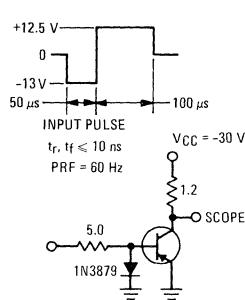
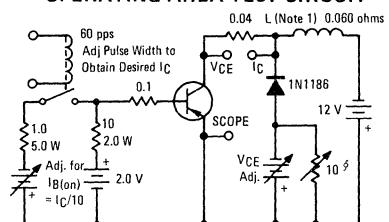


FIGURE 3 – CLAMPED INDUCTIVE SAFE OPERATING AREA TEST CIRCUIT



§ Set to allow reverse surge current to bypass power supply. Not needed for power supply with low reverse impedance.

NOTE 1.  $L = 10 \text{ mH at } I_C = 10 \text{ A}$   
 $L = 0.25 \text{ mH at } I_C = 40 \text{ A}$