

Silicon NPN Power Transistors

2N5732

**DESCRIPTION**

- With TO-3 package
- High current capability

**APPLICATIONS**

- For linear amplifier and inductive switching applications

**PINNING(see fig.2)**

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

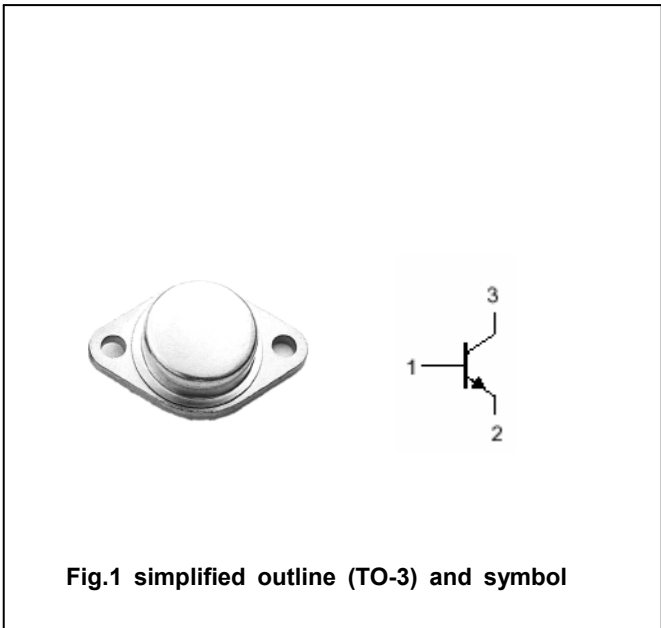


Fig.1 simplified outline (TO-3) and symbol

**ABSOLUTE MAXIMUM RATINGS(Ta=25°C)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	100	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	80	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	7	V
I <sub>C</sub>	Collector current		20	A
I <sub>CM</sub>	Collector current-peak		30	A
P <sub>T</sub>	Total power dissipation	T <sub>C</sub> =25°C	87.5	W
T <sub>j</sub>	Junction temperature		150	°C
T <sub>stg</sub>	Storage temperature		-65~200	°C

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
R <sub>th j-c</sub>	Thermal resistance junction to case	1.17	°C/W

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## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CE0(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>C</sub> =0.2A; I <sub>B</sub> =0	80			V
V <sub>CEsat-1</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =10 A; I <sub>B</sub> =1 A			1.4	V
V <sub>CEsat-2</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =20 A; I <sub>B</sub> =4 A			4.0	V
V <sub>BE</sub>	Base-emitter on voltage	I <sub>C</sub> =10 A; V <sub>CE</sub> =4V			2.2	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =100V; I <sub>E</sub> =0			0.1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =7V; I <sub>C</sub> =0			0.1	mA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =5A ; V <sub>CE</sub> =2V	30		300	
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =20A ; V <sub>CE</sub> =4V	5			
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =1A ; V <sub>CE</sub> =10V	30			MHz

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PACKAGE OUTLINE

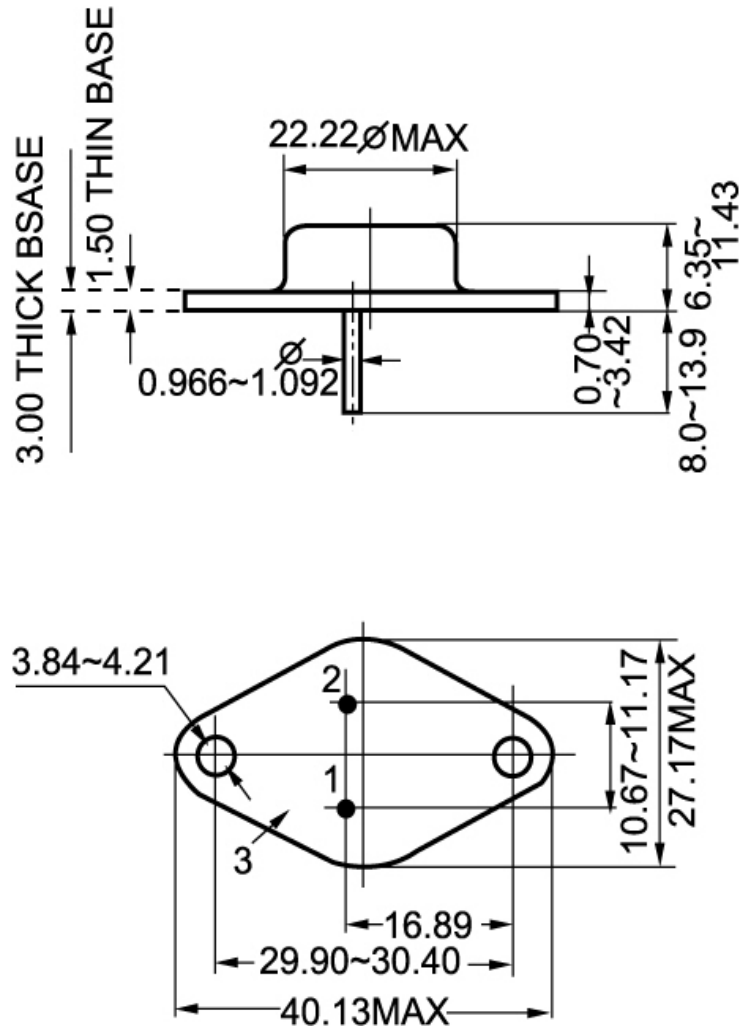


Fig.2 Outline dimensions