

isc Silicon NPN Power Transistors
2N5885
DESCRIPTION

- Low Collector Saturation Voltage-
: $V_{CE(sat)} = 1.0V(\text{Max.}) @ I_C = 15A$
- DC Current Gain-
: $h_{FE} = 20- @ I_C = 10A$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

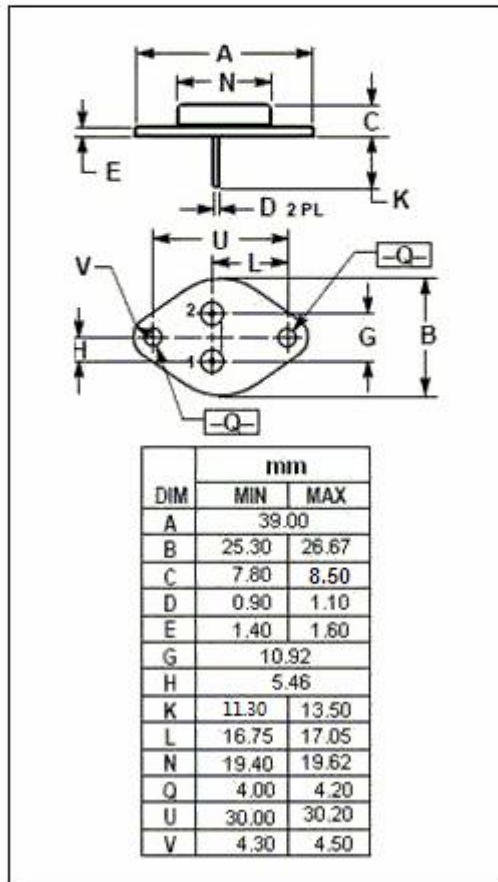
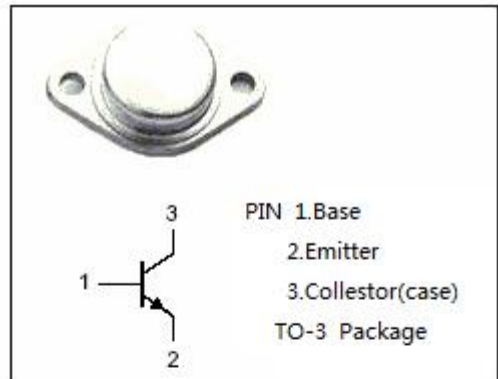
- Designed for general-purpose power amplifier and switching applications.

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	60	V
V_{CEO}	Collector-Emitter Voltage	60	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	25	A
I_{CM}	Collector Current-Peak	50	A
I_B	Base Current-Continuous	7.5	A
P_C	Collector Power Dissipation@ $T_C = 25^\circ\text{C}$	200	W
T_J	Junction Temperature	200	$^\circ\text{C}$
T_{stg}	Storage Temperature	-65~200	$^\circ\text{C}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Thermal Resistance, Junction to Case	0.875	$^\circ\text{C/W}$



isc Silicon NPN Power Transistors**2N5885****ELECTRICAL CHARACTERISTICS** $T_C=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CE(SUS)}$ *	Collector-Emitter Sustaining Voltage	$I_C=200\text{mA}; I_B=0$	60		V
$V_{CE(sat)-1}$	Collector-Emitter Saturation Voltage	$I_C=15\text{A}; I_B=1.5\text{A}$		1.0	V
$V_{CE(sat)-2}$	Collector-Emitter Saturation Voltage	$I_C=25\text{A}; I_B=6.25\text{A}$		4.0	v
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C=25\text{A}; I_B=6.25\text{A}$		2.5	V
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C=10\text{A}; V_{CE}=4\text{V}$		1.5	V
I_{CEO}	Collector Cutoff Current	$V_{CE}=30\text{V}; I_B=0$		2.0	mA
I_{CBO}	Collector Cutoff Current	$V_{CB}=60\text{V}; I_E=0$		1.0	mA
I_{EBO}	Emitter Cutoff Current	$V_{EB}=5\text{V}; I_C=0$		1.0	mA
h_{FE-1}	DC Current Gain	$I_C=3\text{A}; V_{CE}=4\text{V}$	35		
h_{FE-2}	DC Current Gain	$I_C=10\text{A}; V_{CE}=4\text{V}$	20	100	
h_{FE-3}	DC Current Gain	$I_C=25\text{A}; V_{CE}=4\text{V}$	4		
f_T	Current-Gain—Bandwidth Product	$I_C=0.1\text{A}; V_{CE}=10\text{V}; f_{test}=1.0\text{MHz}$	4		MHz

*:Pulse test $PW \leq 300\mu\text{s}$, duty cycle $\leq 2\%$

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