

2N60-CBS

Power MOSFET

2.0A, 600V N-CHANNEL POWER MOSFET

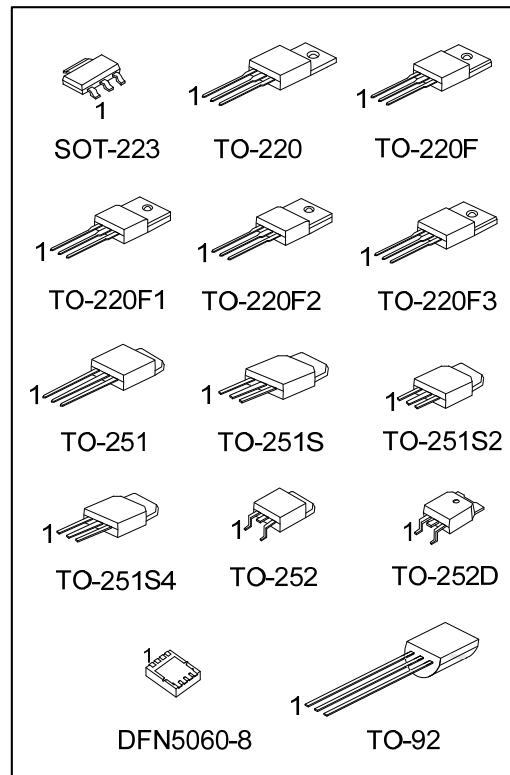
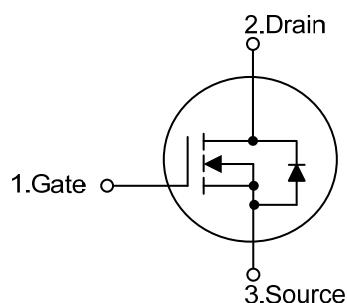
■ DESCRIPTION

The UTC **2N60-CBS** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

■ FEATURES

- * $R_{DS(ON)} \leq 8.5\Omega$ @ $V_{GS}=10V$, $I_D=1.0A$
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
2N60L-AA3-R	2N60G-AA3-R	SOT-223	G	D	S	-	-	-	-	-	Tape Reel
2N60L-TA3-T	2N60G-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
2N60L-TF3-T	2N60G-TF3-T	TO-220F	G	D	S	-	-	-	-	-	Tube
2N60L-TF1-T	2N60G-TF1-T	TO-220F1	G	D	S	-	-	-	-	-	Tube
2N60L-TF2-T	2N60G-TF2-T	TO-220F2	G	D	S	-	-	-	-	-	Tube
2N60L-TF3T-T	2N60G-TF3T-T	TO-220F3	G	D	S	-	-	-	-	-	Tube
2N60L-TM3-T	2N60G-TM3-T	TO-251	G	D	S	-	-	-	-	-	Tube
2N60L-TMS-T	2N60G-TMS-T	TO-251S	G	D	S	-	-	-	-	-	Tube
2N60L-TMS2-T	2N60G-TMS2-T	TO-251S2	G	D	S	-	-	-	-	-	Tube
2N60L-TMS4-T	2N60G-TMS4-T	TO-251S4	G	D	S	-	-	-	-	-	Tube
2N60L-TN3-R	2N60G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
2N60L-TND-R	2N60G-TND-R	TO-252D	G	D	S	-	-	-	-	-	Tape Reel
2N60L-T92-B	2N60G-T92-B	TO-92	G	D	S	-	-	-	-	-	Tape Box
2N60L-T92-K	2N60G-T92-K	TO-92	G	D	S	-	-	-	-	-	Bulk
2N60L-K08-5060-R	2N60G-K08-5060-R	DFN5060-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 2N60G-AA3-R	(1) T: Tube, R: Tape Reel (2) AA3: SOT-223, TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF1: TO-220F2, TF3T: TO-220F3, TM3: TO-251, TMS: TO-251S, TMS2: TO-251S2, TMS4: TO-251S4, TN3: TO-252, TND: TO-252D T92: TO-92, K08-5060: DFN5060-8 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

PACKAGE	MARKING
SOT-223	 2N60□ Lot Code → [□□□□] Date Code 1
TO-220 / TO-220F TO-220F1 / TO-220F2 TO-220F3 / TO-251 TO-251S / TO-251S2 TO-251S4 / TO-252 TO-252D	 UTC 2N60□ Lot Code → [□□□□] Date Code 1
TO-92	 UTC 2N60□ Lot Code → [□□□□] Date Code 1
DFN5060-8	 UTC 2N60 • [□□□] [□□□□] Internal Code → [□□□] Date Code 1

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	± 30	V
Avalanche Current (Note 2)		I_{AR}	0.77	A
Drain Current	Continuous	I_D	2	A
	Pulsed (Note 2)	I_{DM}	4	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	21.6	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.0	V/ns
Power Dissipation	SOT-223	P_D	8	W
	TO-220		44	W
	TO-220F/TO-220F1		23	W
	TO-220F3		24	W
	TO-220F2		30	W
	TO-251/TO-251S		1.5	W
	TO-251S2/TO-251S4		22	W
	TO-252/TO-252D		-55 ~ +150	°C
	TO-92		-55 ~ +150	°C
Junction Temperature	DFN5060-8	T_J	+150	°C
			-55 ~ +150	°C
			-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L=30mH, $I_{AS}=1.8\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 2.0\text{A}$, $di/dt \leq 100\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	θ_{JA}	150	°C/W
	TO-220/TO-220F		62.5	°C/W
	TO-220F1/ TO-220F2		100	°C/W
	TO-220F3		180	°C/W
	TO-251/TO-251S		75	°C/W
	TO-251S2/TO-251S4		14	°C/W
	TO-252/TO-252D		2.84	°C/W
	TO-92		5.4	°C/W
	DFN5060-8		5.3	°C/W
Junction to Case	SOT-223 (Note)	θ_{JC}	4.16	°C/W
	TO-220		80	°C/W
	TO-220F/TO-220F1		5.6	°C/W
	TO-220F3		5.4	°C/W
	TO-220F2		5.3	°C/W
	TO-251/TO-251S		4.16	°C/W
	TO-251S2/TO-251S4		80	°C/W
	TO-252/TO-252D (Note)		5.6	°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

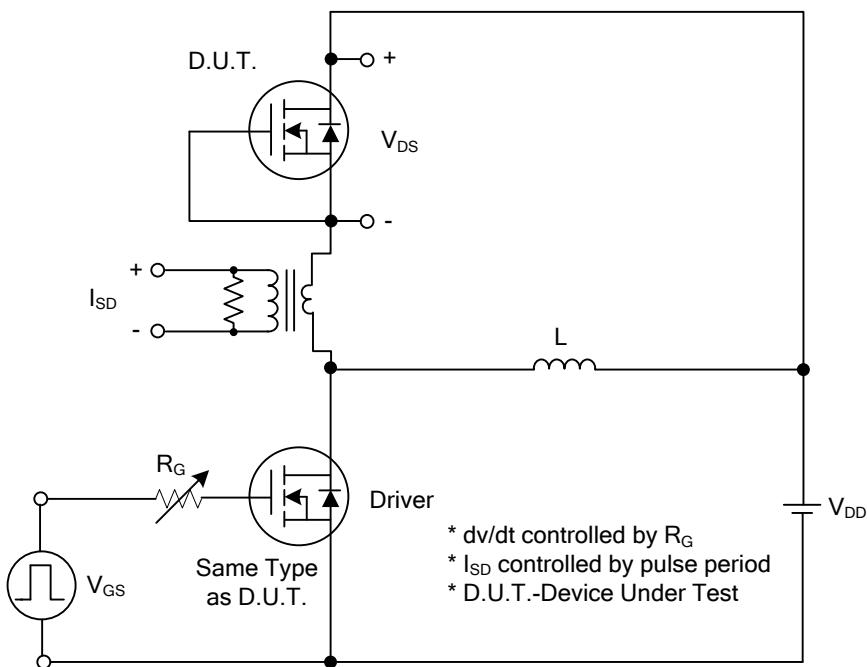
■ ELECTRICAL CHARACTERISTICS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	600			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 600\text{V}, V_{\text{GS}} = 0\text{V}$		10		μA
		$V_{\text{DS}} = 480\text{V}, T_c = 125^\circ\text{C}$		100		μA
Gate-Source Leakage Current	Forward	$V_{\text{GS}} = 30\text{V}, V_{\text{DS}} = 0\text{V}$		100	nA	
	Reverse	$V_{\text{GS}} = -30\text{V}, V_{\text{DS}} = 0\text{V}$		-100	nA	
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 1.0\text{A}$			8.5	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		158		pF
Output Capacitance	C_{OSS}			21		pF
Reverse Transfer Capacitance	C_{RSS}			3		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q_G	$V_{\text{DS}} = 480\text{V}, V_{\text{GS}} = 1.0\text{V}, I_{\text{D}} = 2.0\text{A}$ (Note 1, 2)		9		nC
Gate-Source Charge	Q_{GS}			4.5		nC
Gate-Drain Charge	Q_{GD}			1.5		nC
Turn-On Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}} = 100\text{V}, I_{\text{D}} = 2.0\text{A}, R_{\text{G}} = 25\Omega$ (Note 1, 2)		4.8		ns
Turn-On Rise Time	t_{R}			15		ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			42		ns
Turn-Off Fall Time	t_{F}			32		ns
DRAIN-SOURCE DIODE CHARACTERISTICS						
Continuous Drain-Source Current	I_S				2	A
Pulsed Drain-Source Current	I_{SM}				4	A
Drain-Source Diode Forward Voltage	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_{\text{SD}} = 2.0\text{A}$			1.4	V
Body Diode Reverse Recovery Time	t_{rr}	$I_{\text{F}} = 2.0\text{A}, V_{\text{DD}} = 400\text{V}, dI/dt = 100\text{A}/\mu\text{s}$		210		ns
Body Diode Reverse Recovery Charge	Q_{rr}			656		nC

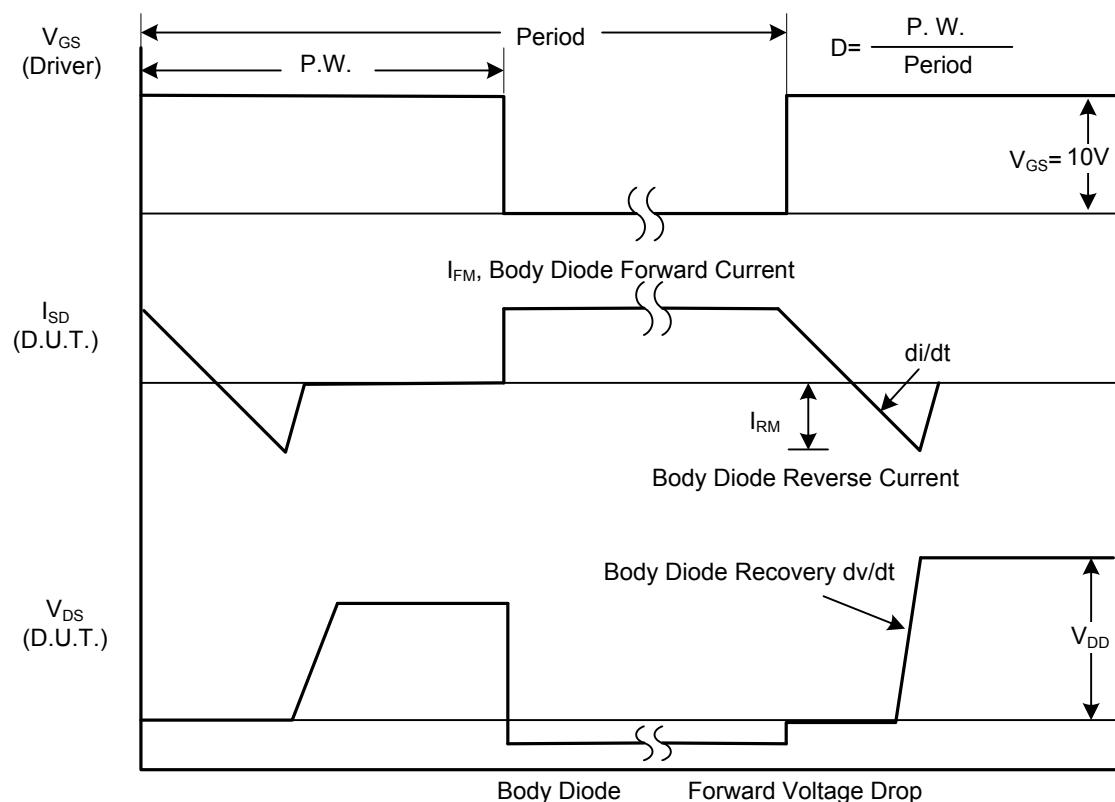
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

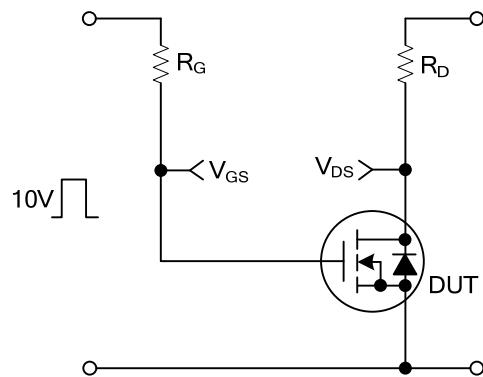


Peak Diode Recovery dv/dt Test Circuit

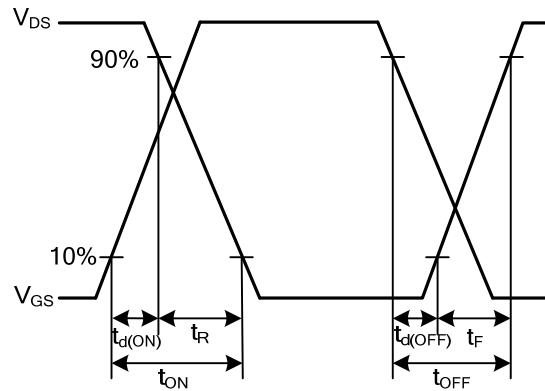


Peak Diode Recovery dv/dt Waveforms

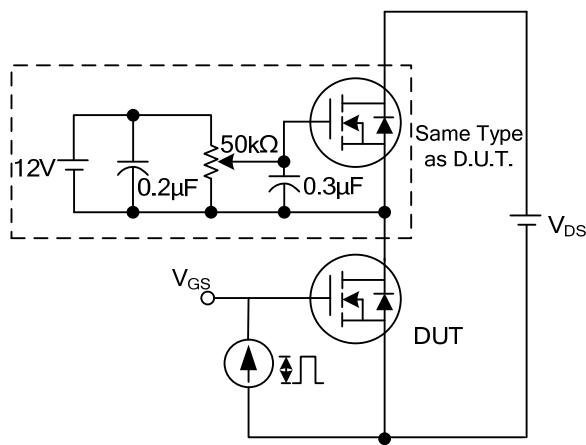
■ TEST CIRCUITS AND WAVEFORMS



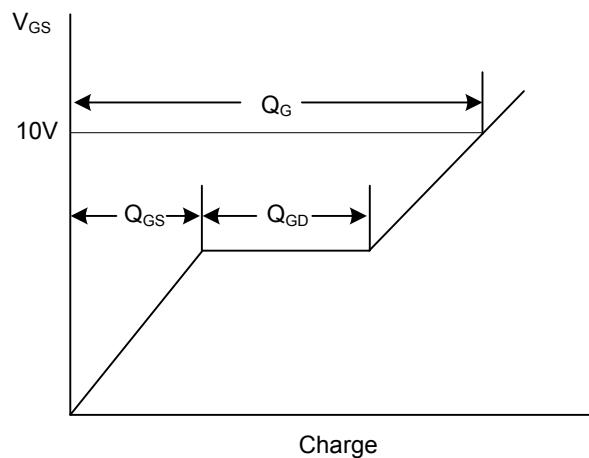
itching Test Circuit



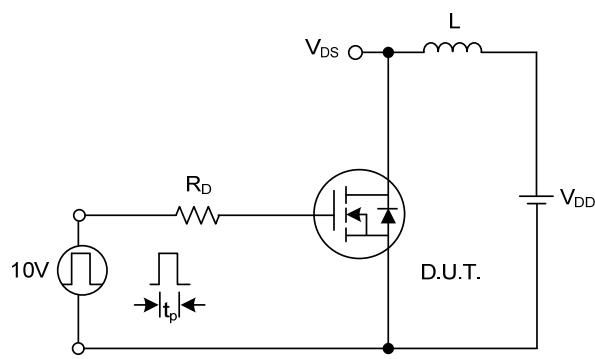
Switching Waveforms



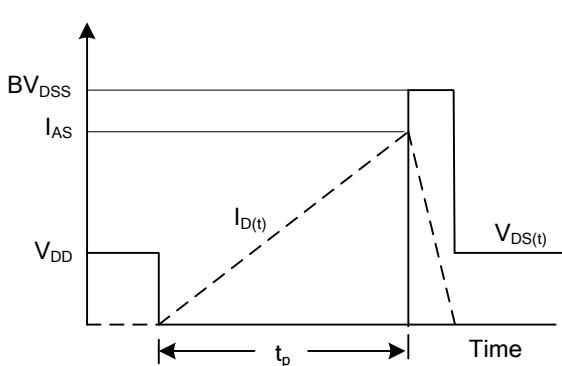
Gate Charge Test Circuit



Gate Charge Waveform

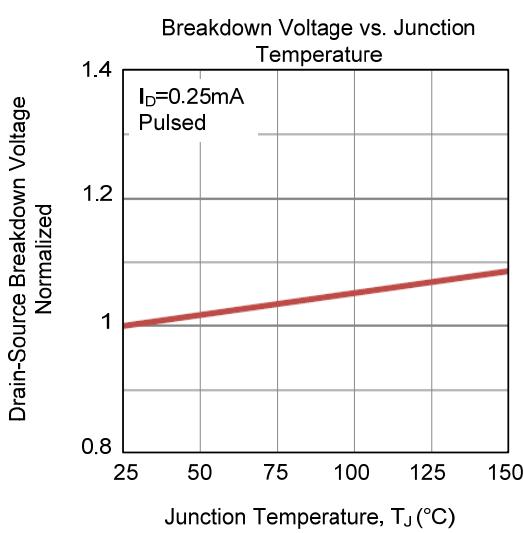
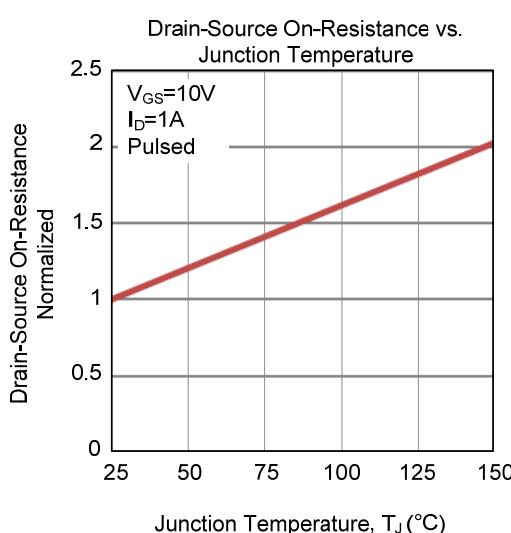
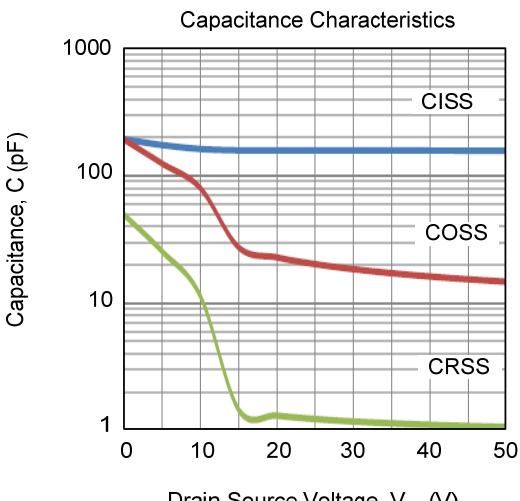
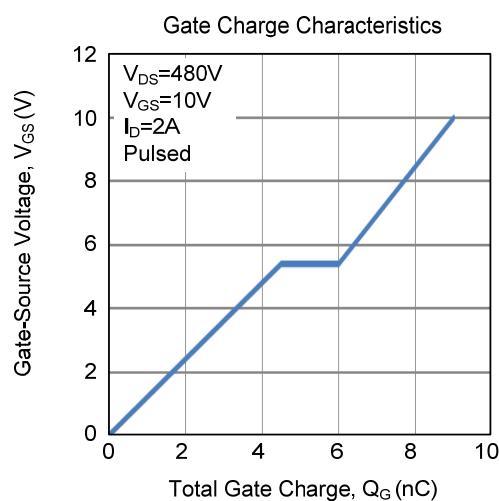
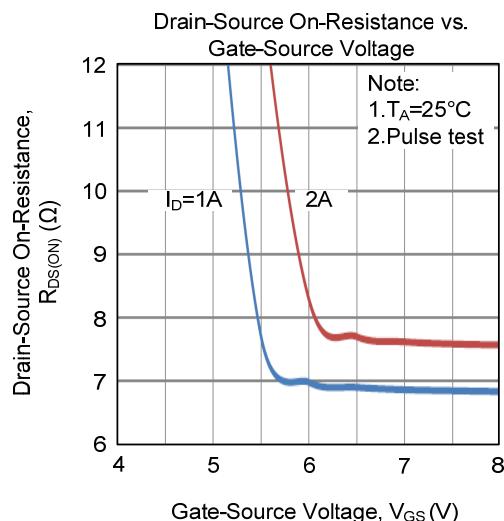
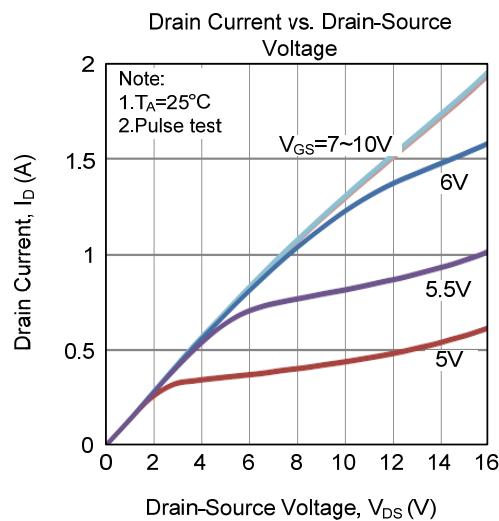


Unclamped Inductive Switching Test Circuit

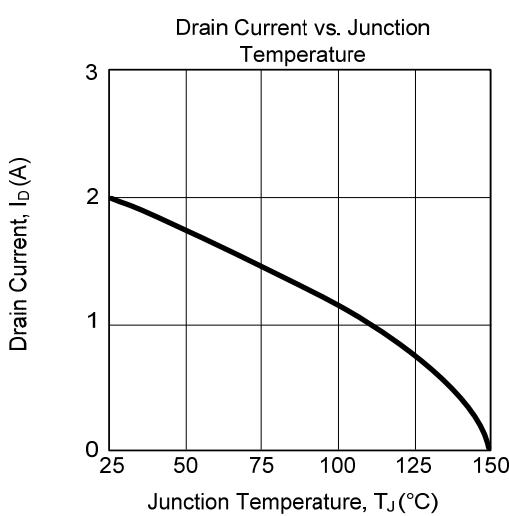
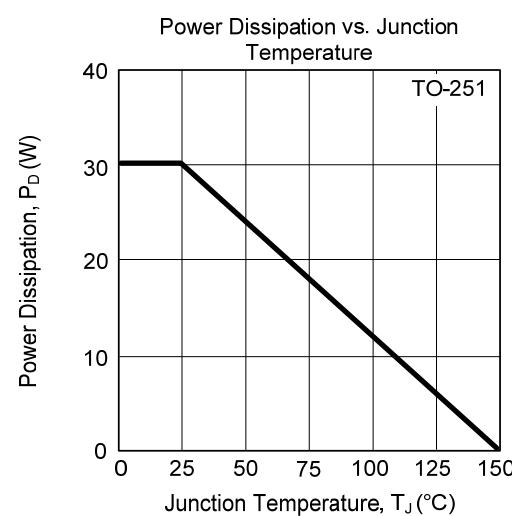
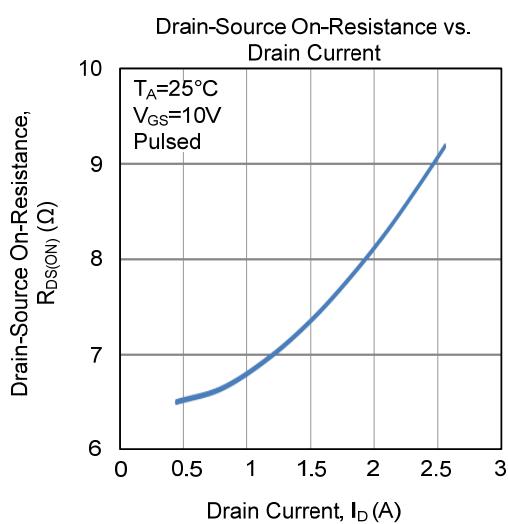
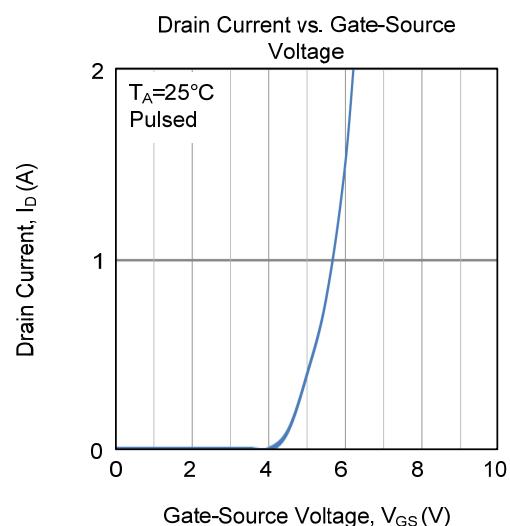
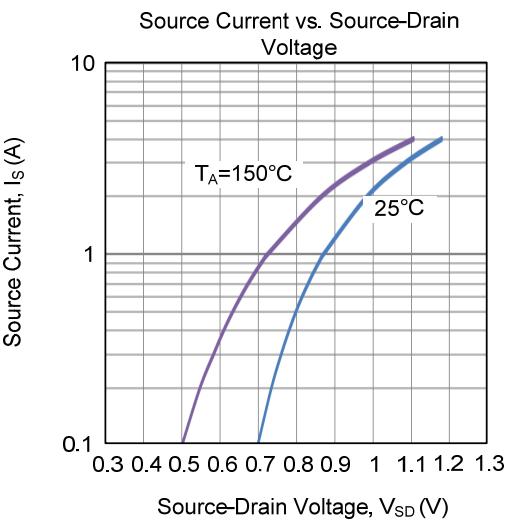
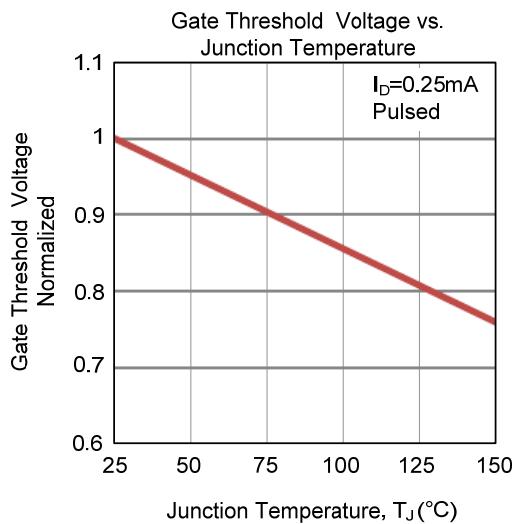


Unclamped Inductive Switching Waveforms

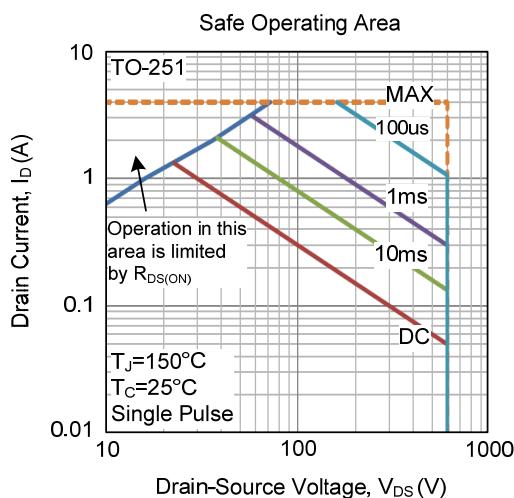
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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