

isc Silicon PNP Power Transistor
2N6132
DESCRIPTION

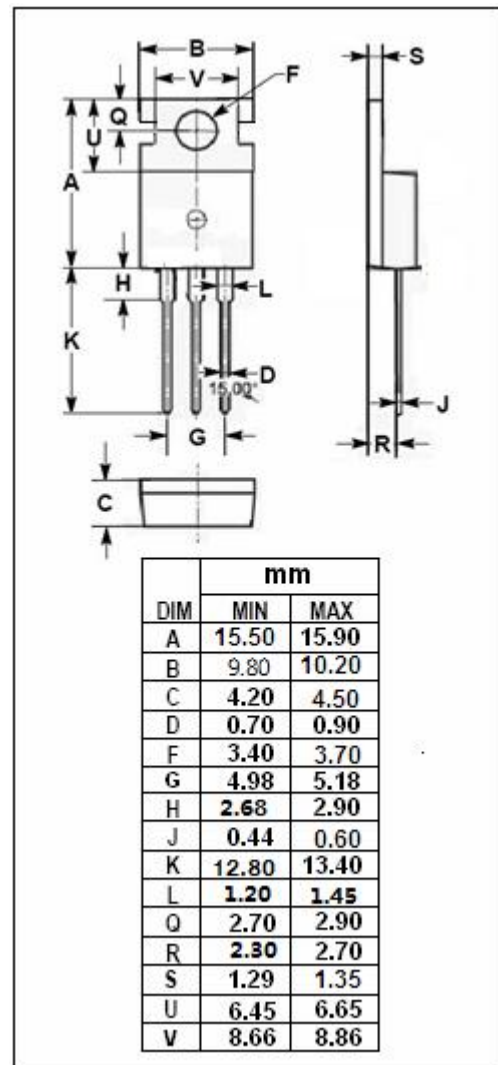
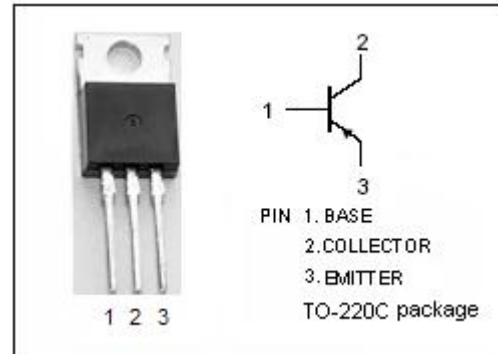
- DC Current Gain-
: $h_{FE} = 20-100 @ I_C = -2.5A$
- Collector-Emitter Sustaining Voltage-
: $V_{CEO(SUS)} = -40V(\text{Min})$
- Complement to Type 2N6129
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for use in general-purpose amplifier and switching applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-40	V
V_{CEO}	Collector-Emitter Voltage	-40	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-7	A
I_B	Base Current	-2	A
P_C	Collector Power Dissipation $T_C=25^\circ\text{C}$	50	W
T_j	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-65~150	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS

 $T_C=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CEQ(SUS)}$	Collector-Emitter Sustaining Voltage	$I_C = -30\text{mA}; I_B = 0$	-40		V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -7\text{A}; I_B = -1.4\text{A}$		-1.4	V
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C = -7\text{A}; V_{CE} = -4\text{V}$		-3.0	V
I_{CBO}	Collector Cutoff Current	$V_{CB} = -40\text{V}; I_E = 0$		-0.1	mA
I_{CEO}	Collector Cutoff Current	$V_{CE} = -40\text{V}; I_B = 0$		-1.0	mA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -5\text{V}; I_C = 0$		-1.0	mA
h_{FE-1}	DC Current Gain	$I_C = -2.5\text{A}; V_{CE} = -4\text{V}$	20	100	
h_{FE-2}	DC Current Gain	$I_C = -7\text{A}; V_{CE} = -4\text{V}$	5		
f_T	Current-Gain—Bandwidth Product	$I_C = -0.5\text{A}; V_{CE} = -4\text{V}$	2.5		MHz

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