



High-reliability discrete products  
and engineering services since 1977

2N3870-2N1850,  
2N3896-2N3899,  
2N6171-2N6174

## SILICON CONTROLLED RECTIFIER

### FEATURES

- Available as "HR" (high reliability) screened per MIL-PRF-19500, JANTX level. Add "HR" suffix to base part number.
- Available as non-RoHS (Sn/Pb plating), standard, and as RoHS by adding "-PBF" suffix.

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
<b>Peak repetitive forward or reverse blocking voltage</b> <sup>(1)</sup> (T <sub>J</sub> = -40 to 100°C, ½ sine wave, 50-400 Hz, gate open) 2N3870, 2N3896, 2N6171 2N3871, 2N3897, 2N6172 2N3872, 2N3898, 2N6173 2N3873, 2N3899, 2N6174	V <sub>RRM</sub> OR V <sub>DRM</sub>	100 200 400 600	Volts
<b>Peak non-repetitive forward or reverse blocking voltage</b> (t ≤ 5ms) 2N3870, 2N3896, 2N6171 2N3871, 2N3897, 2N6172 2N3872, 2N3898, 2N6173 2N3873, 2N3899, 2N6174	V <sub>RSM</sub> OR V <sub>DSM</sub>	150 330 660 700	Volts
<b>Average on-state current</b> <sup>(2)</sup> (T <sub>C</sub> = -40 to 65°C) (T <sub>C</sub> = 85°C)	I <sub>T(AV)</sub>	22 11	Amps
<b>Peak non-repetitive surge current</b> (one cycle, 60Hz) (T <sub>C</sub> = 65°C)	I <sub>TSM</sub>	350	Amps
<b>Circuit fusing</b> (T <sub>C</sub> = -40 to 100°C) (t = 1 to 8.3 ms)	I <sup>2</sup> t	510	A <sup>2</sup> s
<b>Peak gate power</b>	P <sub>GM</sub>	20	Watts
<b>Average gate power</b>	P <sub>G(AV)</sub>	0.5	Watt
<b>Peak forward gate current</b>	I <sub>GM</sub>	2	Amps
<b>Peak gate voltage</b>	V <sub>GM</sub>	10	Volts
<b>Operating junction temperature range</b>	T <sub>J</sub>	-40 to 100	°C
<b>Storage temperature range</b>	T <sub>stg</sub>	-40 to 150	°C
<b>Stud torque</b>	-	30	In. lb.
<b>Thermal resistance, junction to case</b> 2N3870 – 2N3873, 2N3896-2N3899 2N6171-2N6174	R <sub>θJC</sub>	0.9 1	°C/W

Note 1: Ratings apply for zero or negative gate voltage. Devices shall not have a positive bias applied to the gate concurrently with a negative potential on the anode. Devices should not be tested with a constant current source for forward or reverse blocking capability such that the voltage applied exceeds the rated blocking voltage.

Note 2: Isolated stud devices must be derated an additional 10 percent.



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### ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
<b>Peak forward or reverse blocking current</b> (Rated $V_{DRM}$ or $V_{RRM}$ , gate open, $T_J = 100^\circ\text{C}$ ) 2N3870, 2N3896, 2N6171 2N3871, 2N3897, 2N6172 2N3872, 2N3898, 2N6173 2N3873, 2N3899, 2N6174 Rated $V_{DRM}$ or $V_{RRM}$ , gate open, $T_J = 25^\circ\text{C}$ All devices	$I_{DRM}, I_{RRM}$	-	1	2.0	mA
<b>Peak on-state voltage</b> ( $I_{TM} = 69\text{A}$ peak)	$V_{TM}$	-	1.5	1.85	Volts
<b>Gate trigger current (continuous dc)</b> $V_D = 12\text{V}$ , $R_L = 24\text{ohms}$	$I_{GT}$	-	9	80	mA
		-	4	40	
<b>Gate trigger voltage (continuous dc)</b> $V_D = 12\text{V}$ , $R_L = 24\text{ohms}$	$V_{GT}$	-	0.9	3	Volts
		-	0.69	1.6	
<b>Holding current (gate open)</b> $V_D = 12\text{V}$ , $I_{TM} = 200\text{mA}$	$I_H$	-	14	90	mA
		-	5.2	50	
<b>Gate controlled turn-on time (<math>t_d+t_r</math>)</b> ( $I_{TM} = 41\text{Adc}$ , $V_D = \text{rated } V_{DRM}$ , $I_{GT} = 40\text{mAdc}$ , Rise time $\leq 0.05\mu\text{s}$ , pulse width = $10\mu\text{s}$ )	$t_{gt}$	-	-	1.5	$\mu\text{s}$
<b>Circuit commutated turn-off time</b> ( $I_{TM} = 10\text{A}$ , $I_R = 10\text{A}$ ) ( $I_{TM} = 10\text{A}$ , $I_R = 10\text{A}$ , $T_C = 100^\circ\text{C}$ )	$t_q$	-	25	-	$\mu\text{s}$
		-	35	-	
<b>Forward voltage application rate</b> ( $T_C = 100^\circ\text{C}$ , $V_D = \text{rated } V_{DRM}$ )	$dv/dt$	-	50	-	$\text{V}/\mu\text{s}$



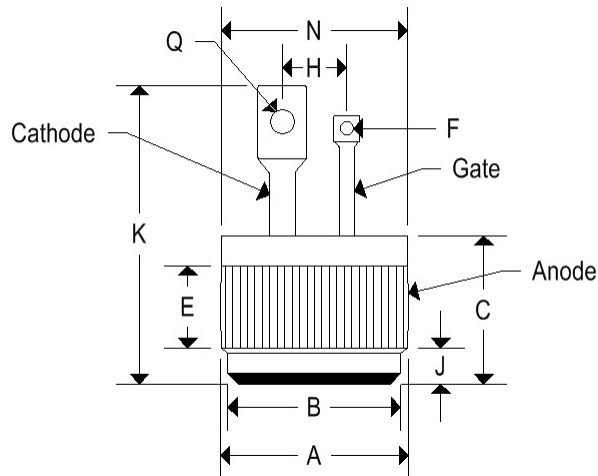
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**MECHANICAL CHARACTERISTICS**

2N3870-2N3873	
Case	DIGI PF2
Marking	Alpha-numeric
Pin out	See below



	DIGI PF2			
	Inches		Millimeters	
	Min	Max	Min	Max
A	0.501	0.505	12.730	12.830
B	0.465	0.475	11.810	12.060
C	0.330	0.380	8.390	9.650
E	0.100	-	2.540	-
F	0.035	0.085	0.890	2.160
H	0.148	0.174	3.750	4.410
J	0.080	0.097	2.040	2.460
K	-	0.800	-	20.320
N	-	0.510	-	12.950
Q	0.065	0.160	1.650	4.060



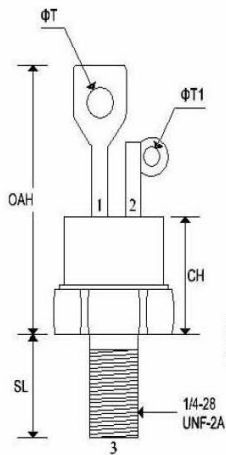
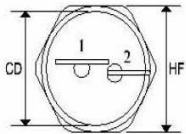
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**MECHANICAL CHARACTERISTICS**

2N3896-2N3899	
Case	TO-48
Marking	Alpha-numeric
Polarity	Cathode



Pin 1: Cathode  
Pin 2: Gate  
Pin 3: Anode

	TO-48			
	Inches		Millimeters	
	Min	Max	Min	Max
CD	-	0.543	-	13.793
CH	-	0.550	-	13.970
HF	0.544	0.563	13.817	14.301
OA	-	1.193	-	30.303
SL	0.422	0.453	10.718	11.507
ΦT	0.125	0.165	3.175	4.191
ΦT <sub>1</sub>	0.060	0.075	1.524	1.905



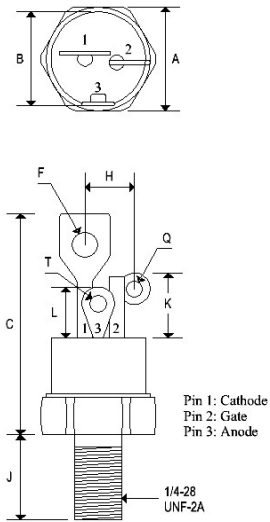
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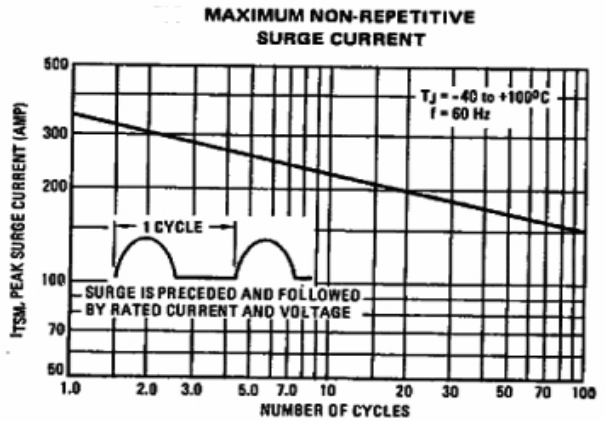
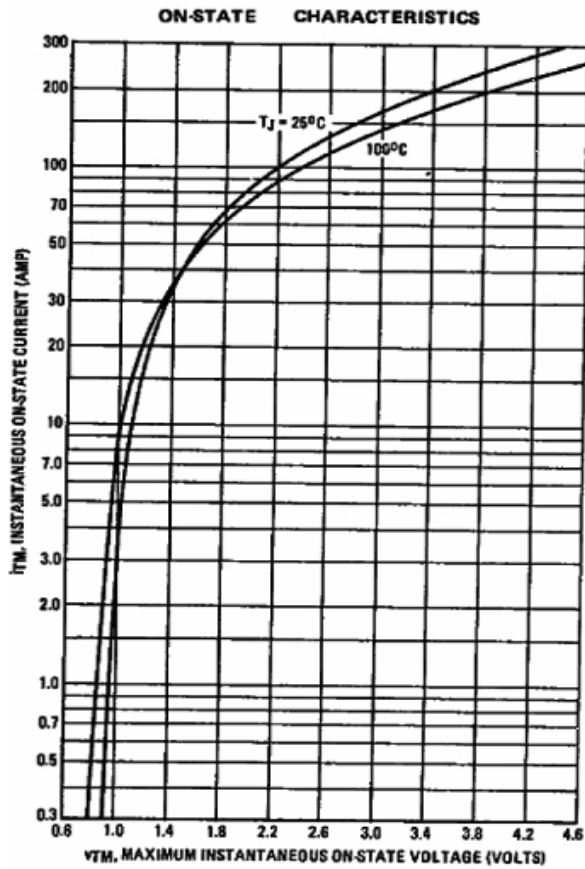
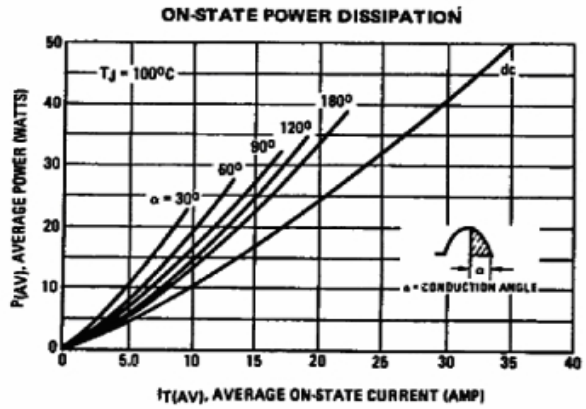
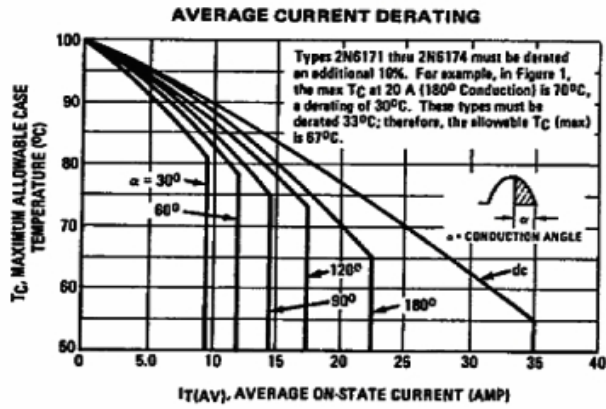
SILICON CONTROLLED RECTIFIER

**MECHANICAL CHARACTERISTICS**

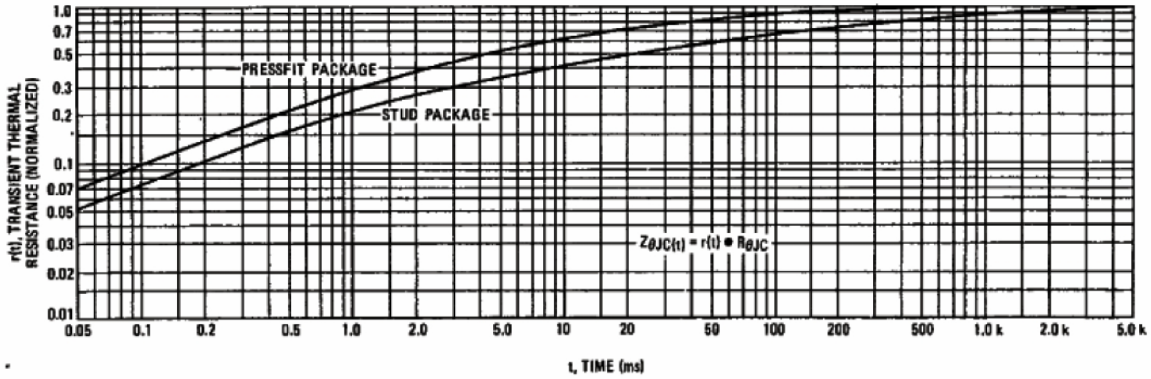
2N6171-2N6174	
Case	TO-48
Marking	Alpha-numeric
Polarity	Cathode



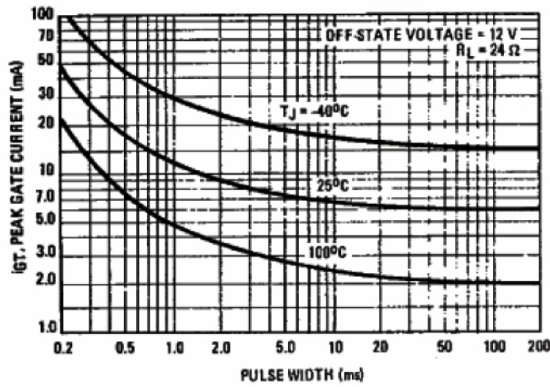
	TO-48 ISO			
	Inches		Millimeters	
	Min	Max	Min	Max
A	0.551	0.559	14.000	14.200
B	0.501	0.505	12.730	12.830
C	-	1.280	-	32.510
F	-	0.160	-	4.060
H	-	0.265	-	6.730
J	0.420	0.455	10.670	11.560
K	0.300	0.350	7.620	8.890
L	0.255	0.275	6.480	6.990
Q	0.055	0.085	1.400	2.160
T	0.135	0.150	3.430	3.810



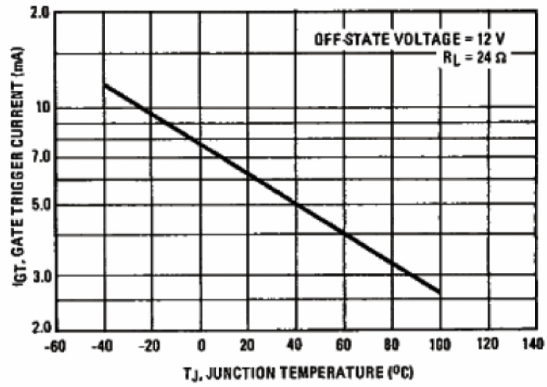
TYPICAL THERMAL RESPONSE



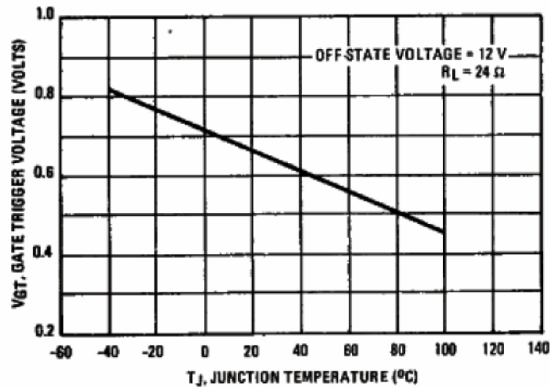
PULSE TRIGGER CURRENT



GATE TRIGGER CURRENT



GATE TRIGGER VOLTAGE



HOLDING CURRENT

