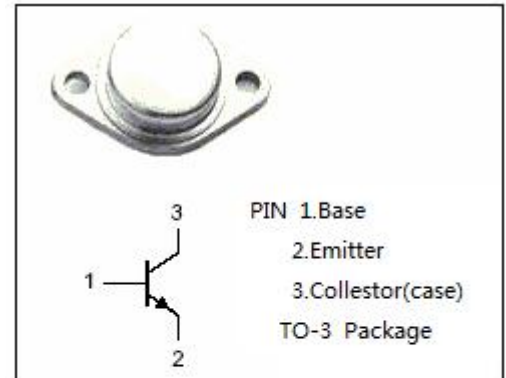


isc Silicon NPN Power Transistor
2N6217
DESCRIPTION

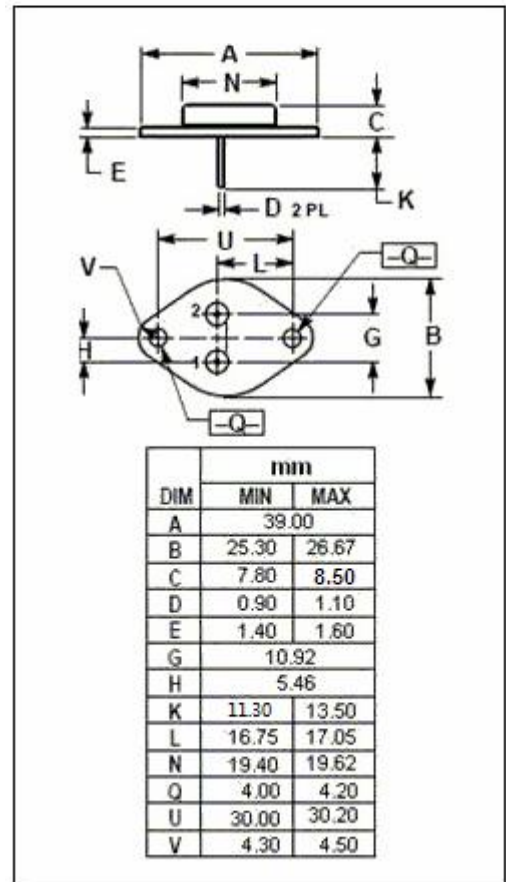
- With TO-3 package
- Excellent Safe Operating Area
- Low Collector-Emitter Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

APPLICATIONS

- Designed for linear power and switching amplifier applications.


ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	180	V
V _{CEO}	Collector-Emitter Voltage	140	V
V _{EBO}	Emitter-Base Voltage	7	V
I _C	Collector Current-Continuous	10	A
P _C	Collector Power Dissipation@T _C =100°C	71	W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-65~200	°C


THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case	1.46	°C/W

isc Silicon NPN Power Transistor

2N6217

ELECTRICAL CHARACTERISTICS

T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C =30mA; I _B = 0	140			V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = 4A; I _B = 0.4A			1.2	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = 6A; I _B = 0.75A			1.6	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C =6A; I _B = 0.75A			2.0	V
I _{EBO}	Emitter Cutoff Current	V _{EB} = 7V; I _C = 0			0.1	mA
I _{CEO}	Collector Cutoff Current	V _{CE} = 140V; I _B =0			0.5	mA
I _{CBO}	Collector Base Cutoff Current	V _{CB} =180V; I _E = 0			1.0	mA
h _{FE}	DC Current Gain	I _C = 5A; V _{CE} = 5V	20		80	
f _T	Current Gain-Bandwidth Product	I _C = 1A; V _{CE} = 10V; f= 1.0MHz		20		MHz

NOTICE:

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