

isc Silicon NPN Power Transistor
2N6274
DESCRIPTION

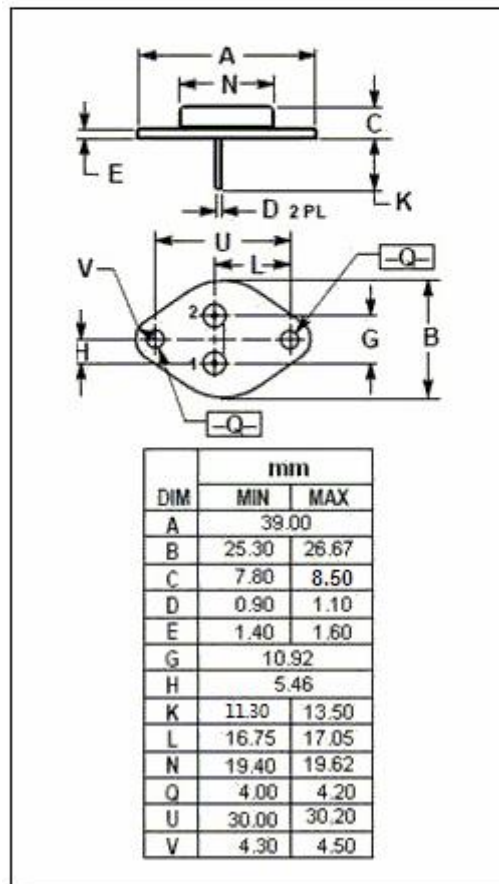
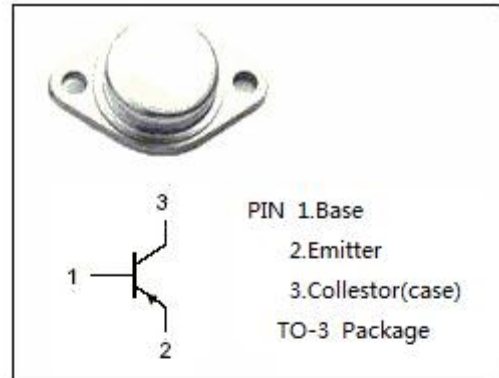
- Collector-Emitter Breakdown Voltage-
: $V_{CE0}=100V(\text{Min})$
- Minimum Lot-to-Lot variations for robust device
Performance and reliable operation

APPLICATIONS

- Power amplifier and switching applications

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	120	V
V_{CEO}	Collector-Emitter Voltage	100	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	50	A
P_D	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	250	W
T_J	Junction Temperature	-65~200	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-65~200	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS

T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C =50mA	100			V
I _{CEO}	Collector-Base Cutoff Current	V _{CB} = 50V			50	uA
I _{EBO}	Emitter-Base Cutoff Current	V _{EB} = 6V			0.1	mA
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = 20A; I _B = 2A			1	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = 50A; I _B = 10A			3	V
V _{BE(sat)-1}	Base-Emitter Saturation Voltage	I _C = 20A; I _B = 2A			1.8	V
V _{BE(sat)-2}	Base-Emitter Saturation Voltage	I _C = 50A; I _B = 10A			3.5	V
H _{fe-1}	DC Current Gain	I _C =1A; V _{CE} = 4V	50			
h _{FE-2}	DC Current Gain	I _C =20A; V _{CE} = 4V	30		120	
h _{FE-3}	DC Current Gain	I _C =50A; V _{CE} = 4V	10			

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