# 2N6274, 2N6277



## **NPN High Power Silicon Transistor**

Rev. V3

### **Features**

- Available in JAN, JANTX, JANTXV per MIL-PRF-19500/514
- TO-3 (TO-204AA) Package
- Designed for Use in Hi-Reliability Power Amplifier and Switching Circuit Applications



## Electrical Characteristics (T<sub>C</sub> = +25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Units	Min.	Max.	
Collector - Emitter Breakdown Voltage	I <sub>C</sub> = 50 mA dc, 2N6274 I <sub>C</sub> = 50 mA dc, 2N6277	V <sub>(BR)CEO</sub>	V dc	100 150	_	
Collector - Base Cutoff Current	V <sub>CB</sub> = 120 V dc, 2N6274 V <sub>CB</sub> = 180 V dc, 2N6277	I <sub>CBO</sub>	μA dc	_	10 10	
Emitter - Base Cutoff Current	V <sub>EB</sub> = 6.0 V dc	I <sub>EBO</sub>	μA dc	_	100	
Collector - Emitter Cutoff Current	V <sub>CE</sub> = 120 V dc, V <sub>BE</sub> = -1.5 V dc, 2N6274 V <sub>CE</sub> = 180 V dc, V <sub>BE</sub> = -1.5 V dc, 2N6277	I <sub>CEX1</sub>	μA dc	_	10 10	
Collector - Emitter Cutoff Current	$V_{CE}$ = 50 V dc, 2N6274 $V_{CE}$ = 75 V dc, 2N6277	I <sub>CEO</sub>	μA dc	_	50 50	
		I				
Forward Current Transfer Ratio	$V_{CE}$ = 4.0 V dc; $I_{C}$ = 1.0 A dc $V_{CE}$ = 4.0 V dc; $I_{C}$ = 20 A dc $V_{CE}$ = 4.0 V dc; $I_{C}$ = 50 A dc	h <sub>FE</sub>	-	50 30 10	120	
Collector - Emitter Saturation Voltage	$I_{C}$ = 20 A dc; $I_{B}$ = 2.0 A dc $I_{C}$ = 50 A dc; $I_{B}$ = 10 A dc	V <sub>CE(sat)1</sub>	V dc	_	1.0 3.0	
Emitter - Base Saturation Voltage	I <sub>C</sub> = 20 A dc: I <sub>B</sub> = 2.0 A dc	V <sub>BE(sat)</sub>	V dc	_	1.8	
Collector - Emitter Cutoff Current	T <sub>A</sub> = +150°C V <sub>CE</sub> = 120 V dc, V <sub>BE</sub> = -1.5 V dc, 2N6274 V <sub>CE</sub> = 180 V dc, V <sub>BE</sub> = -1.5 V dc, 2N6277	I <sub>CEX2</sub>	mA dc	_	10 10	
Forward Current Transfer Ratio	$T_A = -55^{\circ}C$ $V_{CE} = 4.0 \text{ V dc}; I_C = 20 \text{ A dc}$	h <sub>FE4</sub>	-	10		
Dynamic Characteristics						
Magnitude of Common Emitter Small-Signal Short-Circuit Forward Current Transfer Ratio	V <sub>CE</sub> = 10 V dc; I <sub>C</sub> = 1.0 A dc; f = 10 MHz	h <sub>fe</sub>		3.0	12	
Output Capacitance	V <sub>CB</sub> = 10 V dc; I <sub>E</sub> = 0; f = 1.0 MHz	C <sub>obo</sub>	pF	_	600	



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## Absolute Maximum Ratings (T<sub>C</sub> = +25°C unless otherwise specified)

Ratings	Symbol	Value
Collector - Emitter Voltage 2N6274 2N6277	V <sub>CEO</sub>	100 V dc 150 V dc
Collector - Base Voltage 2N6274 2N6277	V <sub>CBO</sub>	120 V dc 180 V dc
Emitter - Base Voltage	$V_{EBO}$	6 V dc
Base Current	I <sub>B</sub>	20 A dc
Collector Current	I <sub>C</sub>	50 A dc
Total Power Dissipation  @ $T_C = +25^{\circ}C^{(1)}$ @ $T_C = +100^{\circ}C^{(1)}$	P <sub>T</sub>	250 W 143 W
Operating & Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65°C to +200°C

(1) Derate linearly @ 1.43 W /  $^{\circ}$ C between T<sub>C</sub> = +25  $^{\circ}$ C and T<sub>C</sub> = +200  $^{\circ}$ C

### **Thermal Characteristics**

Characteristics	Symbol	Max. Value
Thermal Resistance, Junction to Case	$R_{ heta JC}$	0.7°C/W

Switching Characteristics	Symbol	Max. Value
$V_{CC}$ = 80 V dc; $I_C$ = 20 A dc; $I_{B1}$ = 2.0 A dc	t <sub>on</sub>	0.5 µs
$V_{CC}$ = 80 V dc; $I_C$ = 20 A dc; $I_{B1}$ = $I_{B2}$ = 2.0 A dc	t <sub>off</sub>	1.05 µs

### Safe Operating Area

DC Tests:  $T_C = +25$  °C; 1 cycle; t = 1.0 s

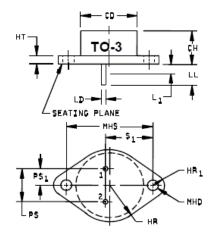
Test 1:  $V_{CE} = 5 \text{ V dc}; I_{C} = 50 \text{ A dc}$ Test 2:  $V_{CE} = 86 \text{ V dc}; I_{C} = 165 \text{ mA dc}$ 

Test 3:  $V_{CE} = 8 \text{ V dc}; I_{C} = 29 \text{ A dc}, 2N6274$ Test 4:  $V_{CE} = 120 \text{ V dc}; I_{C} = 110 \text{ mA dc} 2N6277$ 



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	Dimensions				
Ltr	Inches		Millimeters		Notes
	Min	Max	Min	Max	
CD		.875		22.22	3
CH	.250	.328	6.35	8.33	
HR	.495	.525	12.57	13.34	
HR <sub>1</sub>	.131	.188	3.33	4.78	6
HT	.060	.135	1.52	3.43	
LD	.057	.063	1.45	1.60	5, 9
LL	.312	.500	7.92	12.70	4, 5, 9
L <sub>1</sub>		.050		1.27	5, 9
MHD	.151	.161	3.84	4.09	7
MHS	1.177	1.197	29.90	30.40	
PS	.420	.440	10.67	11.18	
PS <sub>1</sub>	.205	.225	5.21	5.72	5
S <sub>1</sub>	.655	.675	16.64	17.15	

#### NOTES:

- 1. Dimensions are in inches.
- Millimeters are given for general information only.
- Body contour is optional within zone defined by CD.
- These dimensions shall be measured at points .050 inch (1.27 mm) to .055 inch (1.40 mm) below seating plane. When gauge is not used, measurement shall be made at seating plane.
- Both terminals.
- At both ends.
- Two holes.
- 8. Terminal 1 is the emitter, terminal 2 is base. The collector shall be electrically connected to the case.
- 9. LD applies between L1 and LL. Lead diameter shall not exceed twice LD within L1.
- In accordance with ASME Y14.5M, diameters are equivalent to \$\psi\$x symbology.
- The seating plane of the header shall be flat within .001 inch (0.03 mm) concave to .004 inch (0.10 mm) convex inside a .930 inch (23.62 mm) diameter circle on the center of the header and flat within .001 inch (0.03 mm) concave to .006 inch (0.15 mm) convex overall.

FIGURE 1. Physical dimensions (TO-3).

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