

# **isc Silicon PNP Power Transistor**

### **DESCRIPTION**

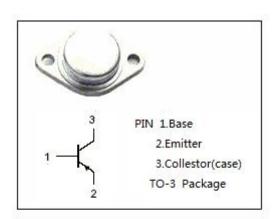
- · Collector-Emitter Breakdown Voltage-
  - : V<sub>CEO</sub>=-80V(Min)
- Minimum Lot-to-Lot variations for robust device Performance and reliable operation

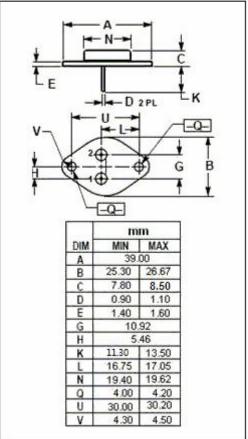
### **APPLICATIONS**

Power amplifier and switching applications

## ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNI T
V <sub>CBO</sub>	Collector-Base Voltage	-100	V
Vceo	Collector-Emitter Voltage	-80	V
V <sub>EBO</sub>	Emitter-Base Voltage	-6	V
Ic	Collector Current-Continuous	-50	Α
P <sub>D</sub>	Collector Power Dissipation @ $T_C$ =25 $^{\circ}$ C	250	W
TJ	Junction Temperature	-65~200	$^{\circ}$
T <sub>stg</sub>	Storage Temperature Range	-65~200	$^{\circ}$







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2N6377

#### **ELECTRICAL CHARACTERISTICS**

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V(BR)CEO	Collector-Emitter Breakdown Voltage	Ic=-50mA	-80			V
I <sub>EBO</sub>	Emitter-Base Cutoff Current	V <sub>BE</sub> =-6V			-100	uA
I <sub>CEO</sub>	Collector-Emitter Cutoff Current	V <sub>CE</sub> = -50V			-0.05	mA
V <sub>CE</sub> (sat)-1	Collector-Emitter Saturation Voltage	Ic=-20A; I <sub>B</sub> =- 2A			-1.2	V
V <sub>CE(sat)-2</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> =-50A; I <sub>B</sub> = -10A			-3	٧
V <sub>BE</sub> (sat)-1	Base-Emitter Saturation Voltage	I <sub>C</sub> =-20A; I <sub>B</sub> =- 2A			-1.8	V
V <sub>BE(sat)-2</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> =-50A; I <sub>B</sub> = -10A			-3.5	V
h <sub>FE-1</sub>	DC Current Gain	Ic=-1A; Vc== -4V	50			
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> =-20A; V <sub>CE</sub> = -4V	30		120	
h <sub>FE-3</sub>	DC Current Gain	Ic=-50A; Vc==-4V	10			

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