

2N6428A **NPN Epitaxial Silicon Transistor**

Features

· This device is designed for high gain, general purpose amplifier applications at collector currents from 1uA to 200 mA.



December 2006

1. Emitter 2. Base 3. Collector

Absolute Maximum Ratings * T_a = 25°C unless otherwise noted

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	60	V
V _{CEO}	Collector-Emitter Voltage	50	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current - Continuous	200	mA
P _D	Total Device Dissipation	625	mW
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	- 55 ~ 150	°C

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

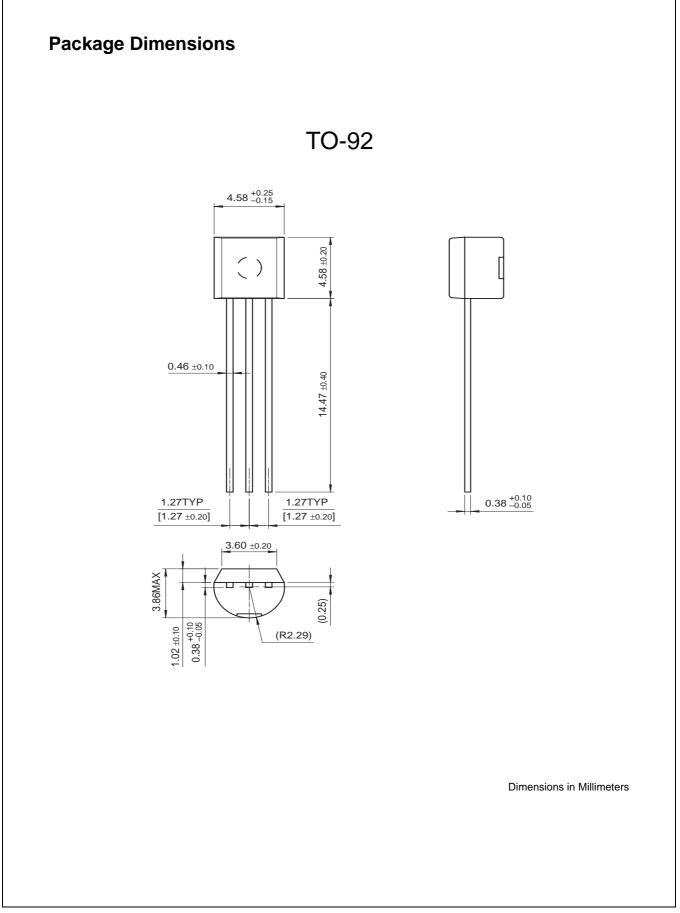
NOTES:

These ratings are based on a maximum junction temperature of 150 degrees C.
These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

Electrical Characteristics* T_C = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Max.	Units	
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{\rm C} = 100 \mu {\rm A}, \ I_{\rm E} = 0$	60		V	
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 1 {\rm mA}, I_{\rm B} = 0$	50		V	
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_{E} = 100 \mu A, I_{C} = 0$	5		V	
I _{CBO}	Collector Cut-off Current	$V_{CB} = 30V, I_E = 0$		10	nA	
I _{EBO}	Emitter Cut-off Current	$V_{BE} = 5V, I_{C} = 0$		10	nA	
h _{FE}	DC Current Gain	$V_{CE} = 5V, I_C = 0.01mA$ $V_{CE} = 5V, I_C = 0.1mA$ $V_{CE} = 5V, I_C = 1.0mA$ $V_{CE} = 5V, I_C = 10mA$	250 250 250 250	650 650		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_{C} = 10$ mA, $I_{B} = 0.5$ mA $I_{C} = 100$ mA, $I_{B} = 5.0$ mA	0.2 0.6		V V	
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = 5V, I_{C} = 1.0mA$	0.56	0.66	V	
f _T	Current Gain Bandwidth Product	I _C = 1mA, V _{CE} = 5.0V, f = 100MHz	100	700	MHz	
C _{ob}	Output Capacitance	V _{CB} = 10V, I _E = 0, f = 1MHz		3	pF	

* DC Item are tested by Pulse Test: Pulse Width≤300us, Duty Cycle≤2%





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