

2N6432

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CASE 22, STYLE 1
TO-18 (TO-206AA)

GENERAL PURPOSE
TRANSISTOR

PNP SILICON

MAXIMUM RATINGS

Rating	Symbol	2N6432	2N6433	Unit
Collector-Emitter Voltage	V_{CEO}	200	300	Vdc
Collector-Base Voltage	V_{CBO}	200	300	Vdc
Emitter-Base Voltage	V_{EBO}		5.0	Vdc
Collector Current — Continuous	I_C		500	mA
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D		500 2.86	mW mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D		1.8 10.3	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	–65 to +200		$^\circ\text{C}$

Refer to 2N3743 for graphs.

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage(1) ($I_C = 1.0 \text{ mA}_{dc}, I_B = 0$)	2N6432 2N6433	$V_{(BR)CEO}$	200 300	— —	Vdc
Collector-Base Breakdown Voltage ($I_C = 0.1 \text{ mA}_{dc}, I_E = 0$)	2N6432 2N6433	$V_{(BR)CBO}$	200 300	— —	Vdc
Emitter-Base Breakdown Voltage ($I_E = 0.1 \text{ mA}_{dc}, I_C = 0$)		$V_{(BR)EBO}$	5.0	—	Vdc
Collector Cutoff Current ($V_{CB} = 160 \text{ Vdc}$) ($V_{CB} = 200 \text{ Vdc}$)	2N6432 2N6433	I_{CBO}	— —	0.25 0.25	μA_{dc}
Emitter Cutoff Current ($V_{EB} = 3.0 \text{ Vdc}, I_C = 0$)		I_{EBO}	—	0.1	μA_{dc}

ON CHARACTERISTICS

DC Current Gain ($I_C = 1.0 \text{ mA}_{dc}, V_{CE} = 10 \text{ Vdc}$) ($I_C = 10 \text{ mA}_{dc}, V_{CE} = 10 \text{ Vdc}$) ($I_C = 30 \text{ mA}_{dc}, V_{CE} = 10 \text{ Vdc}$)	h_{FE}	25 40 30	— — 150	—
Collector-Emitter Saturation Voltage ($I_C = 20 \text{ mA}_{dc}, I_B = 2.0 \text{ mA}_{dc}$)	$V_{CE(sat)}$	—	0.5	Vdc
Base-Emitter Saturation Voltage ($I_C = 20 \text{ mA}_{dc}, I_B = 2.0 \text{ mA}_{dc}$)	$V_{BE(sat)}$	—	0.9	Vdc

SMALL-SIGNAL CHARACTERISTICS

Current-Gain — Bandwidth Product ($I_C = 10 \text{ mA}_{dc}, V_{CE} = 20 \text{ Vdc}, f = 20 \text{ MHz}$)	f_T	50	500	MHz
Collector-Base Capacitance ($V_{CB} = 20 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$)	C_{cb}	—	6.0	pF

(1) Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.