
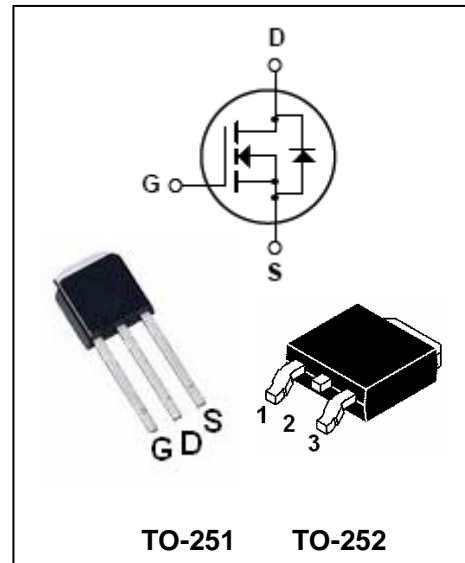


## 2A, 650 Volts N-CHANNEL POWER MOSFET

**2N65**

### FEATURES

- $R_{DS(on)}=5.0\Omega@V_{GS}=10V$ .
- Ultra Low gate charge (typical 9.0nC)  Lead-free
- Low reverse transfer capacitance ( $C_{rss} = \text{typical } 5.0 \text{ pF}$ )
- Fast switching capability
- Avalanche energy specified
- Improved dv/dt capability, high ruggedness



### MAXIMUM RATING operating temperature range applies unless otherwise specified

Symbol	Parameter	Value	Units
$V_{DSS}$	Drain-Source voltage	650	V
$I_D$	Drain current continuous	2.0	A
$I_{DM}$	Drain current Pulsed (Note2)	8.0	A
$V_{GSS}$	Gate -Source voltage	$\pm 30$	V
$I_{AR}$	Avalanche Current (Note2)	2.0	A
$E_{AR}$	Avalanche Energy Repetitive(Note 2)	4.5	mJ
$E_{AS}$	Avalanche Energy Single Pulse(Note 3)	140	mJ
dv/dt	Peak Diode Recovery dv/dt (Note4)	4.5	V/ns
$P_D$	Power Dissipation	35	W
$T_J$	Junction Temperature	+150	$^{\circ}\text{C}$
$T_{OPR} T_{STG}$	Operating and Storage Temperature	-55 ~ +150	$^{\circ}\text{C}$
$\theta_{JA}$	Thermal Resistance Junction-Ambient	50	$^{\circ}\text{C/W}$
$\theta_{Jc}$	Thermal Resistance Junction-Case	2.87	$^{\circ}\text{C/W}$

Note:1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.



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2. Repetitive Rating: Pulse width limited by maximum junction temperature
3.  $L=64\text{mH}$ ,  $I_{AS}=2.0\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\ \Omega$ , Starting  $T_J = 25^\circ\text{C}$
4.  $I_{SD} \leq 2.4\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

### ELECTRICAL CHARACTERISTICS@ $T_a=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	650	-	-	V
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	$I_D=250\mu\text{A}$	-	0.4	-	$\text{V}/^\circ\text{C}$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=650\text{V}, V_{GS}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-body Leakage	$I_{GSS}$	Forward $V_{DS}=0\text{V}, V_{GS}=30\text{V}$	-	-	100	nA
Reverse		$V_{DS}=0\text{V}, V_{GS}=-30\text{V}$	-	-	-100	
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.0	-	4.0	
Static drain-Source on-resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}, I_D=1\text{A}$ ,	-	4.2	5.0	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Forward transconductance	$g_{FS}$	$V_{DS}=50\text{V}, I_D=1\text{A}$ (Note1)	-	2.25	-	S
Input Capacitance	$C_{iss}$	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1.0\text{MHz}$	-	270	350	pF
Output Capacitance	$C_{oss}$		-	40	50	pF
Reverse Transfer Capacitance	$C_{riss}$		-	5	7	pF
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=325\text{V}, I_D=2.4\text{A}, R_G=25\Omega$ (Note1,2)	-	10	30	ns
Turn-On Rise Time	$t_R$		-	25	60	ns
Turn-Off Delay Time	$t_{d(off)}$		-	20	50	ns
Turn-Off Fall Time	$t_f$		-	25	60	ns
Total Gate Charge	$Q_g$	$V_{DS}=520\text{V}, I_D=2.4\text{A}, V_{GS}=10\text{V}$ (Note1,2)	-	9.0	11	nC
Gate-Source Charge	$Q_{gs}$		-	1.6	-	nC
Gate-Drain Charge	$Q_{gd}$		-	4.3	-	nC
<b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_{SD}$		-	-	2.0	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$		-	-	8.0	A
Drain-Source Recovery Charge	$V_{SD}$	$V_{GS}=0\text{V}, I_{SD}=2.0\text{A}$	-	-	1.5	V
Reverse Recovery Time	$t_{rr}$	$V_{GS}=0\text{V}, I_{SD}=2.4\text{A}$ ,	-	180	-	ns
Reverse Recovery Charge	$Q_{rr}$	$di_F/dt=100\text{A}/\mu\text{s}$ (Note1)	-	0.72	-	$\mu\text{C}$

Notes:

1. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$

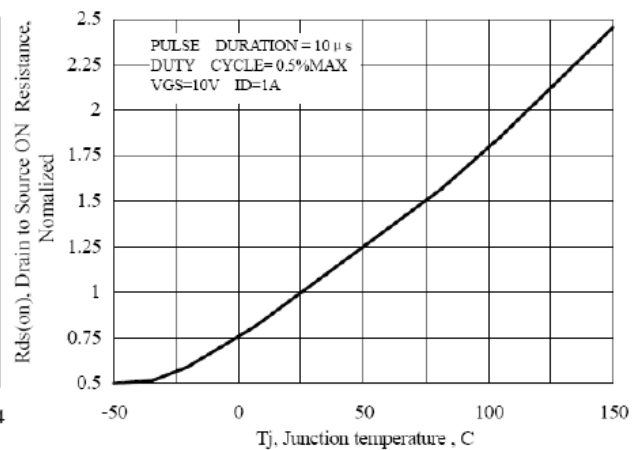
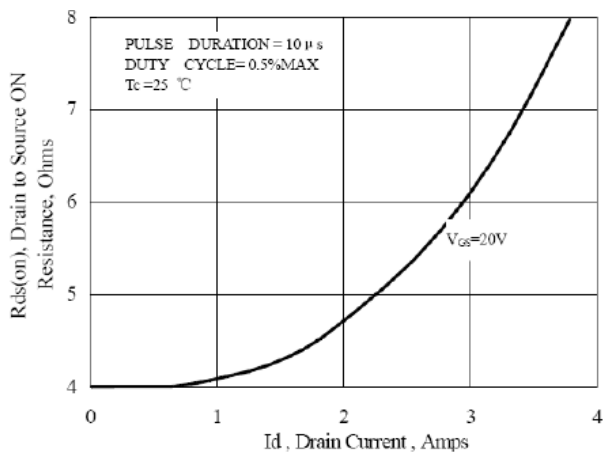
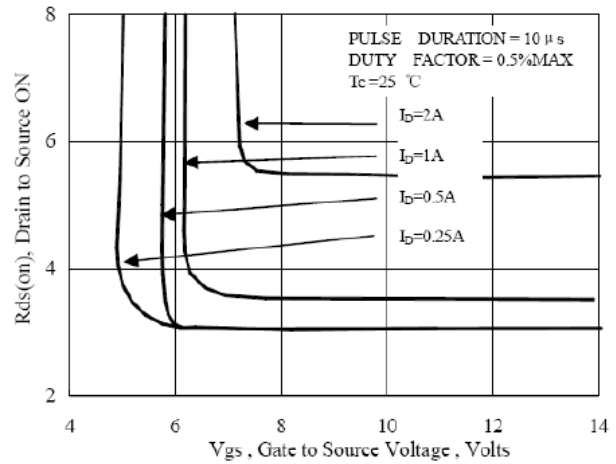
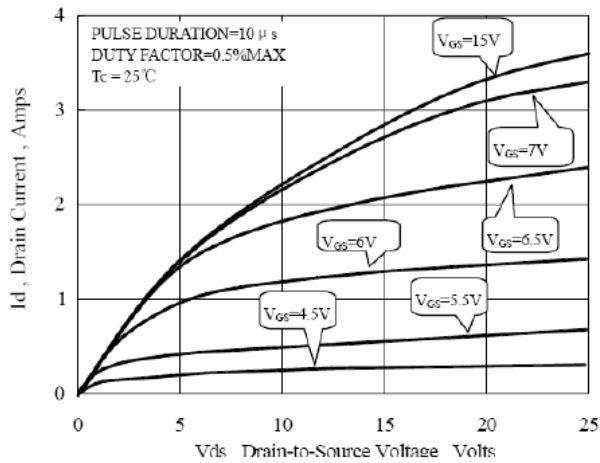


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## 2N65

### 2. Essentially Independent of Operating Temperature

### TYPICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified





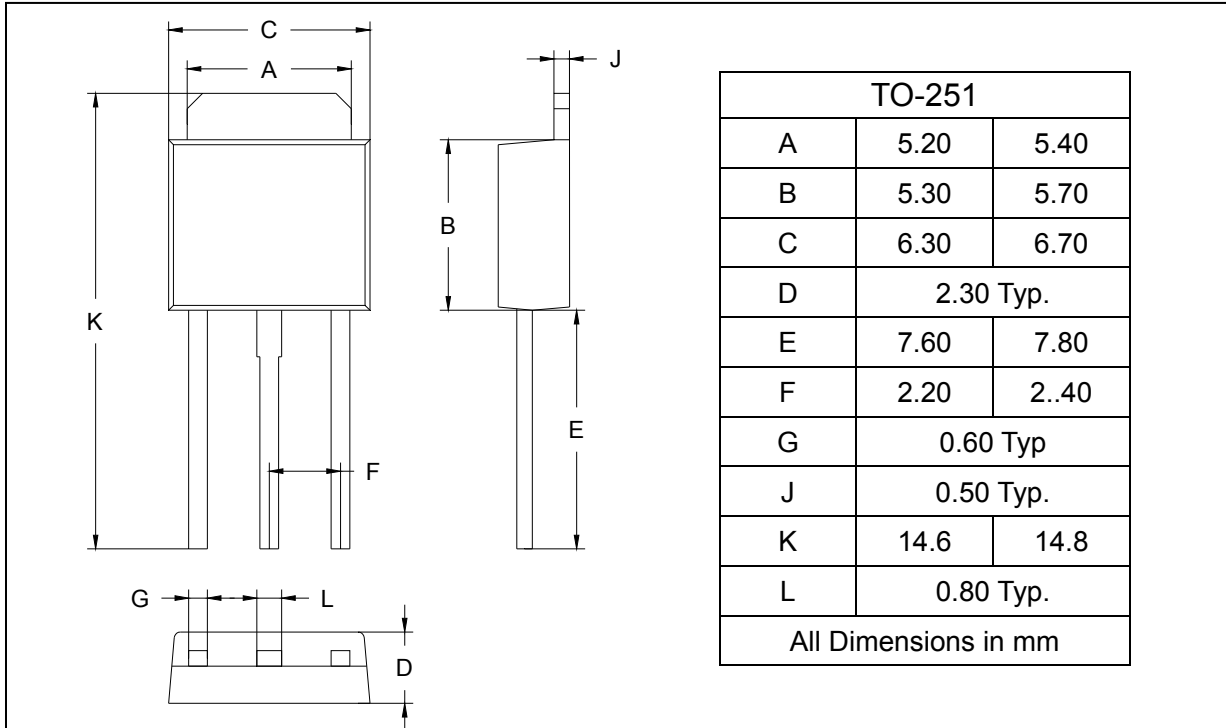
**2A, 650 Volts N-CHANNEL POWER MOSFET**

**2N65**

**PACKAGE OUTLINE**

Plastic surface mounted package

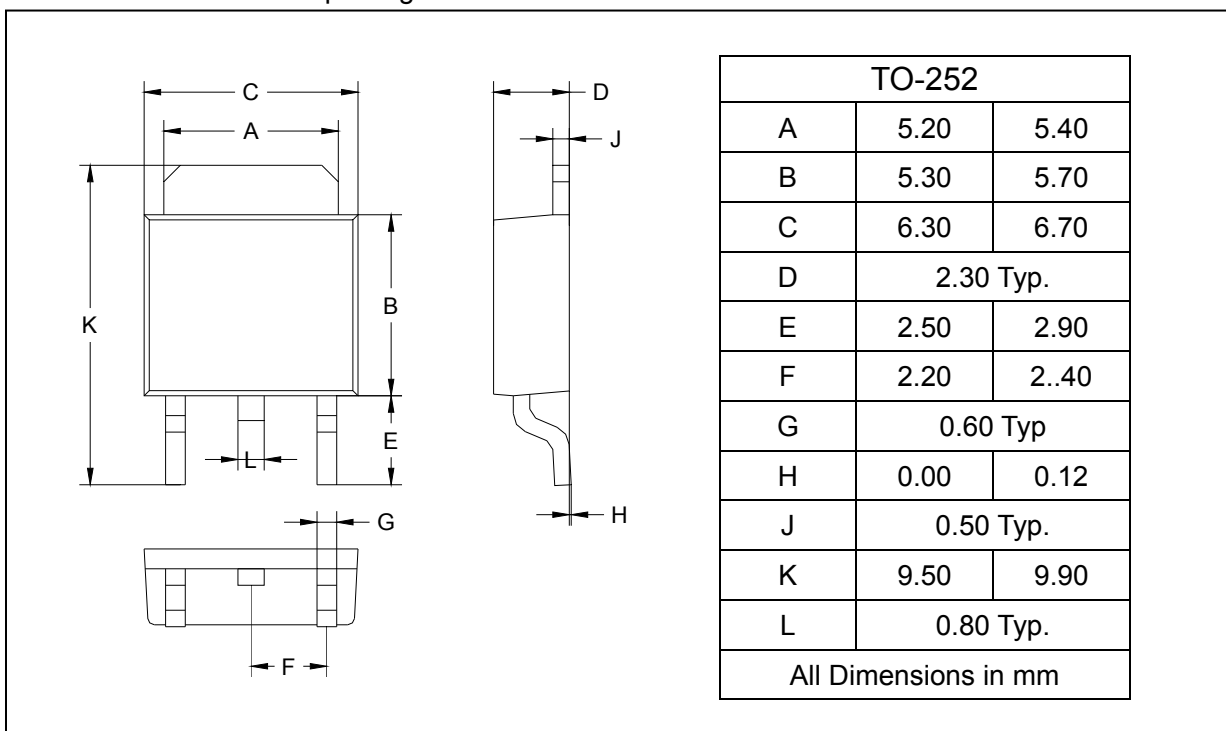
TO-251



**PACKAGE OUTLINE**

Plastic surface mounted package

TO-252





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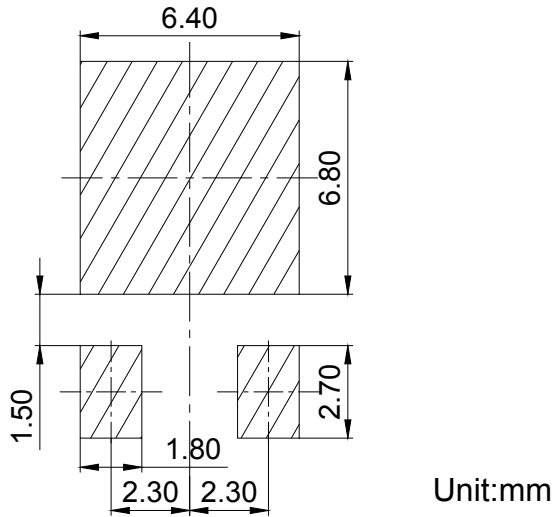
**2A, 650 Volts N-CHANNEL POWER MOSFET**

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**2N65**

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**SOLDERING FOOTPRINT**



**PACKAGE INFORMATION**

Device	Package	Shipping
2N65	TO-251/252	80PCS/Tube
	TO-252	2500PCS/Tape&Reel