

isc Silicon NPN Power Transistor

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
- : $V_{CEO} = 300V(Min.)$
- · Fast Switching Speed
- · High Current Capability
- · 100% avalanche tested
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation

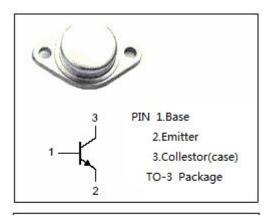


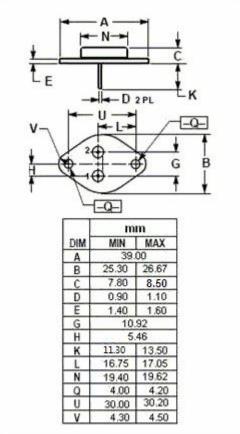
APPLICATIONS

· Designed for converters, inverters, pulse-width- modulated regulators and a variety of power switching circuits.



SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	700	V
Vceo	Collector-Emitter Voltage	300	V
V _{EBO}	Emitter-Base Voltage	5	V
Ic	Collector Current-Continuous	10	Α
Pc	Collector Power Dissipation@Tc=25℃	125	W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature	-65~150	$^{\circ}$







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2N6575

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT		
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C =3A; I _B = 0.3A		1	V		
V _{BE(on)}	Base-Emitter On Voltage	I _C = 7A; V _{CE} =3V		1.4	V		
I _{EBO}	Emitter Cutoff Current	V _{EB} = 8V; I _C = 0		0.1	mA		
Ісво	Collector Base Cutoff Current	V _{CB} =700V; I _E = 0		0.1	mA		
h _{FE-1}	DC Current Gain	I _C =3A; V _{CE} =3V	20	60			
h _{FE-2}	DC Current Gain	I _C = 7A; V _{CE} = 3V	7	21			
f⊤	Current Gain-Bandwidth Product	I _C = 1A; V _{CE} = 10V	5		MHz		
Switching times							
Ton	On Time	I _C = 7A; I _B =1.4A,		1	μ \$		
t _{off}	Off Time			3.2	μS		

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