

### **isc** Silicon NPN Power Transistor

# 2N6739

#### DESCRIPTION

- Collector-Emitter Sustaining Voltage-
- : V<sub>CEO(SUS)</sub> = 350V(Min)
- High Switching Speed
- Low Saturation Voltage
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

#### APPLICATIONS

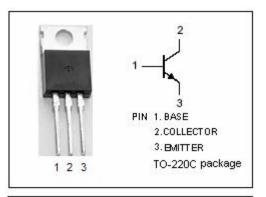
• Designed for use in high-voltage, high-speed, power switching in inductive circuit, they are particularly suited for 115 and 220V switchmode applications such as switching regulators, inverters, DC-DC and converter.

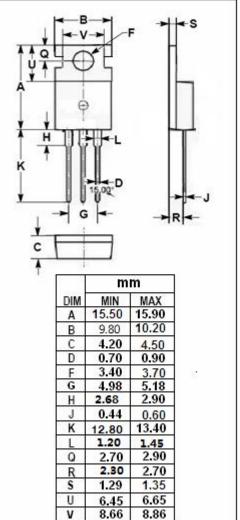
ABSOLUTE MAXIMUM RATINGS(Ta=25°C)					
SYMBOL	PARAMETER	VALUE	UNIT		
VCEV	Collector-Emitter Voltage-V <sub>BE</sub> = -1.5V	550	V		
VCEX	Collector-Emitter Voltage-V <sub>BE</sub> = -1.5V	400	V		
V <sub>CEO</sub>	Collector-Emitter Voltage	350	V		
Vebo	Emitter-Base Voltage	8	V		
lc	Collector Current-Continuous	8	А		
I <sub>CM</sub>	Collector Current-Peak	10	А		
I <sub>B</sub>	Base Current-Continuous	4	А		
Pc	Collector Power Dissipation $T_c$ =25 °C	100	W		
Tj	Junction Temperature	150	°C		
T <sub>stg</sub>	Storage Ttemperature Range	-65~150	°C		

### ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

### THERMAL CHARACTERISTICS

SYMBOL	MBOL PARAMETER		UNIT	
R <sub>th j-c</sub>	Thermal Resistance, Junction to Case	1.25	°C/W	







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### **ELECTRICAL CHARACTERISTICS**

#### $T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	МАХ	UNIT
V <sub>CEO(SUS)</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = 50mA; I <sub>B</sub> = 0	350		V
V <sub>CE(sat)-1</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 1A		1	V
V <sub>CE(sat)-2</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 8A; I <sub>B</sub> = 4A		2	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 5Α; I <sub>B</sub> = 1Α		1.6	V
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 8V; I <sub>C</sub> = 0		2	mA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 5A ; V <sub>CE</sub> = 3V	10	40	
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = 0.2A; V <sub>CE</sub> = 10V, f <sub>test</sub> = 1MHz	10		MHz

Switching Times; Resistive Load

t <sub>d</sub>	Delay Time		0.1	μs
tr	Rise Time	Ic= 5A; I <sub>B1</sub> = -I <sub>B2</sub> = 1A,V <sub>CC</sub> = 125V; t <sub>p</sub> = 20 μ s, Duty Cycle≪1%	0.4	μs
ts	Storage Time		2.5	μs
t <sub>f</sub>	Fall Time		0.5	μs

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