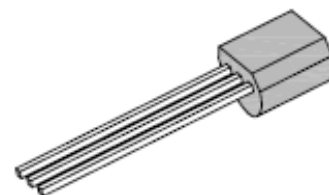


N-Channel Enhancement Mode Field Effect Transistor

Features

- High density cell design for low $R_{DS(ON)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability
- RoHS compliance



TO-92



Mechanical Data

Case:	TO-92, Plastic Package
Terminals:	Solderable per MIL-STD-202G, Method 208
Weight:	0.18 gram

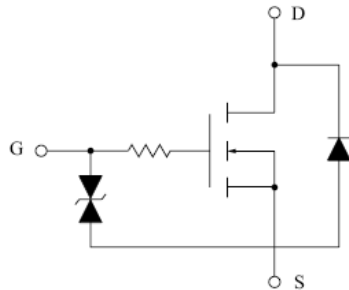
Maximum Ratings ($T_{Ambient}=25^{\circ}C$ unless noted otherwise)

Symbol	Description	2N7000	Unit
V_{DSS}	Drain-Source Voltage	60	V
V_{DGR}	Drain-Gate Voltage ($R_{GS} \leq 1M\Omega$)	60	V
V_{GSS}	Gate-Source Voltage	Continuous	± 20
		Non Repetitive ($t_p < 50\mu s$)	± 40
I_D	Drain Current	Continuous	200
I_{DP}		Pulsed	500
P_D	Drain Power Dissipation	400	mW
T_J	Junction Temperature	150	$^{\circ}C$
T_{STG}	Storage Temperature Range	-55 to +150	$^{\circ}C$

N-Channel Enhancement Mode Field Effect Transistor

2N7000

Equivalent Circuit



This transistor is electrostatic sensitive device.
Please handle with caution.

Electrical Characteristics ($T_{Ambient}=25^{\circ}C$ unless noted otherwise)

Symbol	Description	Min.	Typ.	Max.	Unit	Conditions
BV_{DSS}	Drain-Source Breakdown Voltage	60	-	-	V	V _{GS} =0V, I _D =10μA
I_{DSS}	Zero Gate Voltage Drain Current	-	-	1	μA	V _{DS} =48V, V _{GS} =0V
I_{GSSF}	Gate- Body Leakage, Forward	-	-	1	μA	V _{GS} =15V, V _{DS} =0V
I_{GSSR}	Gate- Body Leakage, Reverse	-	-	-1	μA	V _{GS} =-15V, V _{DS} =0V

On Characteristics (Note)

Symbol	Description	Min.	Typ.	Max.	Unit	Conditions
V_{th}	Gate Threshold Voltage	0.8	2.1	3.0	V	V _{DS} =V _{GS} , I _D =1mA
R_{DS(ON)}	Drain-Source ON Resistance	-	1.2	5.0	Ω	V _{GS} =10V, I _D =500mA
		-	1.8	5.3	Ω	V _{GS} =4.5V, I _D =75mA
V_{DS(ON)}	Drain-Source ON Voltage	-	0.6	2.5	V	V _{GS} =10V, I _D =500mA
		-	0.14	0.4	V	V _{GS} =4.5V, I _D =75mA
I_{D(ON)}	On State Drain Current	75	600	-	mA	V _{GS} =4.5V, V _{DS} =10V
g_{FS}	Forward Transconductance	100	320	-	mS	V _{DS} =10V, I _D =200mA

Note: Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%

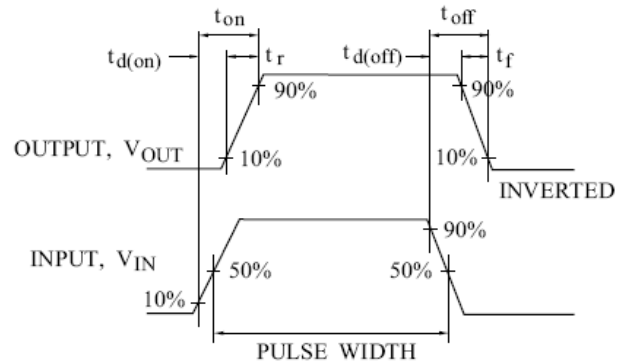
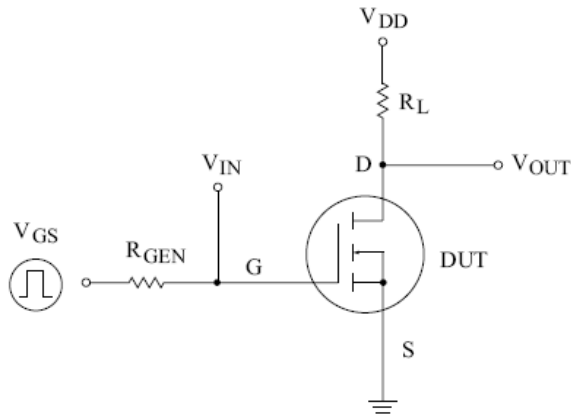
N-Channel Enhancement Mode Field Effect Transistor

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Dynamic Characteristics

Symbol	Description	Min.	Typ.	Max.	Unit	Conditions	
C_{ISS}	Input Capacitance	-	20	50	pF	V _{DS} =25V, V _{GS} =0V, f=1MHz	
C_{RSS}	Reverse Transfer Capacitance	-	4	5	pF		
C_{OSS}	Output Capacitance	-	11	25	pF		
t_{on}	Switching Time	Turn-on Time	-	-	10	nS	V _{DD} =15V, R _L =25Ω, I _D =200mA, V _{GS} =10V, R _{GEN} =25Ω
t_{off}		Turn-off Time	-	-	10	nS	

Switching Time Test Circuit



N-Channel Enhancement Mode Field Effect Transistor

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Typical Characteristics Curves

Fig.1- $I_D - V_{DS}$

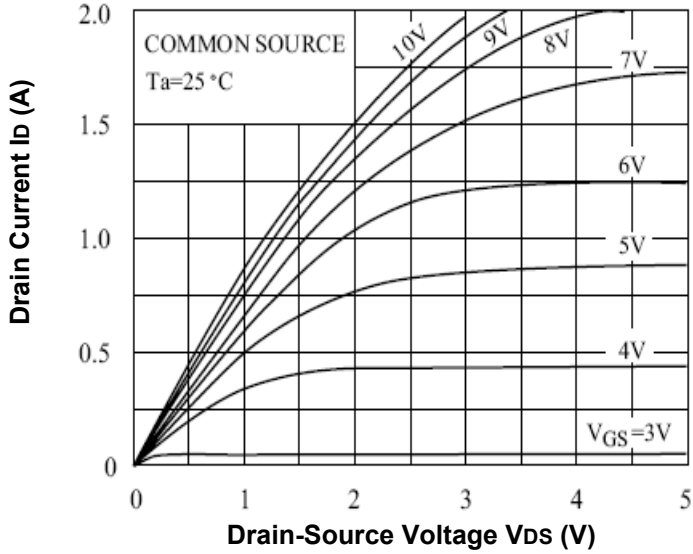


Fig.2- $R_{DS(ON)} - I_D$

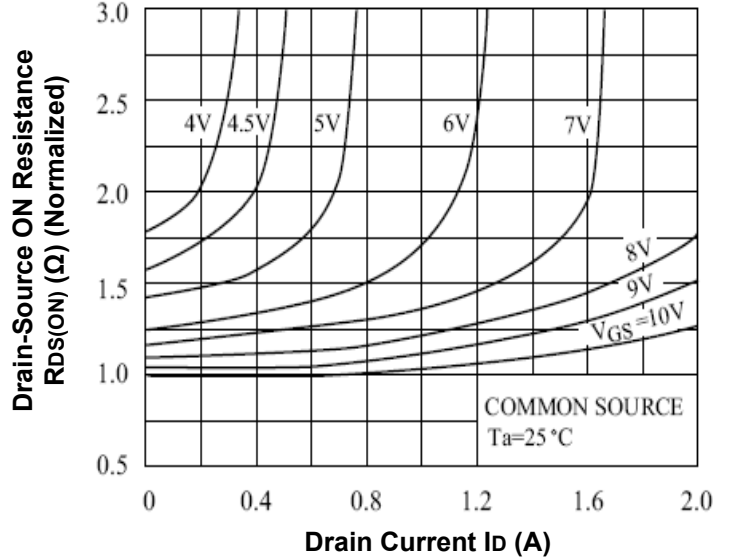


Fig.3- $R_{DS(ON)} - T_J$

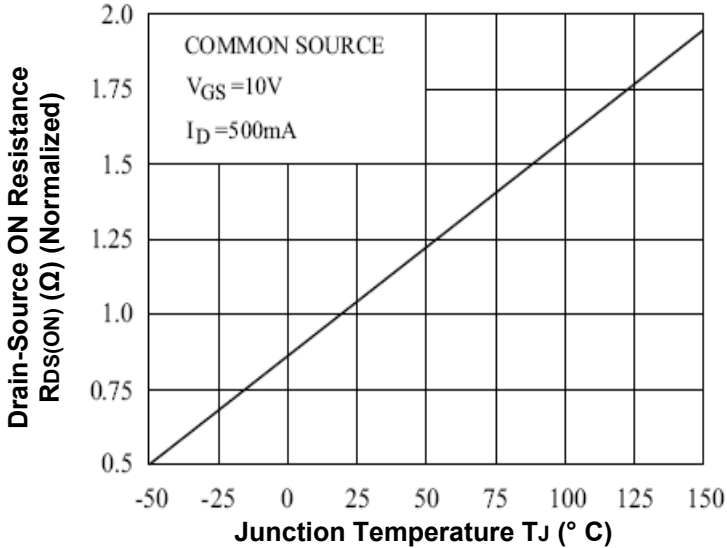
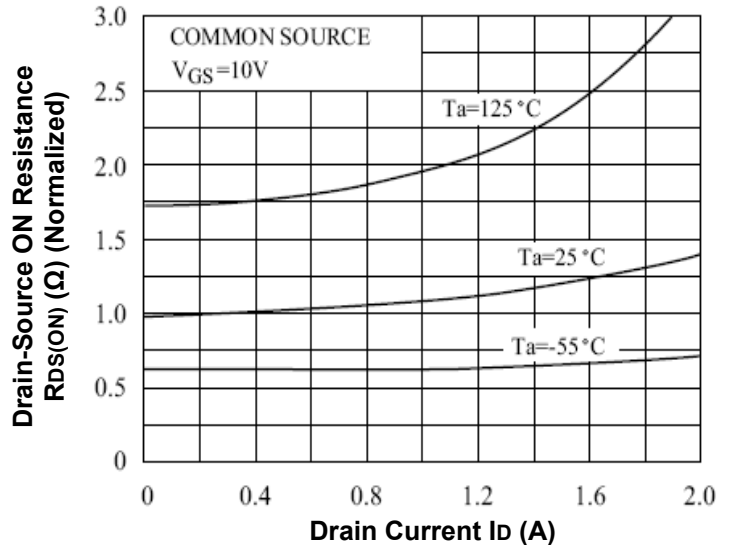


Fig.4- $R_{DS(ON)} - I_D$



N-Channel Enhancement Mode Field Effect Transistor

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Fig.5- I_D - V_{GS}

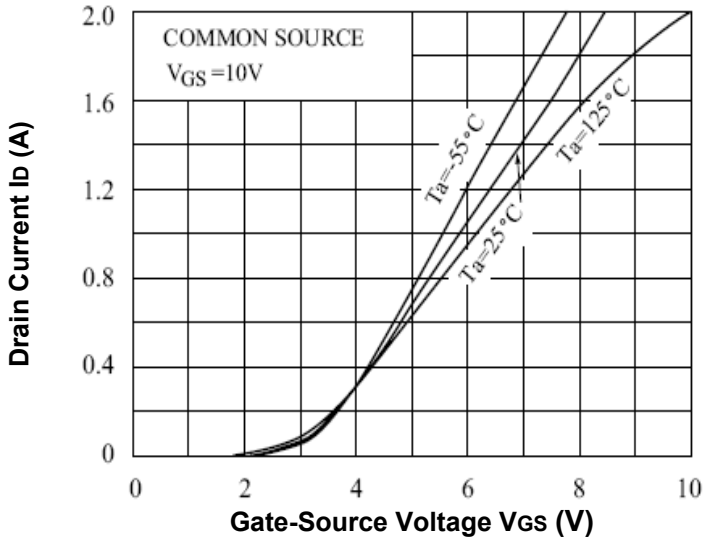


Fig.6- V_{th} - T_J

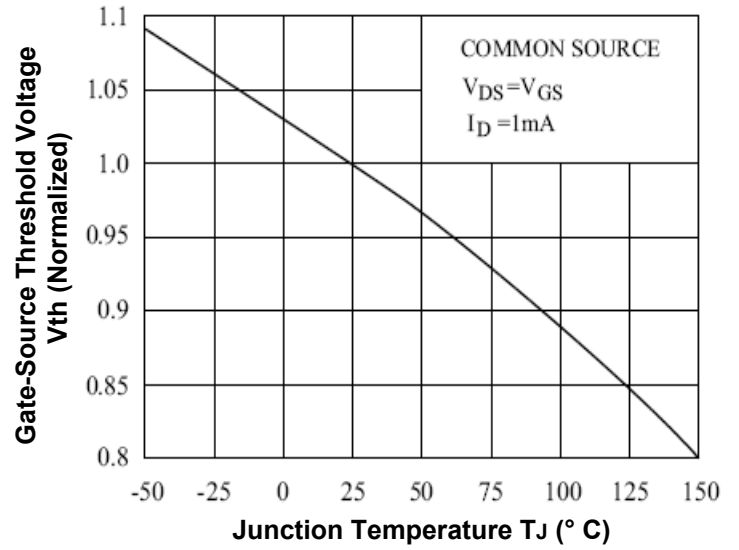


Fig.7- I_S - V_{SD}

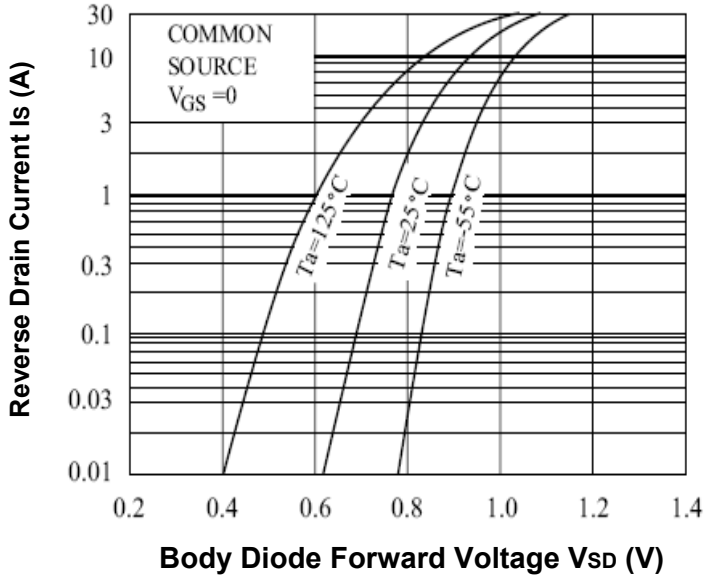
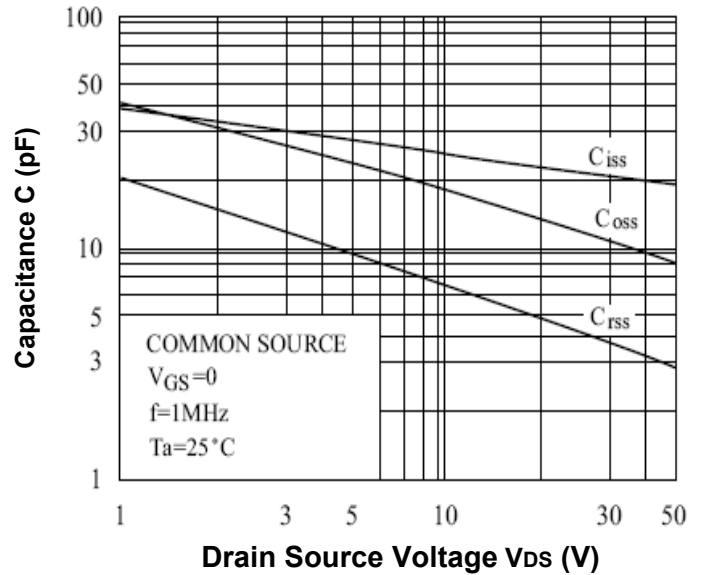


Fig.8- C - V_{DS}



N-Channel Enhancement Mode Field Effect Transistor

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Fig.9- $V_{GS} - Q_g$

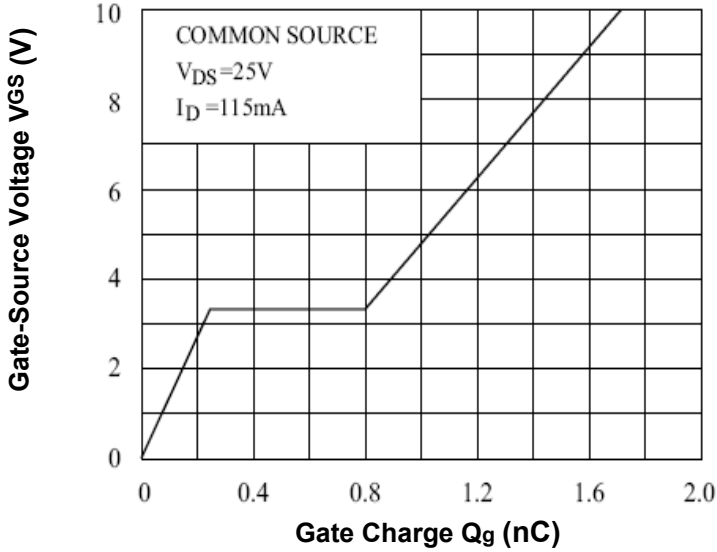


Fig.10- $I_D - V_{DS}$

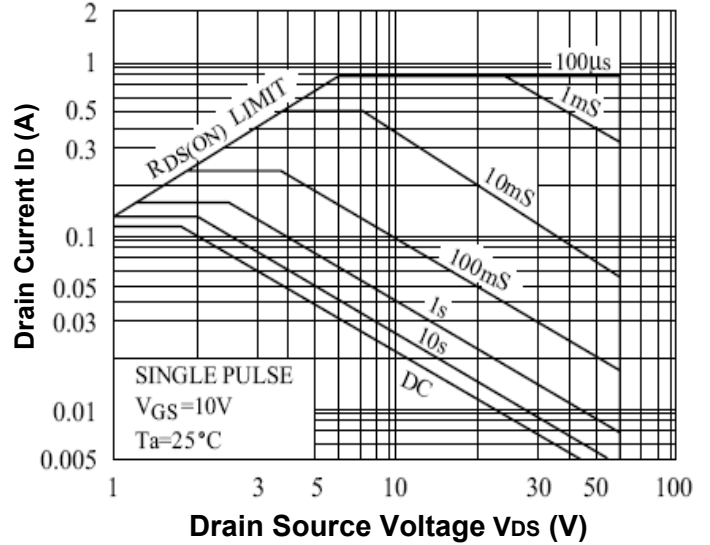
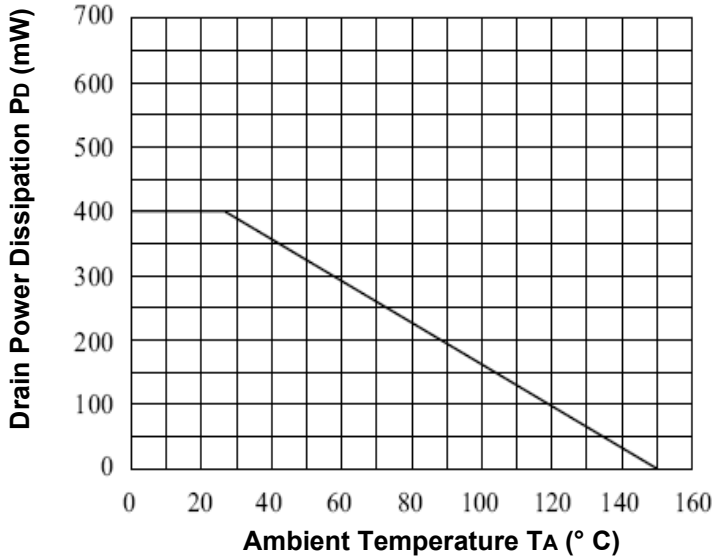


Fig.11- $P_D - T_A$

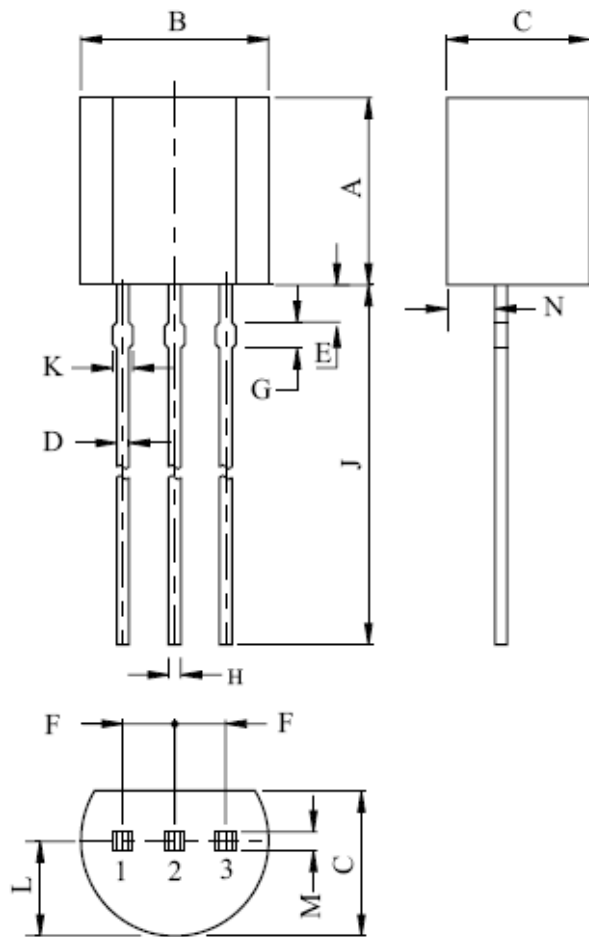


N-Channel Enhancement Mode Field Effect Transistor

2N7000

Dimensions in mm

TO-92



DIM	MILLIMETERS
A	4.70 MAX
B	4.80 MAX
C	3.70 MAX
D	0.45
E	1.00
F	1.27
G	0.85
H	0.45
J	14.00±0.50
K	0.55 MAX
L	2.30
M	0.45 MAX
N	1.00

1. SOURCE
2. GATE
3. DRAIN

N-Channel Enhancement Mode Field Effect Transistor

2N7000

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