

2N711, A, B (GERMANIUM)



CASE 22
(TO-18)

Collector
connected to case

PNP germanium mesa transistors for high-speed switching applications.

MAXIMUM RATINGS

Rating	Symbol	2N711	2N711A	2N711B	Unit
Collector-Base Voltage	V_{CB}	12	15	18	Vdc
Collector-Emitter Voltage	V_{CES}	12	14	15	Vdc
Collector-Emitter Voltage	V_{CEO}	—	7.0	7.0	Vdc
Emitter-Base Voltage	V_{EB}	1.0	1.5	2.0	Vdc
Collector Current (Continuous)	I_C	50	100	100	mAdc
Emitter Current (Continuous)	I_E	50	100	100	mAdc
Junction Temperature	T_J	↔ 100 ↔			°C
Storage Temperature	T_{stg}	↔ -65 to +100 ↔			°C
Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	↔ 300 ↔		↔ 4.0 ↔	
Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	↔ 150 ↔		↔ 2.0 ↔	

2N711, A, B (continued)

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Sym	Min	Typ	Max	Unit
Collector-Base Breakdown Voltage ($I_C = 100 \mu\text{Adc}, I_E = 0$)	BV_{CBO}	12 15	—	—	Vdc
($I_C = 20 \mu\text{Adc}, I_E = 0$)	2N711B	18	—	—	
Collector-Emitter Breakdown Voltage ($I_C = 100 \mu\text{Adc}$)	BV_{CES}	12 14	—	—	Vdc
($I_C = 20 \mu\text{Adc}$)	2N711B	15	—	—	
Collector-Emitter Breakdown Voltage ($I_C = 5 \text{ mAdc}, I_B = 0$)	BV_{CEO}	7.0	—	—	Vdc
Emitter-Base Breakdown Voltage ($I_E = 0.1 \text{ mAdc}, I_C = 0$)	BV_{EBO}	1.0 1.5 2.0	—	—	Vdc
Collector-Base Cutoff Current ($V_{CB} = 5 \text{ Vdc}, I_E = 0$)	I_{CBO}	—	0.2	3.0	μAdc
($V_{CB} = 10 \text{ Vdc}, I_E = 0$)	2N711B	—	—	1.5	
Emitter-Base Cutoff Current ($V_{EB} = 1 \text{ Vdc}$)	I_{EBO}	—	—	100 20	μAdc
DC Current Gain ($I_C = 10 \text{ mAadc}, V_{CE} = 0.5 \text{ Vdc}$)	h_{FE}	20 25 30	30	—	—
($I_C = 50 \text{ mAadc}, V_{CE} = 0.7 \text{ Vdc}$)	2N711A, 2N711B	40	—	—	
Collector Saturation Voltage ($I_C = 10 \text{ mAadc}, I_B = 0.5 \text{ mAadc}$)	$V_{CE(\text{sat})}$	—	0.2	0.5 0.30	Vdc
($I_C = 10 \text{ mAadc}, I_B = 0.4 \text{ mAadc}$)	2N711B	—	—	0.25	
($I_C = 50 \text{ mAadc}, I_B = 2 \text{ mAadc}$)	2N711A 2N711B	—	—	0.55 0.45	
Small-Signal Current Gain ($I_C = 10 \text{ mAadc}, V_{CE} = 5 \text{ Vdc}, f = 100 \text{ MHz}$)	h_{fe}	1.5	—	—	—
($I_C = 10 \text{ mAadc}, V_{CE} = 0.5 \text{ Vdc}, f = 100 \text{ MHz}$)	2N711A 2N711B	1.1 1.2	—	—	
Base-Emitter Voltage ($I_C = 10 \text{ mAadc}, I_B = 0.4 \text{ mAadc}$)	V_{BE}	0.30 0.30	0.38	0.44 0.44	Vdc
($I_C = 50 \text{ mAadc}, I_B = 2 \text{ mAadc}$)	2N711A 2N711B	0.40 0.40	—	0.65 0.65	
Collector Output Capacitance ($V_{CB} = 5 \text{ Vdc}, I_E = 0, f = 1 \text{ MHz}$)	C_{ob}	—	—	6.0	pF
($V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 1 \text{ MHz}$)	2N711	—	5.0	—	
Fall Time	t_f	—	—	150 110	ns
Figure 1: { 2N711A 2N711B		—	—	110	
Figure 2: { 2N711A 2N711B 2N711		—	—	110 100 150	
Minority Carrier Storage Time	t_s	—	—	150 140	ns
Figure 1: { 2N711A 2N711B		—	—	120	
Figure 2: { 2N711A 2N711B 2N711		—	—	100 200	
Delay Plus Rise Time	$t_d + t_r$	—	—	100	ns
Figure 1: { 2N711A, 2N711B		—	—	75	
Figure 2: { 2N711A, 2N711B 2N711		—	—	100	

2N711,A,B (continued)

SWITCHING CIRCUITS

FIGURE 1

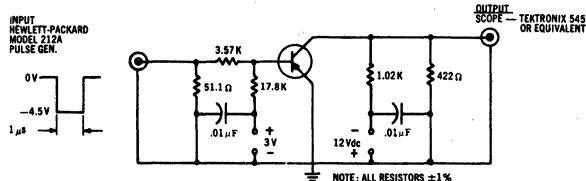
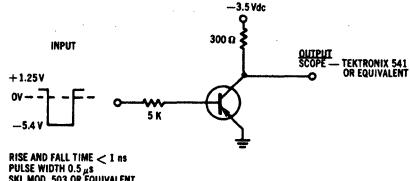
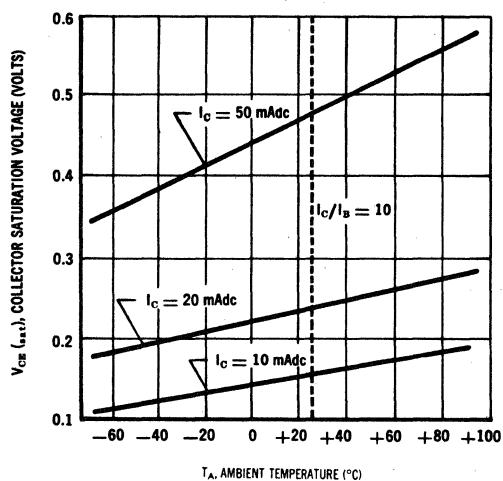


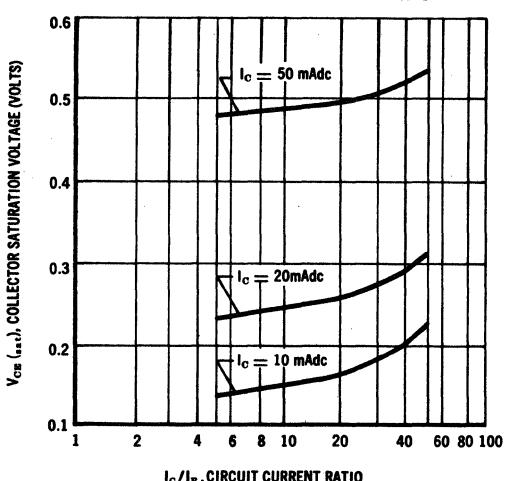
FIGURE 2



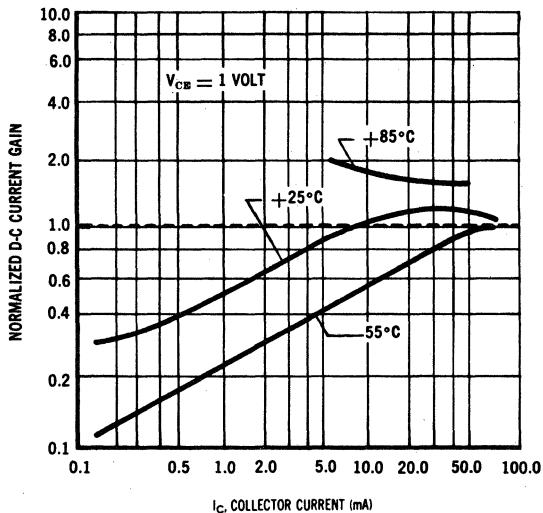
COLLECTOR SATURATION VOLTAGE
versus AMBIENT TEMPERATURE



COLLECTOR SATURATION VOLTAGE
STORAGE TIME versus CIRCUIT CURRENT RATIO



NORMALIZED DC CURRENT GAIN
versus COLLECTOR CURRENT



STORAGE TIME versus
CIRCUIT CURRENT RATIO

