

2N722 (SILICON)
 (2N1132 JAN AVAILABLE)
2N1132
2N1132A
2N2303

**PNP SILICON
 SWITCHING
 TRANSISTORS**

PNP SILICON ANNULAR TRANSISTORS

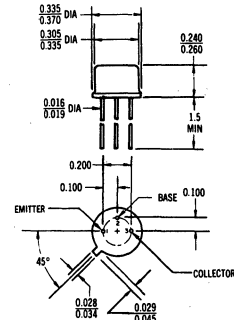
... designed for medium-current switching and amplifier applications.

**CASE 22
 (TO-18)**
 2N722

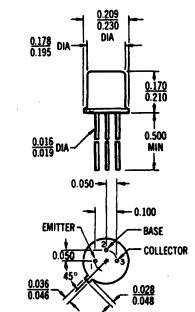
**CASE 31
 (TO-5)**
 2N1132
 2N1132A
 2N2303

MAXIMUM RATINGS

Rating	Symbol	2N722	2N1132	2N1132A	2N2303	Unit
Collector-Emitter Voltage	V_{CEO}	35	35	40	35	Vdc
Collector-Emitter Voltage ($R_{BE} \leq 10$ Ohms)	V_{CER}	50	50	50	50	Vdc
Collector-Base Voltage	V_{CB}	50	50	60	50	Vdc
Emitter-Base Voltage	V_{EB}	5.0	5.0	5.0	5.0	Vdc
Collector Current	I_C	-	-	600	500	mA dc
Total Device Dissipation @ $T_A = 25^\circ C$ Derate above $25^\circ C$	P_D	400 2.67	600 4.0	600 4.0	600 4.0	mW mW/ $^\circ C$
Total Device Dissipation @ $T_C = 25^\circ C$ Derate above $25^\circ C$	P_D	1.5 10	2.0 13.3	2.0 13.3	2.0 13.3	Watts mW/ $^\circ C$
Operating Junction Temperature Range	T_J	-65 to +175				$^\circ C$
Storage Temperature Range	T_{stg}	-65 to +300				$^\circ C$



TO-5 OUTLINE
 (Collector internally connected to case)



TO-18 OUTLINE
 (Collector internally connected to case)

2N722, 2N1132, 2N1132A, 2N2303 (continued)

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage ⁽¹⁾ ($I_C = 100\text{ mAdc}$, $I_B = 0$)	2N722, 2N1132, 2N2303 2N1132A	BV_{CEO}	35 40	- -	Vdc
Collector-Emitter Breakdown Voltage ⁽¹⁾ ($I_C = 100\text{ mAdc}$, $R_{BE} \leq 10\text{ Ohms}$)		BV_{CER}	50	-	Vdc
Collector-Base Breakdown Voltage ($I_C = 100\text{ }\mu\text{Adc}$, $I_E = 0$)	2N722, 2N1132, 2N2303 2N1132A	BV_{CBO}	50 60	- -	Vdc
Emitter-Base Breakdown Voltage ($I_E = 100\text{ }\mu\text{Adc}$, $I_C = 0$) ($I_E = 1.0\text{ mAdc}$, $I_C = 0$)	2N722, 2N1132, 2N2303 2N1132A	BV_{EBO}	5.0 5.0	- -	Vdc
Collector Cutoff Current ($V_{CB} = 30\text{ Vdc}$, $I_E = 0$) ($V_{CB} = 30\text{ Vdc}$, $I_E = 0$, $T_A = 150^\circ\text{C}$) ($V_{CB} = 50\text{ Vdc}$, $I_E = 0$) ($V_{CB} = 50\text{ Vdc}$, $I_E = 0$, $T_A = 150^\circ\text{C}$)	2N722, 2N1132, 2N2303 2N722, 2N1132, 2N2303 2N1132A 2N1132A	I_{CBO}	- - - -	1.0 100 0.5 50	μAdc
Emitter Cutoff Current ($V_{BE} = 5.0\text{ Vdc}$, $I_C = 0$) ($V_{BE} = 2.0\text{ Vdc}$, $I_C = 0$)	2N1132A 2N2303	I_{EBO}	- -	100 100	μAdc
ON CHARACTERISTICS					
DC Current Gain ($I_C = 5.0\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$) ($I_C = 150\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$)	2N722, 2N1132, 2N1132A 2N2303 2N722, 2N1132, 2N1132A 2N2303	h_{FE}	25 75 30 75	- - 90 200	-
Collector-Emitter Saturation Voltage ($I_C = 150\text{ mAdc}$, $I_B = 15\text{ mAdc}$)		$V_{CE(sat)}$	-	1.5	Vdc
Base-Emitter Saturation Voltage ($I_C = 150\text{ mAdc}$, $I_B = 15\text{ mAdc}$)		$V_{BE(sat)}$	-	1.3	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain-Bandwidth Product ($I_C = 50\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$, $f = 20\text{ MHz}$)		f_T	60	-	MHz
Output Capacitance ($V_{CB} = 10\text{ Vdc}$, $I_E = 0$, $f = 100\text{ kHz}$) ($V_{CB} = 10\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$)	2N722, 2N1132, 2N2303 2N1132A	C_{ob}	- -	45 30	pF
Input Capacitance ($V_{BE} = 0.5\text{ Vdc}$, $I_C = 0$, $f = 100\text{ kHz}$)		C_{ib}	-	80	pF
Input Resistance ($I_C = 1.0\text{ mAdc}$, $V_{CB} = 5.0\text{ Vdc}$, $f = 1.0\text{ kHz}$) ($I_C = 5.0\text{ mAdc}$, $V_{CB} = 10\text{ Vdc}$, $f = 1.0\text{ kHz}$)		h_{ib}	25 -	35 10	Ohms
Voltage Feedback Ratio ($I_C = 1.0\text{ Adc}$, $V_{CE} = 5.0\text{ Vdc}$, $f = 1.0\text{ kHz}$) ($I_C = 5.0\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$, $f = 1.0\text{ kHz}$)		h_{rb}	- -	8.0 8.0	$\times 10^{-4}$
Small-Signal Current Gain ($I_C = 1.0\text{ mAdc}$, $V_{CE} = 5.0\text{ Vdc}$, $f = 1.0\text{ kHz}$) ($I_C = 5.0\text{ mAdc}$, $V_{CE} = 5.0\text{ Vdc}$, $f = 1.0\text{ kHz}$)	2N722, 2N1132 2N1132A 2N2303 2N722, 2N1132, 2N1132A 2N2303	h_{fe}	25 25 75 30 75	100 75 300 - -	-
Output Admittance ($I_C = 1.0\text{ mAdc}$, $V_{CE} = 5.0\text{ Vdc}$, $f = 1.0\text{ kHz}$) ($I_C = 5.0\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$, $f = 1.0\text{ kHz}$)		h_{ob}	- -	1.0 5.0	μmhos

⁽¹⁾ Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2.0\%$.