

2N726 (SILICON)

2N727

PNP SILICON ANNULAR TRANSISTORS

... designed for general purpose audio amplifier applications.

- Collector-Emitter Breakdown Voltage –
BV_{CEO} = 20 Vdc (Min) @ I_C = 10 mAdc
- Low Output Capacitance –
C_{ob} = 5.0 pF (Max) @ V_{CB} = 5.0 Vdc

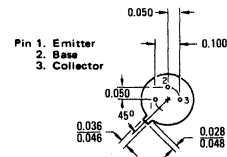
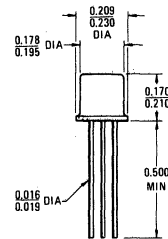
**PNP SILICON
AMPLIFIER
TRANSISTORS**



***MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	20	Vdc
Collector-Base Voltage	V _{CB}	25	Vdc
Emitter-Base Voltage	V _{EB}	5.0	Vdc
Collector Current – Continuous	I _C	50	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	300 2.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.0 6.67	Watt mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +200	°C

*Indicates JEDEC Registered Data



Collector Connected to Case
CASE 22 (1)
(TO-18)

2N726, 2N727 (continued)

*ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage ⁽¹⁾ ($I_C = 10 \text{ mAdc}, I_B = 0$)	BV_{CEO}	20	—	Vdc
Collector Cutoff Current ($V_{CE} = 15 \text{ Vdc}, I_B = 0$)	I_{CEO}	—	5.0	μAdc
Collector Cutoff Current ($V_{CB} = 25 \text{ Vdc}, I_E = 0$) ($V_{CB} = 25 \text{ Vdc}, I_E = 0, T_A = 150^\circ\text{C}$)	I_{CBO}	—	1.0 25	μAdc
Emitter Cutoff Current ($V_{EB} = 5.0 \text{ Vdc}, I_C = 0$)	I_{EBO}	—	0.5	μAdc
ON CHARACTERISTICS				
DC Current Gain ⁽¹⁾ ($I_C = 10 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$)	h_{FE}	15 30	45 120	—
($I_C = 10 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}, T_A = -55^\circ\text{C}$)		6.0 12	— —	
Collector-Emitter Saturation Voltage ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$)	$V_{CE(sat)}$	—	0.6	Vdc
Base-Emitter Saturation Voltage ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$)	$V_{BE(sat)}$	—	1.0	Vdc
DYNAMIC CHARACTERISTICS				
Current-Gain-Bandwidth Product ⁽²⁾ ($I_C = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 100 \text{ MHz}$)	f_T	140	—	MHz
Output Capacitance ($V_{CB} = 5.0 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$)	C_{ob}	—	5.0	pF
Small-Signal Current Gain ($I_C = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz}$)	h_{fe}	15 30	90 240	—

*Indicates JEDEC Registered Data.

(1) Pulse Test: Pulse Width = 300 μs , Duty Cycle $\leq 2.0\%$.

(2) f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.