

2N743 (SILICON)



NPN silicon annular transistor designed for high-speed, low-current, saturated switching operations.

CASE 22 (TO-18)

Collector connected to case

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	12	Vdc
Collector-Base Voltage	V_{CB}	20	Vdc
Emitter-Base Voltage	V_{EB}	5.0	Vdc
Collector Current - Continuous	I_C	200	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300 1.71	mW mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +200	$^\circ\text{C}$

SWITCHING TEST CIRCUITS

FIGURE 1 — TURN-ON AND TURN-OFF TIME TEST CIRCUIT

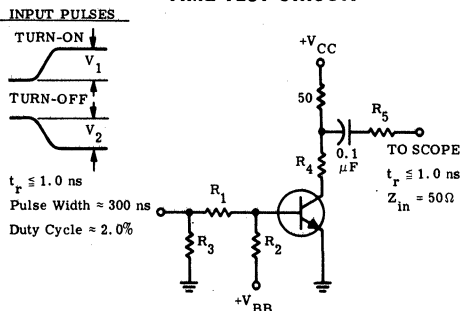
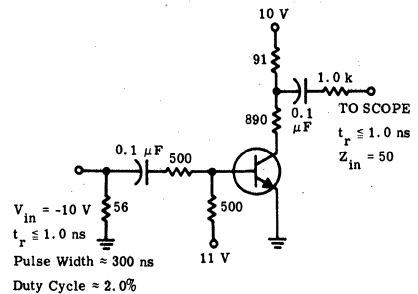


FIGURE 2 — CHARGE-STORAGE TIME TEST CIRCUIT



Condition	CIRCUIT CONDITIONS									
	(VOLTS)					(OHMS)				
	V_1	V_{BB}	V_2	V_{BB}	V_{CC}	$R_1 = R_2$	R_3	R_4	R_5	
1	15	-3.0	-15	+12	3.0	3.3 k	50	220	-	
2	20	-4.5	-20	+15.3*	6.0	330	56	-	1.0 k	

* V_{BB} is pulsed for 1.5 s at less than 10% Duty Cycle to maintain $T_C < 30^\circ\text{C}$.

2N743 (continued)

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

Collector-Emitter Breakdown Voltage ($I_C = 10\text{ mAdc}$, $I_B = 0$)	V_{CE0}	12	-	Vdc
Collector-Cutoff Current ($V_{CE} = 20\text{ Vdc}$, $V_{BE} = 0$) ($V_{CE} = 20\text{ Vdc}$, $V_{BE} = 0$, $T_A = 170^\circ\text{C}$)	I_{CES}	-	1.0 100	μAdc
Collector Cutoff Current ($V_{CE} = 10\text{ Vdc}$, $V_{EB(\text{off})} = 0.35\text{ Vdc}$, $T_A = 100^\circ\text{C}$)	I_{CEX}	-	30	μAdc
Collector Cutoff Current ($V_{CB} = 20\text{ Vdc}$, $I_E = 0$)	I_{CBO}	-	1.0	μAdc
Emitter Cutoff Current ($V_{BE} = 5.0\text{ Vdc}$, $I_C = 0$)	I_{EBO}	-	10	μAdc

ON CHARACTERISTICS

DC Current Gain ($I_C = 1.0\text{ mAdc}$, $V_{CE} = 0.25\text{ Vdc}$) ($I_C = 10\text{ mAdc}$, $V_{CE} = 0.35\text{ Vdc}$) ($I_C = 10\text{ mAdc}$, $V_{CE} = 0.35\text{ Vdc}$, $T_A = -55^\circ\text{C}$) ($I_C = 100\text{ mAdc}$, $V_{CE} = 1.0\text{ Vdc}$)	h_{FE}	10 20 10 10	- 60 - -	
Collector-Emitter Saturation Voltage ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$, $T_A = 170^\circ\text{C}$) ($I_C = 100\text{ mAdc}$, $I_B = 10\text{ mAdc}$, $T_A = 170^\circ\text{C}$) ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$, $T_A = -55^\circ\text{C}$)	$V_{CE(\text{sat})}$	- - -	0.35 1.0 1.1	Vdc
Base-Emitter Saturation Voltage ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$) ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$, $T_A = -55^\circ\text{C}$) ($I_C = 100\text{ mAdc}$, $I_B = 10\text{ mAdc}$) ($I_C = 100\text{ mAdc}$, $I_B = 10\text{ mAdc}$, $T_A = -55^\circ\text{C}$)	$V_{BE(\text{sat})}$	0.65 - - -	0.85 1.1 1.5 1.6	Vdc

DYNAMIC CHARACTERISTICS

Current-Gain-Bandwidth Product ($I_C = 10\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$, $f = 100\text{ MHz}$)	f_T	282	-	MHz
Output Capacitance ($V_{CB} = 5.0\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$)	C_{ob}	-	5.0	pF
Turn-On Time ($V_{CC} = 3.0\text{ Vdc}$, $V_{BE(\text{off})} = 1.5\text{ Vdc}$, $I_C = 10\text{ mAdc}$, $I_{B1} = 3.0\text{ mAdc}$, Condition 1) ($V_{CC} = 6.0\text{ Vdc}$, $V_{BE(\text{off})} = 2.4\text{ Vdc}$, $I_C = 100\text{ mAdc}$, $I_{B1} = 40\text{ mAdc}$, Figure 1, Condition 2)	t_{on}	- -	16 12	ns
Turn-Off Time ($V_{CC} = 3.0\text{ Vdc}$, $I_C = 10\text{ mAdc}$, $I_{B1} = 3.0\text{ mAdc}$, $I_{B2} = 1.5\text{ mAdc}$, Condition 1) ($V_{CC} = 6.0\text{ Vdc}$, $I_C = 100\text{ mAdc}$, $I_{B1} = 40\text{ mAdc}$, $I_{B2} = 20\text{ mAdc}$, Figure 1, Condition 2)	t_{off}	- -	24 40	ns
Storage Time ($I_C = 10\text{ mAdc}$, $I_{B1} = I_{B2} = 10\text{ mAdc}$, $V_{CC} = 10\text{ Vdc}$, Figure 2)	t_s	-	14	ns