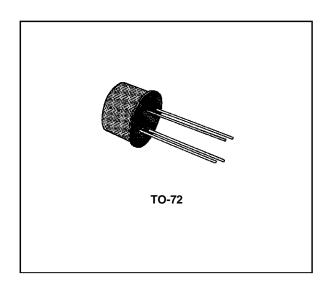
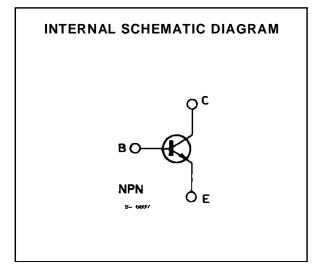


## HIGH-FREQUENCY OSCILLATORS AND AMPLIFIERS

#### **DESCRIPTION**

The 2N918 is a silicon planar epitaxial NPN transistors in Jedec TO-72 metal case. It is designed for low-noise VHF amplifiers, oscillators up to 1 GHz, non-neutralized IF amplifiers and non-saturating circuits with rise and fall times of less than 2.5 ns.





#### **ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Value	Unit	
$V_{CBO}$	Collector-base Voltage (I <sub>E</sub> = 0)	30	V	
V <sub>CEO</sub>	Collector-emitter Voltage (I <sub>B</sub> = 0)	15	V	
$V_{EBO}$	Emitter-base Voltage (I <sub>C</sub> = 0)	3	V	
Ic	Collector Current	50	mA	
P <sub>tot</sub>	Total Power Dissipation at T <sub>amb</sub> ≤ 25 °C	200	mW	
	at T <sub>case</sub> ≤ 25 °C	300	mW	
T <sub>stg</sub> , T <sub>j</sub>	Storage and Junction Temperature	- 65 to 200	°C	

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### THERMAL DATA

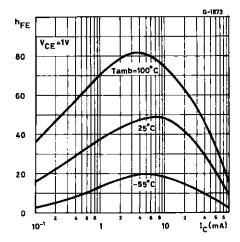
R <sub>th j-case</sub>	Thermal Resistance Junction-case	Max	584	°C/W
R <sub>th j-amb</sub>	Thermal Resistance Junction-ambient	Max	875	°C/W

# ELECTRICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C unless otherwise specified)

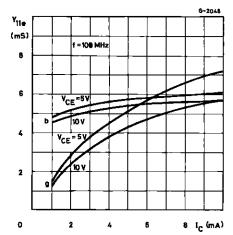
Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
I <sub>CBO</sub>	Collector Cutoff Current (I <sub>E</sub> = 0)	V <sub>CB</sub> = 15 V V <sub>CB</sub> = 15 V	T <sub>amb</sub> = 150 °C			10 1	nA μA
V <sub>(BR)CBO</sub>	Collector-base Breakdown Voltage (I <sub>E</sub> = 0)	I <sub>C</sub> = 1 μA		30			V
V <sub>CEO(sus)</sub>	Collector–emitter Sustaining Voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 3 mA		15			V
V <sub>(BR)EBO</sub>	Emitter–base Breakdown Voltage (I <sub>C</sub> = 0)	I <sub>E</sub> = 10 μA		3			V
V <sub>CE(sat)</sub>	Collector–emitter Saturation Voltage	I <sub>C</sub> = 10 mA	$I_B = 1 \text{ mA}$			0.4	V
V <sub>BE(sat)</sub>	Base–emitter Saturation Voltage	I <sub>C</sub> = 10 mA	$I_B = 1 \text{ mA}$			1	V
h <sub>FE</sub>	DC Current Gain	$I_C = 3 \text{ mA}$	$V_{CE} = 1 V$	20	50		_
f <sub>T</sub>	Transition Frequency	I <sub>C</sub> = 4 mA f = 100 MHz	V <sub>CE</sub> = 10 V	600	900		MHz
C <sub>EBO</sub>	Emitter-base Capacitance	I <sub>C</sub> = 0 f = 1 MHz	$V_{EB} = 0.5 \text{ V}$			2	pF
С <sub>СВО</sub>	Collector-base Capacitance	I <sub>E</sub> = 0	f = 1 MHz $V_{CE} = 0$ $V_{CE} = 10 V$		1.8 1	3 1.7	pF pF
NF	Noise Figure	$I_C = 1 \text{ mA}$ $R_g = 400 \Omega$	$V_{CE} = 6 V$ f = 60 MHz			6	dB
G <sub>pe</sub>	Power Gain	$R_g = 50 \Omega$ $I_C = 6 \text{ mA}$	f = 200 MHz V <sub>CE</sub> = 12 V	15	21		dB
P <sub>o</sub> *	Output Power	I <sub>C</sub> = 12 mA f = 500 MHz	V <sub>CB</sub> = 10 V	30	40		mW
η	Collector Efficiency	I <sub>C</sub> = 12 mA f = 500 MHz	V <sub>CB</sub> = 10 V	25			%

<sup>\*</sup> See test circuit.

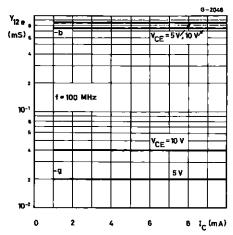
DC Current Gain.



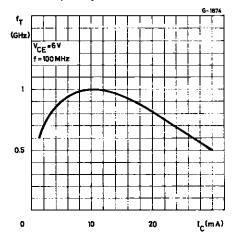
Input Admittance vs. Collector Current.



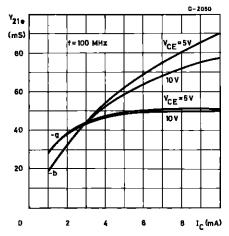
Reverse Transadmittance vs. Collector Current.



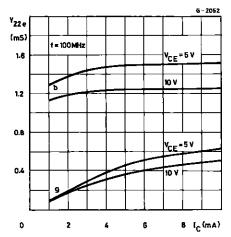
Transition Frequency.



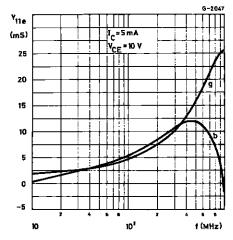
Forward Transadmittance vs. Collector Current.



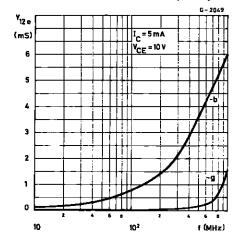
Output Admittance vs. Collector Current.



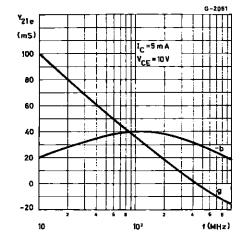
Input Admittance vs. Frequency.



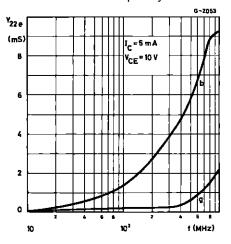
Reverse Transadmittance vs. Frequency.



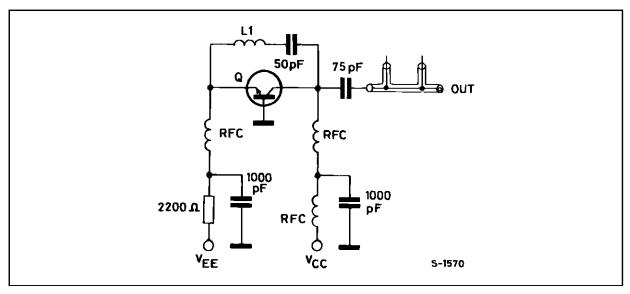
Forward Transadmittance vs. Frequency.



Output Admittance vs. Frequency.

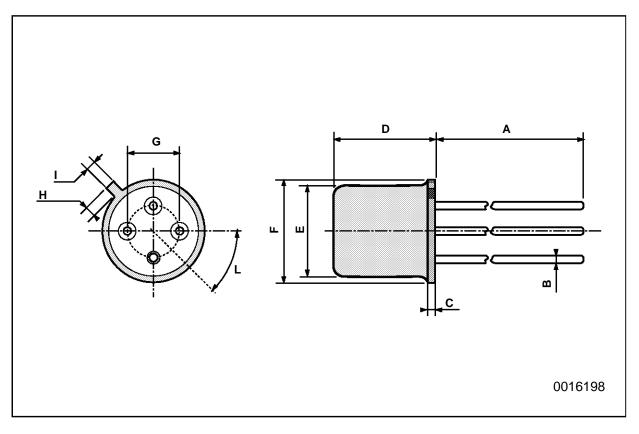


500 MHz Oscillator Test Circuit.



## **TO-72 MECHANICAL DATA**

DIM.	mm		inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А		12.7			0.500	
В			0.49			0.019
D			5.3			0.208
Е			4.9			0.193
F			5.8			0.228
G	2.54			0.100		
Н			1.2			0.047
I			1.16			0.045
L	45°			45°		



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