

2N996 (SILICON)

PNP SILICON ANNULAR TRANSISTOR

... designed for general-purpose amplifier applications.

- Collector-Emitter Sustaining Voltage –
 $V_{CE(sus)} = 12 \text{ Vdc (Min) @ } I_C = 10 \text{ mAdc}$
- Collector-Base Breakdown Voltage –
 $BV_{CBO} = 15 \text{ Vdc (Min) @ } I_C = 10 \text{ } \mu\text{Adc}$
- Emitter-Base Breakdown Voltage –
 $BV_{EBO} = 4.0 \text{ Vdc (Min) @ } I_E = 10 \text{ } \mu\text{Adc}$

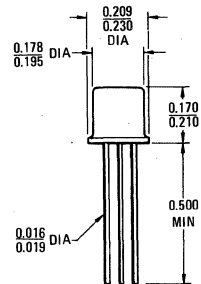
PNP SILICON TRANSISTOR



*MAXIMUM RATINGS

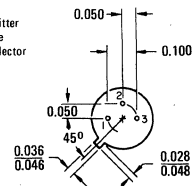
Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	12	Vdc
Collector-Base Voltage	V_{CB}	15	Vdc
Emitter-Base Voltage	V_{EB}	4.0	Vdc
Collector Current	I_C	200	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	360 2.06	mW mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	1.2 6.86	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +200	$^\circ\text{C}$

* Indicates JEDEC Registered Data.



STYLE 1

- Pin 1. Emitter
- 2. Base
- 3. Collector



Collector Connected to Case
CASE 22(1)
(TO-18)

2N996 (continued)

*ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

Collector-Emitter Sustaining Voltage(1) ($I_C = 10 \text{ mAdc}$, $I_B = 0$)	$V_{CE(sus)}$	12	—	Vdc
Collector-Base Breakdown Voltage ($I_C = 10 \mu\text{A}$, $I_E = 0$)	BV_{CBO}	15	—	Vdc
Emitter-Base Breakdown Voltage ($I_E = 10 \mu\text{A}$, $I_C = 0$)	BV_{EBO}	4.0	—	Vdc
Collector Cutoff Current ($V_{CB} = 10 \text{ Vdc}$, $I_E = 0$) ($V_{CB} = 10 \text{ Vdc}$, $I_E = 0$, $T_A = 150^\circ\text{C}$)	I_{CBO}	—	0.005 15	μA μA
Emitter Cutoff Current ($V_{BE} = 4.0 \text{ Vdc}$, $I_C = 0$)	I_{EBO}	—	10	μA

ON CHARACTERISTICS

DC Current Gain ($I_C = 20 \text{ mAdc}$, $V_{CE} = 1.0 \text{ Vdc}$)	h_{FE}	35	—	—
Collector-Emitter Saturation Voltage ($I_C = 60 \text{ mAdc}$, $I_B = 2.0 \text{ mAdc}$)	$V_{CE(sat)}$	—	0.3	Vdc
Base-Emitter Saturation Voltage ($I_C = 20 \text{ mAdc}$, $I_B = 2.0 \text{ mAdc}$)	$V_{BE(sat)}$	—	0.95	Vdc

DYNAMIC CHARACTERISTICS

Current-Gain-Bandwidth Product(2) ($I_C = 10 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$, $f = 100 \text{ MHz}$)	f_T	100	—	MHz
Output Capacitance ($V_{CB} = 10 \text{ Vdc}$, $I_E = 0$, $f = 100 \text{ kHz}$ to 1.0 MHz)	C_{ob}	—	10	pF

*Indicates JEDEC Registered Data.

(1)Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

(2) f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.