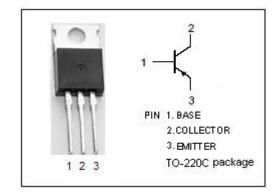


isc Silicon PNP Power Transistor

2SA1009A

DESCRIPTION

- Low Collector Saturation Voltage-
- : VCE(sat)= -1V(Max.)@ IC= -0.3A
- · Fast Switching Speed
- Wide Reverse Bias Safe Operating Area
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

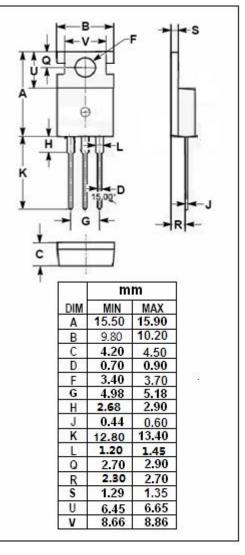


APPLICATIONS

 Designed for switching regulators, DC/DC converters and High frequency power amplifier application.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-400	V
V _{CEO}	Collector-Emitter Voltage	-400	V
V _{ЕВО}	Emitter-Base Voltage	-7.0	V
lc	Collector Current-Continuous	-2.0	Α
Ісм	Collector Current-Peak	-4.0	Α
Pc	Collector Power Dissipation@ T _a =25°C	15	W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$





isc Silicon PNP Power Transistor

2SA1009A

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -0.3A; I _B = -0.06A, L=1mH	-400		٧
V _{CEX(SUS)-1}	Collector-Emitter Sustaining Voltage	I_C = -0.3A; I_{B1} =- I_{B2} = -0.06A, L=180 μ H, clamped	-400		V
V _{CEX(SUS)-2}	Collector-Emitter Sustaining Voltage	I_C = -0.6A; I_{B1} = -0.2A; - I_{B2} = 0.06A, L= 180 μ H, clamped	-400		٧
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -0.3A; I _B = -0.06A		-1	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	Ic= -0.3A; I _B = -0.06A		-1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -400V; I _E = 0		-10	μ A
I _{CER}	Collector Cutoff Current	V _{CE} = -400V ;R _{BE} = 51 Ω ,T _a =125℃		-1.0	mA
I _{CEX}	Collector Cutoff Current	V_{CE} = -400V; $V_{BE(off)}$ = -1.5V V_{CE} = -400V; $V_{BE(off)}$ = -1.5V, T_a =125°C		-10 -1.0	μA mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5.0V; I _C = 0		-10	μ A
h _{FE-1}	DC Current Gain	I _C = -0.1A; V _{CE} = -5V	20	200	
h _{FE-2}	DC Current Gain	Ic= -0.3A; VcE= -5V	10		

h_{FE-1} Classifications

М	L	К	J	Н
20-40	30-60	40-80	60-120	100-200

NOTICE:

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