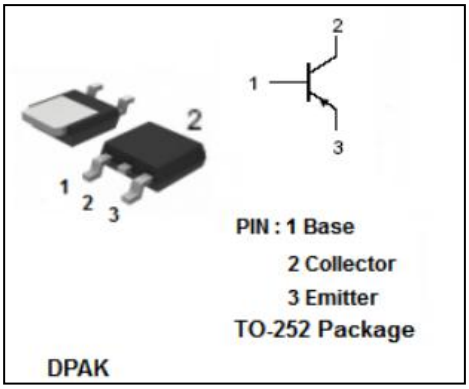
isc Silicon PNP Power Transistor — 2SA1012

DESCRIPTION

- Low Collector Saturation Voltage
  :$V_{CE(sat)}$= -0.4(V)(Max)@I_C= -3A
- High Switching Speed
- Complement to Type 2SC2562
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

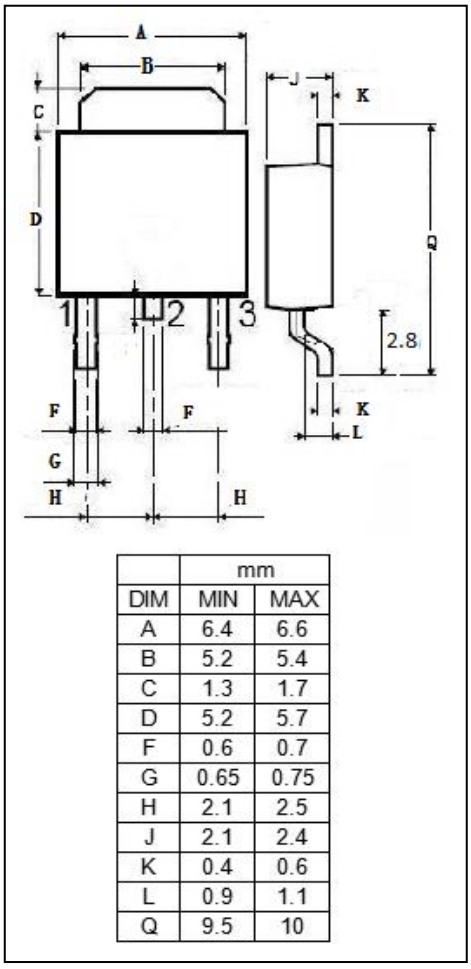


APPLICATIONS

- Designed for high current switching applications.

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-60	V
V_{CEO}	Collector-Emitter Voltage	-50	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-5	A
P_C	Total Power Dissipation @ T_C=25℃	20	W
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature Range	-55~150	℃



	mm	
DIM	MIN	MAX
A	6.4	6.6
B	5.2	5.4
C	1.3	1.7
D	5.2	5.7
F	0.6	0.7
G	0.65	0.75
H	2.1	2.5
J	2.1	2.4
K	0.4	0.6
L	0.9	1.1
Q	9.5	10

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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	I_C= -10mA ; I_B= 0	-50			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C= -3A; I_B= -0.15A			-0.4	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	I_C= -3A; I_B= -0.15A			-1.2	V
I_{CBO}	Collector Cutoff Current	V_{CB}= -50V ; I_E= 0			-1	μA
I_{EBO}	Emitter Cutoff Current	V_{EB}= -5V; I_C= 0			-1	μA
h_{FE-1}	DC Current Gain	I_C= -1A ; V_{CE}= -1V	70		240	
h_{FE-2}	DC Current Gain	I_C= -3A ; V_{CE}= -1V	30			

◆ **h_{FE-1} Classifications**

O	Y
70-140	120-240

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