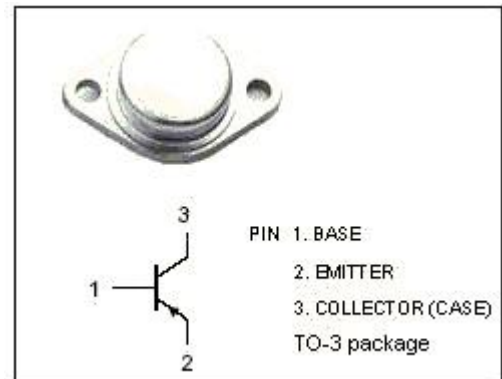


isc Silicon PNP Power Transistor
2SA1068
DESCRIPTION

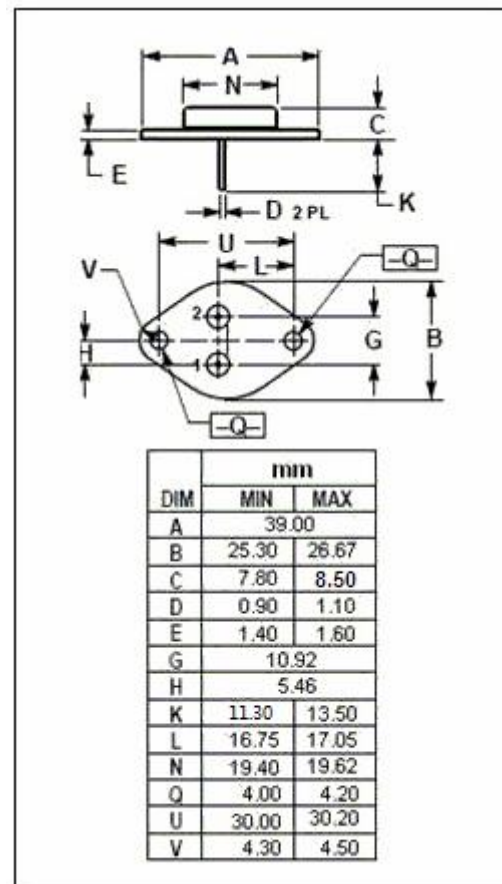
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -150V(\text{Min.})$
- Good Linearity of h_{FE}
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for AF amplifier, high power amplifier applications.


ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-150	V
V_{CEO}	Collector-Emitter Voltage	-150	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-10	A
I_{CM}	Collector Current-Peak	-15	A
P_C	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	100	W
T_j	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-65~150	$^\circ\text{C}$



isc Silicon PNP Power Transistor**2SA1068****ELECTRICAL CHARACTERISTICS**T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-150			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A			-2.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A			-2.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -150V; I _E = 0			-0.1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-0.1	mA
h _{FE}	DC Current Gain	I _C = -3A; V _{CE} = -4V	30		200	
f _T	Current-Gain—Bandwidth Product	I _C = -0.5A; V _{CE} = -10V		50		MHz

NOTICE:

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