

isc Silicon PNP Power Transistor

2SA1074

DESCRIPTION

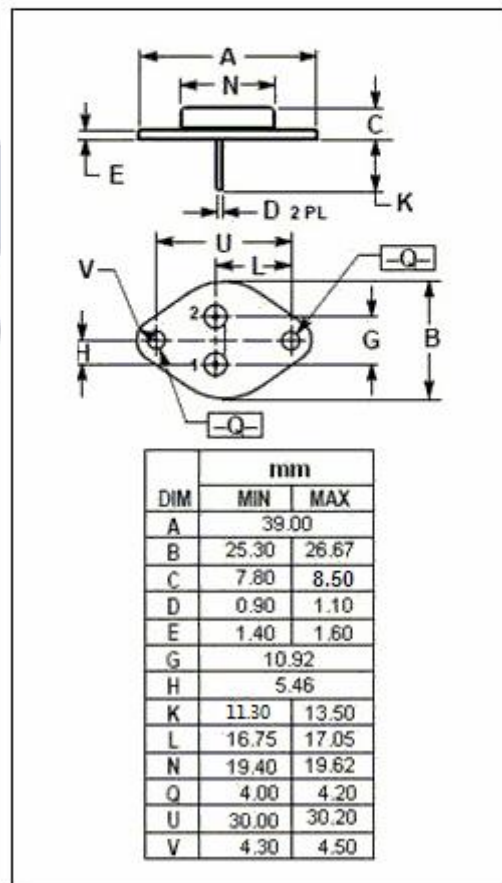
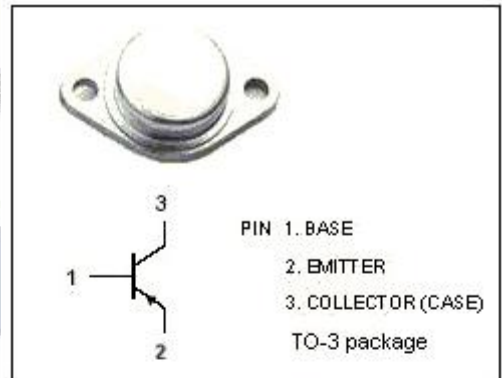
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -160V(\text{Min.})$
- Good Linearity of h_{FE}
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- High power audio stepping motor and other linear applications

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-160	V
V_{CEO}	Collector-Emitter Voltage	-160	V
V_{EBO}	Emitter-Base Voltage	-7	V
I_C	Collector Current-Continuous	-15	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	150	W
T_J	Junction Temperature	150	°C
T_{stg}	Storage Temperature Range	-65~150	°C



isc Silicon PNP Power Transistor**2SA1074****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -30mA; R _{BE} = ∞	-160			V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = -4A; I _B = -0.4A			-1.1	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = -10A; I _B = -3.3A			-3.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -4A; V _{CE} = -4V			-1.8	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -60V; I _E = 0			-1	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -7V; I _C = 0			-1	μ A
h _{FE-1}	DC Current Gain	I _C = -4A; V _{CE} = -4V	20			
h _{FE-2}	DC Current Gain	I _C = -10A; V _{CE} = -4V	5			

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