

isc Silicon PNP Transistor

2SA1082

DESCRIPTION

- High Voltage
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

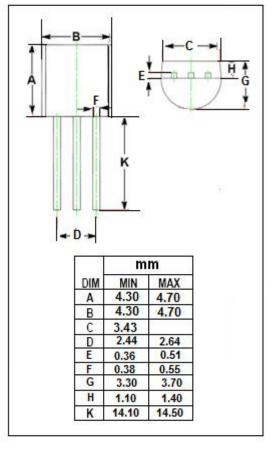
APPLICATIONS

Design For Amplifier and general purpose applications

PIN: 1 Emitter
2 Collector
3 Base
TO-92 package

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-120	V
V _{CEO}	Collector-Emitter Voltage	-120	V
V _{EBO}	Emitter-Base Voltage	-5	V
lc	Collector Current-Continuous -100		mA
P _D	Collector Power Dissipation@T _A =25°C 400		mW
TJ	Junction Temperature 150		$^{\circ}$
T _{stg}	Storage Temperature Range -5		°C





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ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP ·	MAX	UNIT
V _{(BR)CES}	Collector-Emitter Breakdown Voltage	I _C = -10 μ A; I _E = 0	-120			V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -1mA; I _B = 0	-120			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -10 μ A; I _C = 0	-5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C =-10mA ; I _B = -1mA			-0.2	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -2mA ; V _{CE} = -12V		-0.6		V
I _{CBO}	Collector Cutoff Current	V _{CB} = -50V; I _E = 0			-0.1	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -2V; I _C = 0			-0.1	μА
h _{FE}	DC Current Gain	I _C =-2mA ; V _{CE} = -12V	250		800	
f⊤	Current-Gain—Bandwidth Product	I _C = -2mA; V _{CE} = -12V; f= 1MHz		90		MHz
Cob	Output Capacitance	V _{CB} = −10 V, I _E = 0, f =1.0MHz		3.5		pF

♦ h_{FE} Classifications

D	E
250-500	400-800

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