



## 2SA1208/2SC2910

### High-Voltage Switching Audio 80W Output Predriver Applications

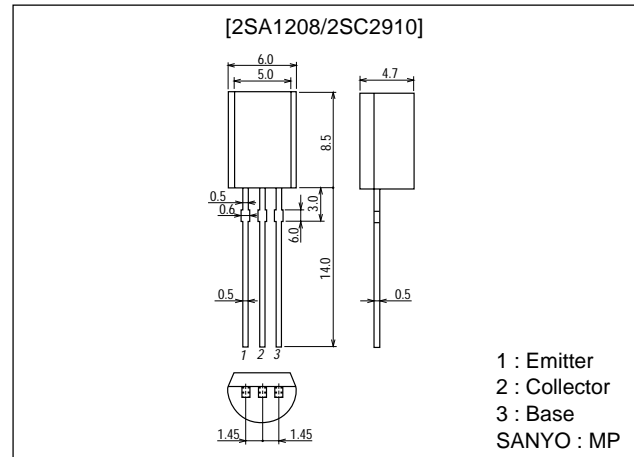
#### Features

- Adoption of FBET process.
- High breakdown voltage.
- Excellent linearity of  $h_{FE}$  and small  $C_{ob}$ .
- Fast switching speed.

#### Package Dimensions

unit:mm

2006B



1 : Emitter  
2 : Collector  
3 : Base  
SANYO : MP

( ) : 2SA1208

#### Specifications

**Absolute Maximum Ratings** at  $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	$V_{CBO}$		(-)180	V
Collector-to-Emitter Voltage	$V_{CEO}$		(-)160	V
Emitter-to-Base Voltage	$V_{EBO}$		(-)5	V
Collector Current	$I_C$		(-)70	mA
Collector Current (Pulse)	$I_{CP}$		(-)140	mA
Collector Dissipation	$P_C$		900	mW
Junction Temperature	$T_j$		150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** at  $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	$I_{CBO}$	$V_{CB}=(-)80\text{V}, I_E=0$			(-)0.1	$\mu\text{A}$
Emitter Cutoff Current	$I_{EBO}$	$V_{EB}=(-)4\text{V}, I_C=0$			(-)0.1	$\mu\text{A}$
DC Current Gain	$h_{FE}$	$V_{CE}=(-)5\text{V}, I_C=(-)10\text{mA}$	100*		400*	
Gain-Bandwidth Product	$f_T$	$V_{CE}=(-)10\text{V}, I_C=(-)10\text{mA}$		150		MHz
Output Capacitance	$C_{ob}$	$V_{CB}=(-)10\text{V}, f=1\text{MHz}$		(2.5)2.0		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=(-)30\text{mA}, I_B=(-)3\text{mA}$		0.08 (-0.14)	0.3 (-0.4)	V

\* : The 2SA1208/2SC2910 are classified by 10mA  $h_{FE}$  are follows :

Continued on next page.

Rank	R	S	T
$h_{FE}$	100 to 200	140 to 280	200 to 400

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**SANYO Electric Co., Ltd. Semiconductor Company**

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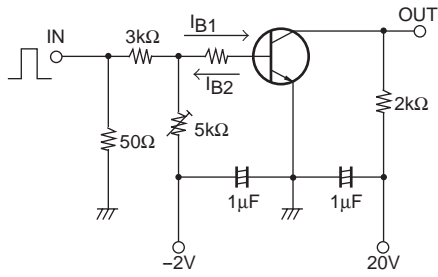
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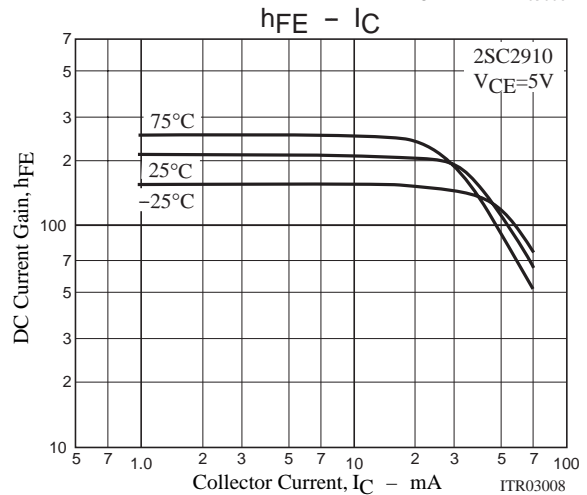
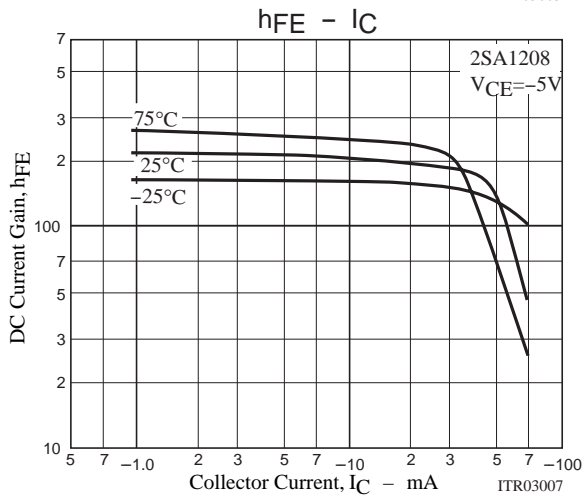
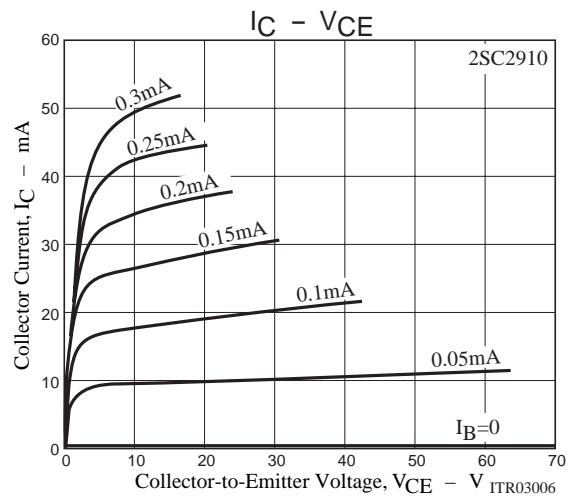
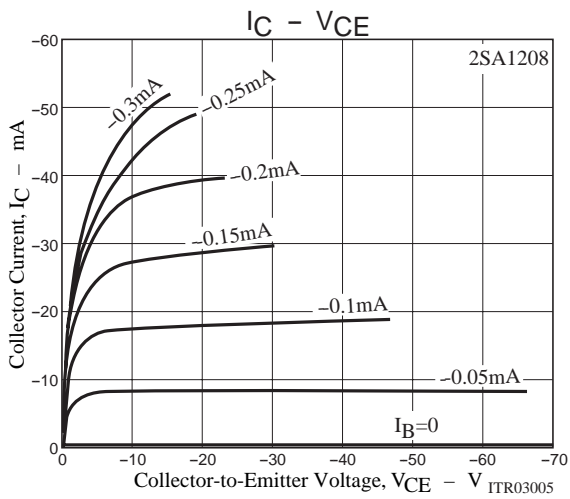
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Turn-ON Time	$t_{on}$	See specified Test Circuit		0.1		$\mu s$
Fall Time	$t_f$	See specified Test Circuit		0.2		$\mu s$
Storage Time	$t_{stg}$	See specified Test Circuit		1.0		$\mu s$

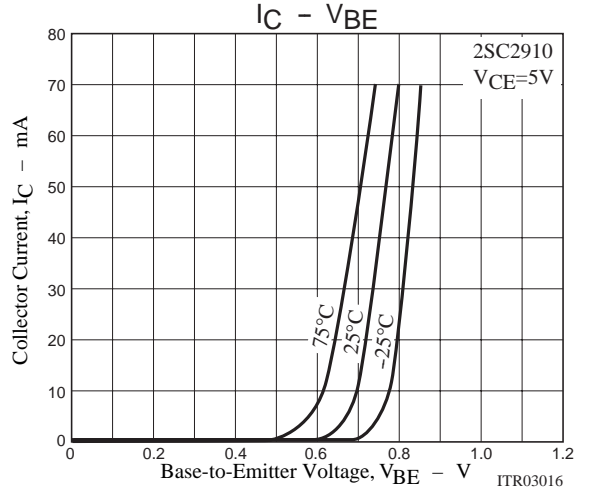
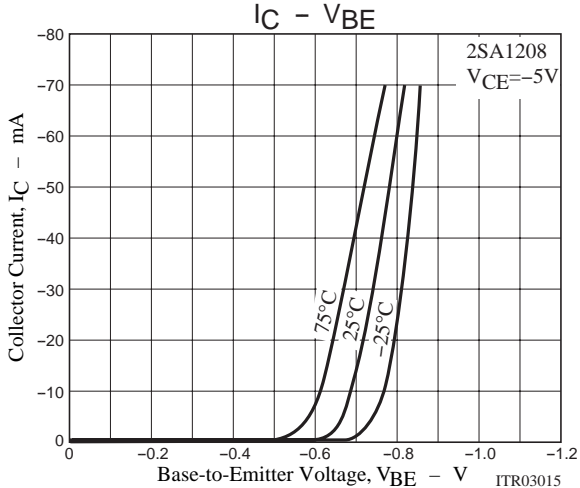
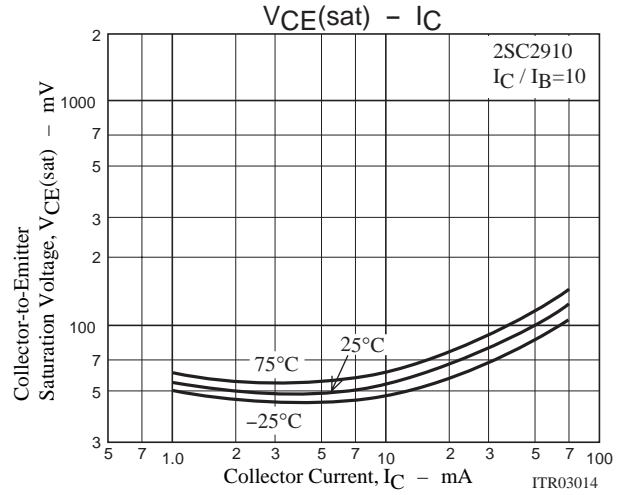
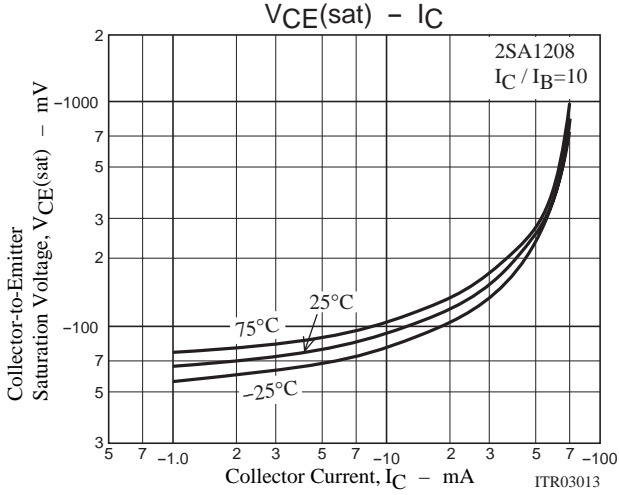
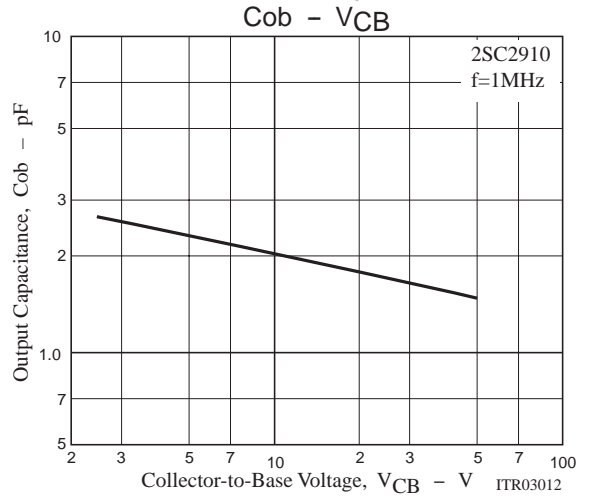
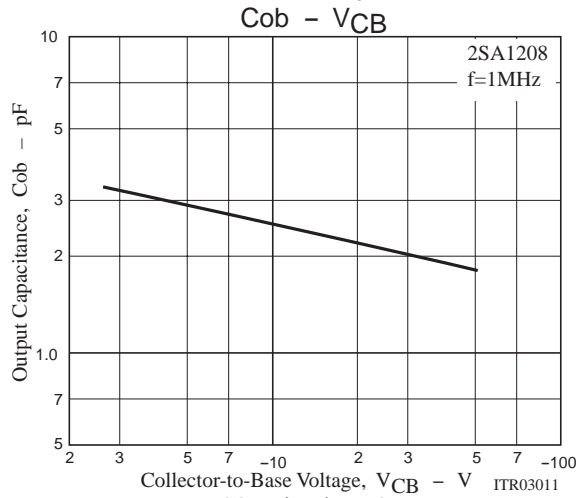
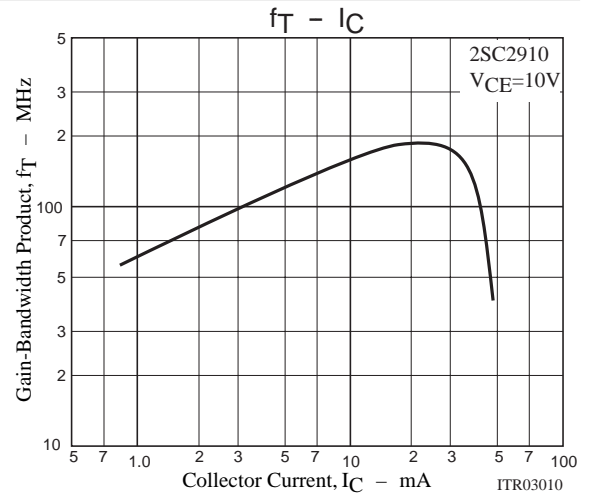
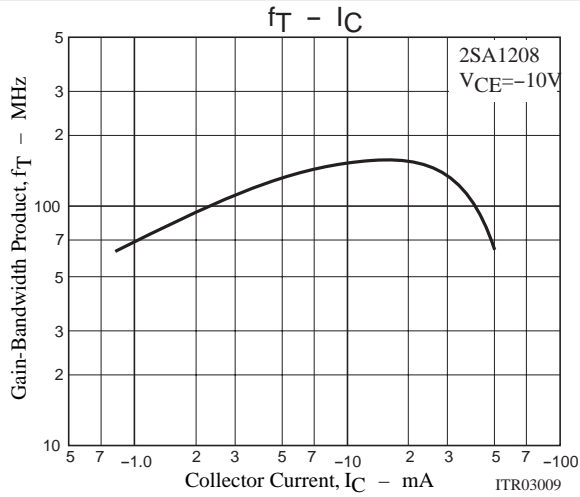
## Switching Time Test Circuit



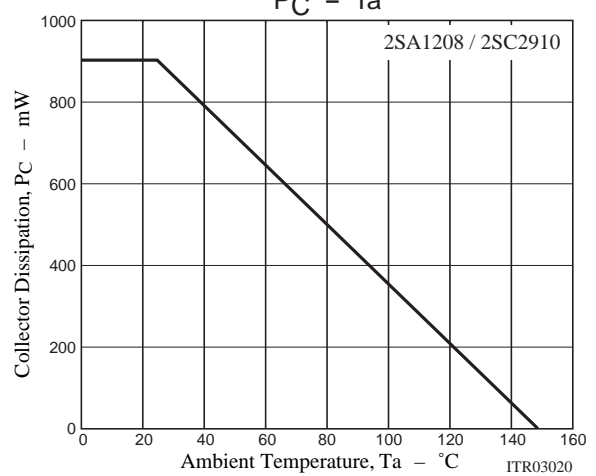
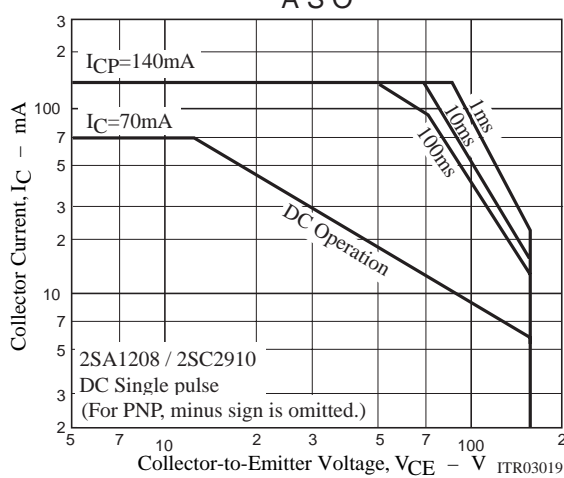
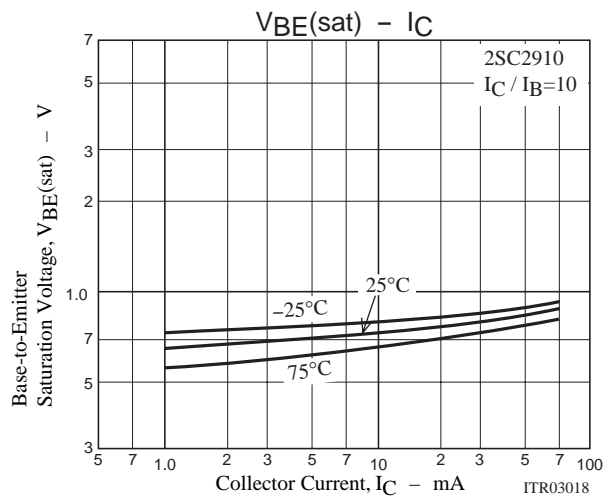
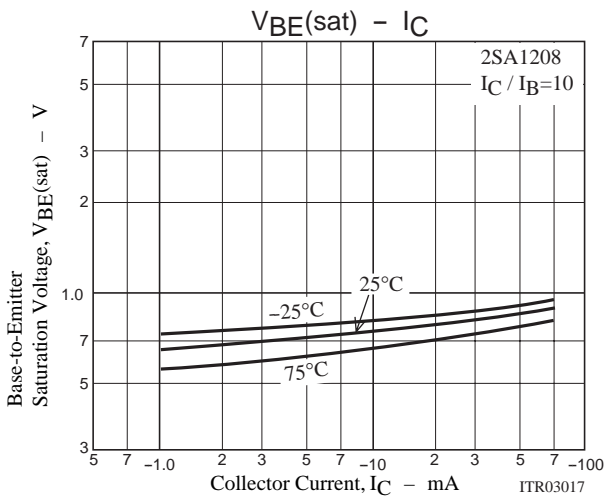
$I_C = 10I_{B1} = -10I_{B2} = 10mA$   
 (For PNP, the polarity is reversed.)



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