

isc Silicon PNP Power Transistor
2SA1250
DESCRIPTION

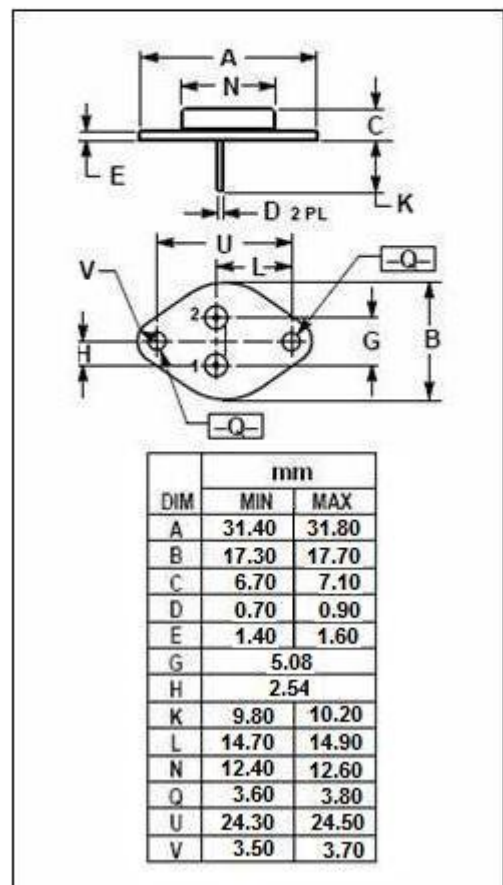
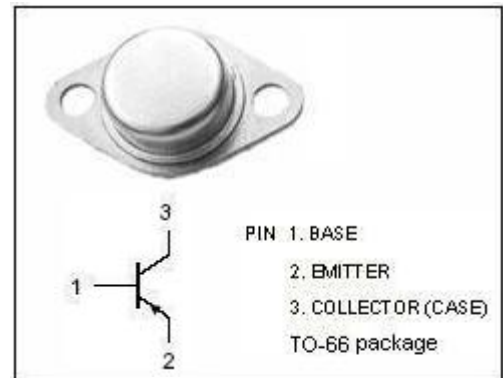
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -200V(\text{Min})$
- Low Collector Saturation Voltage-
: $V_{CE(sat)} = -1.0V(\text{Max.}) @ I_C = -5A$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for general-purpose power switching applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-200	V
V_{CEO}	Collector-Emitter Voltage	-200	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-8	A
I_B	Base Current-Continuous	-2	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	30	W
T_J	Junction Temperature	175	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-65~175	$^\circ\text{C}$



isc Silicon PNP Power Transistor**2SA1250****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CE(SUS)}$	Collector-Emitter Sustaining Voltage	$I_C = -10\text{mA}; I_B = 0$	-200			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -5\text{A}; I_B = -0.5\text{A}$			-1.0	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = -5\text{A}; I_B = -0.5\text{A}$			-1.5	V
I_{CEO}	Collector Cutoff Current	$V_{CE} = -200\text{V}; I_B = 0$			-1.0	mA
I_{CBO}	Collector Cutoff Current	$V_{CB} = -200\text{V}; I_E = 0$			-0.1	mA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -5\text{V}; I_C = 0$			-0.1	mA
h_{FE}	DC Current Gain	$I_C = -5\text{A}; V_{CE} = -1\text{V}$	10			
f_T	Current-Gain—Bandwidth Product	$I_C = -0.5\text{A}; V_{CE} = -10\text{V}$	40			MHz

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