

isc Silicon PNP Power Transistor
2SA1279
DESCRIPTION

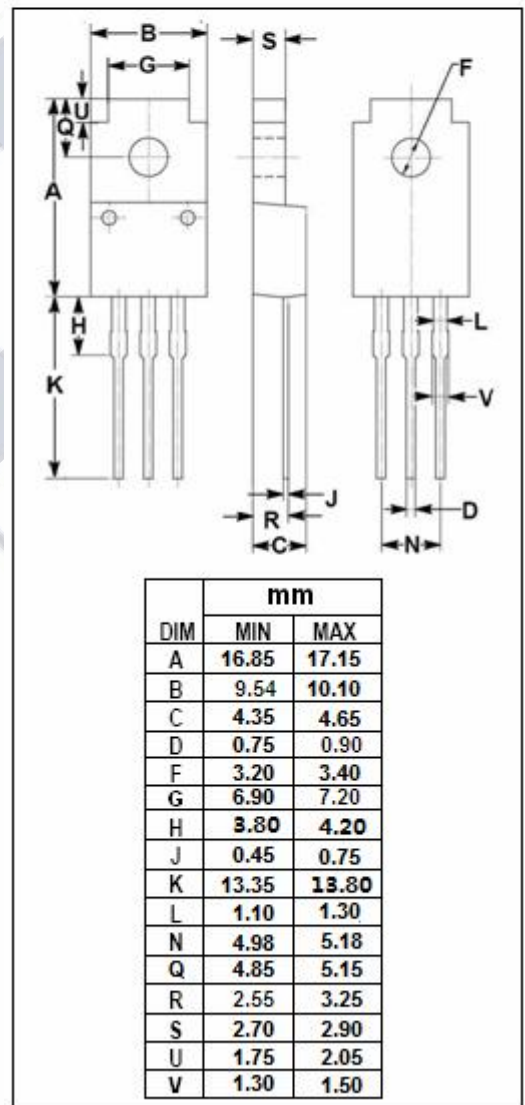
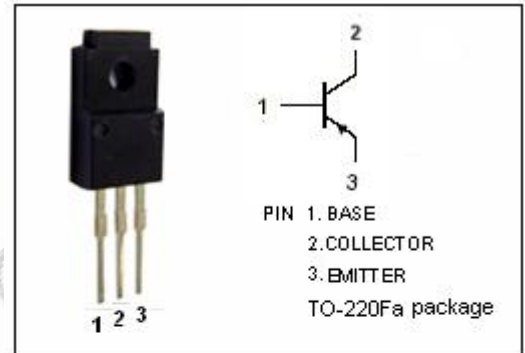
- Collector-Emitter Breakdown Voltage
: $V_{(BR)CEO} = -60V(\text{Min})$
- Good Linearity of h_{FE}
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for high current switching applications

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-60	V
V_{CEO}	Collector-Emitter Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-7	V
I_C	Collector Current-Continuous	-5	A
I_{cm}	Emitter Current-pulse	-8	A
P_C	Total Power Dissipation @ $T_C=25^\circ\text{C}$	30	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon PNP Power Transistor**2SA1279****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _c = -10mA ; I _B = 0	-60			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _c = -3A; I _B = -0.15A			-0.4	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _c = -3A; I _B = -0.15A			-1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -60V; I _E = 0			-1.0	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -7V; I _C = 0			-1.0	μ A
h _{FE-1}	DC Current Gain	I _c = -1.0A; V _{CE} = -1V	70		240	
h _{FE-2}	DC Current Gain	I _c = -3.0A; V _{CE} = -1V	30			
f _T	Current-Gain—Bandwidth Product	I _c = -1.0A; V _{CE} = -4V		60		MHz
C _{OB}	Output Capacitance	I _E =0; V _{CB} = -10V; f _{test} = 1.0MHz		200		pF

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