

# isc Silicon PNP Power Transistor

# 2SA1280

### **DESCRIPTION**

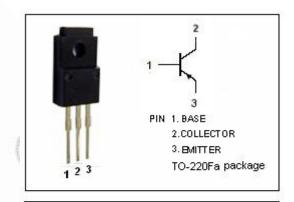
- · Collector-Emitter Breakdown Voltage
  - : V<sub>(BR)CEO</sub>= -150V(Min)
- Good Linearity of hFE
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

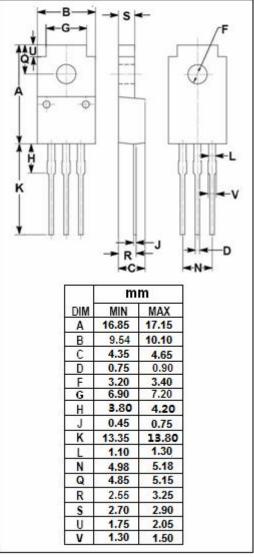
#### **APPLICATIONS**

· Designed for high current switching applications

# ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	-150	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-150	V
V <sub>EBO</sub>	Emitter-Base Voltage	-6	V
Ic	Collector Current-Continuous	-1.5	Α
Pc	Total Power Dissipation @ T <sub>C</sub> =25℃	25	W
TJ	Junction Temperature	150	°C
T <sub>stg</sub>	Storage Temperature Range	-55~150	°C







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#### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -10mA ; I <sub>B</sub> = 0	-150			V
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -0.5A; I <sub>B</sub> = -0.05A			-3.0	V
V <sub>BE</sub> (ON)	Base-Emitter On Voltage	I <sub>C</sub> = -50mA; Vce=-4V			-1.0	V
Ісво	Collector Cutoff Current	V <sub>CB</sub> = -120V; I <sub>E</sub> = 0			-1.0	μА
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -7V; I <sub>C</sub> = 0			-1.0	μА
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -50mA; V <sub>CE</sub> = -4V	60		200	
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -500mA; V <sub>CE</sub> = -10V	40			
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> = -0.5A;V <sub>CE</sub> = -10V	4			MHz
Сов	Output Capacitance	I <sub>E</sub> =0; V <sub>CB</sub> = -10V; f <sub>test</sub> = 1.0MHz		30		pF

#### **NOTICE:**

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