

isc Silicon PNP Power Transistor

2SA1288

DESCRIPTION

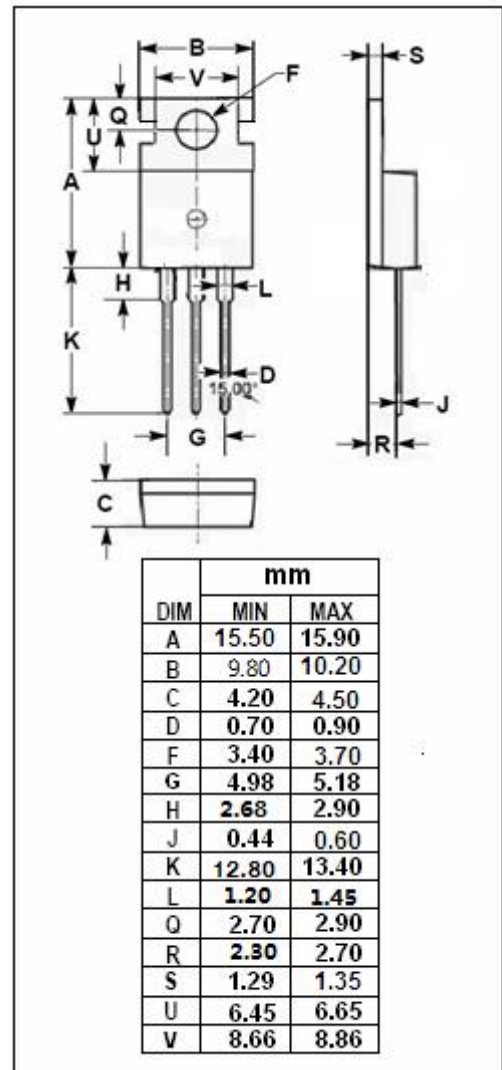
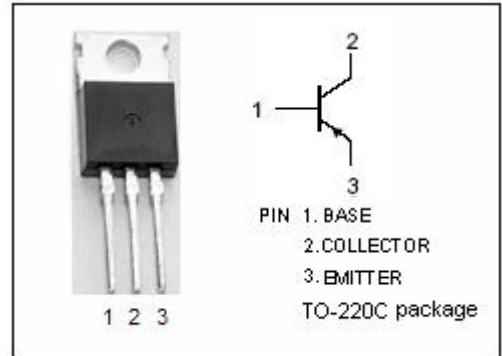
- Low Collector Saturation Voltage
- Large Current Capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Power amplifiers
- Switching regulator

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-80	V
V_{CEO}	Collector-Emitter Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-3	A
I_{CM}	Collector Current-Peak	-5	A
P_C	Total Power Dissipation @ $T_C=25^{\circ}\text{C}$	30	W
T_J	Junction Temperature	150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}\text{C}$



isc Silicon PNP Power Transistor**2SA1288****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)} -1	Collector-Emitter Saturation Voltage	I _C = -1.5A; I _B = -75mA			-0.4	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -40V; I _E = 0			-1	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -4V; I _C = 0			-1	μ A
h _{FE}	DC Current Gain	I _C = -0.5A ; V _{CE} = -2V	70		280	

◆ h_{FE} Classifications

Q	R	S
70-140	100-200	140-280

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