

isc Silicon PNP Power Transistors

2SA1306

DESCRIPTION

- Good Linearity of h_{FE}
- High Collector-Emitter Breakdown Voltage- $V_{(BR)CEO}$ = -160V(Min)
- Complement to Type 2SC3298
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

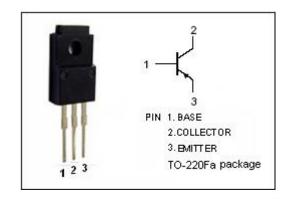
APPLICATIONS

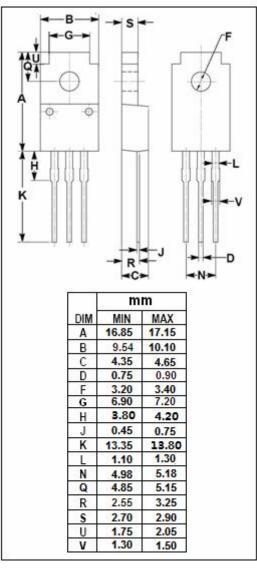


- · Power amplifier applications.
- Driver stage amplifier applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{СВО}	Collector-Base Voltage	-160	V	
V _{CEO}	Collector-Emitter Voltage	-160	V	
V _{EBO}	Emitter-Base Voltage -5		V	
lc	Collector Current-Continuous -1.5		Α	
l _Β	Base Current-Continuous	-0.15	Α	
Pc	Collector Power Dissipation		W	
TJ	Junction Temperature	150	$^{\circ}$ C	
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$	







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ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

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SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-160			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -500mA; I _B = -50mA			-1.5	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -500mA; V _{CE} = -5V			-1.0	V
Ісво	Collector Cutoff Current	V _{CB} = -160V; I _E = 0			-1.0	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C =0			-1.0	μА
h _{FE}	DC Current Gain	I _C = -100mA ; V _{CE} = -5V	70		240	
f⊤	Current-Gain—Bandwidth Product	I _C = -100mA ; V _{CE} = -10V		100		MHz
Сов	Output Capacitance	I _E = 0 ; V _{CB} = -10V;f _{test} = 1.0MHz		30		pF

♦ h_{FE} Classifications

0	Y
70-140	120-240

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