

isc Silicon PNP Power Transistor
2SA1355
DESCRIPTION

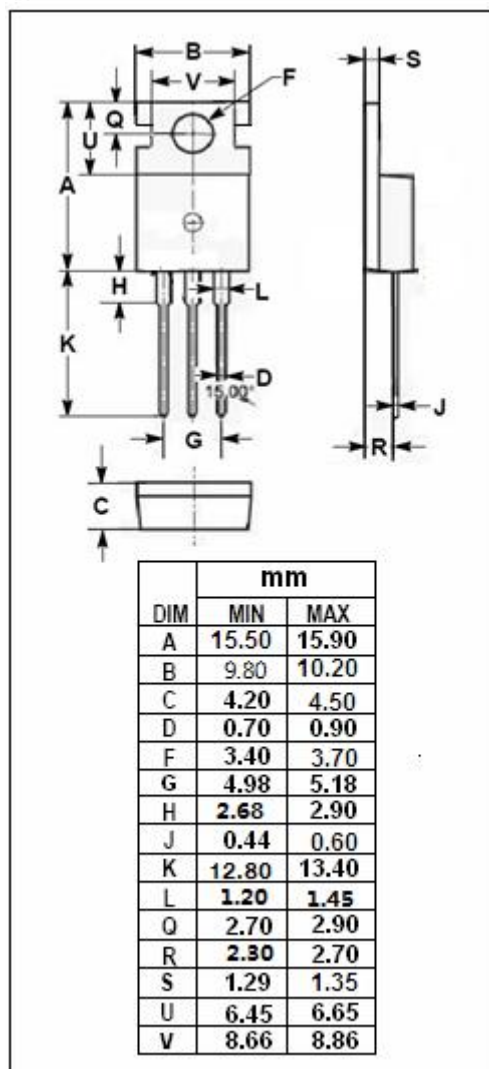
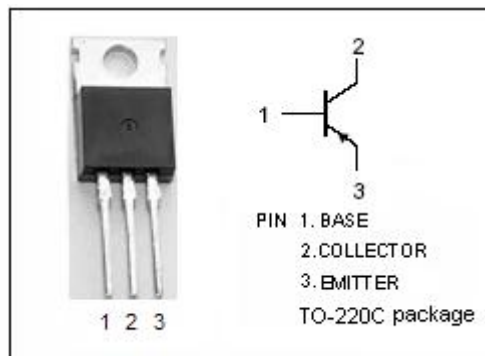
- TO-220 package
- High DC Current Gain
- Low Saturation Voltage
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- This type of power transistor is developed for high-speed switching and features a high h_{FE} at low $V_{CE(sat)}$, which is ideal for use as a driver in DC/DC converters and actuators.

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-70	V
V_{CEO}	Collector-Emitter Voltage	-70	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-4	A
P_T	Total Power Dissipation @ $T_C=25^\circ\text{C}$	30	W
	Total Power Dissipation @ $T_a=25^\circ\text{C}$	3.0	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~150	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -1mA ; R _{BE} = ∞	-70			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -2A; I _B = -0.2A			-0.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -2A; I _B = -0.2A			-1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -40V ; I _E =0			-10	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -4V; I _C =0			-10	μ A
h _{FE}	DC Current Gain	I _C = -1.0A ; V _{CE} = -2V	70		280	
f _T	Current-Gain—Bandwidth Product	I _C =-1A ; V _{CE} = -5V		40		MHz

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