

isc Silicon PNP Power Transistor

2SA1440

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
- : V_{CEO(SUS)}= -60V(Min)
- · High DC Current Gain-
 - : h_{FE} = 100(Min)@ (V_{CE} = -2V , I_{C} = -0.5A)
- · Low Saturation Voltage-
 - : $V_{CE(sat)} = -0.3V(Max)@ (I_C = -2A, I_B = -0.1A)$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

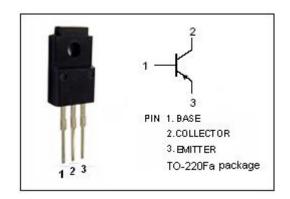


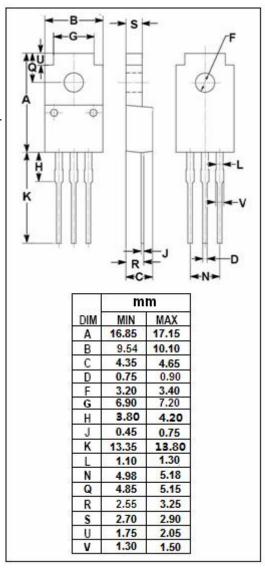
APPLICATIONS

 This type of power transistor is developed for high-speed switching and features a high h_{FE} at low V_{CE(sat)}, which is ideal for use as a driver in DC/DC converters and actuators.



SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-100	V	
V _{CEO}	Collector-Emitter Voltage	-60	V	
V _{EBO}	Emitter-Base Voltage	-7.0	V	
Ic	Collector Current-Continuous	-3	А	
I _{CM}	Collector Current-Pulse	-6	Α	
I _B	Base Current-Continuous	-1.5	Α	
P _T	Total Power Dissipation @T _C =25℃	15	W	
	Total Power Dissipation @T _a =25°C	2.0		
TJ	Junction Temperature 15		$^{\circ}\!\mathbb{C}$	
T _{stg}	Storage Temperature -55~150		$^{\circ}$	







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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -2.0A ; I _B = -0.2A, L= 1mH	-60			V
V _{CE} (sat)-1	Collector-Emitter Saturation Voltage	I _C = -2A; I _B = -0.1A			-0.3	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -0.15A			-0.5	V
V _{BE(sat)-1}	Base-Emitter Saturation Voltage	I _C = -2A; I _B = -0.1A			-1.2	V
V _{BE(sat)-2}	Base-Emitter Saturation Voltage	I _C = -3A; I _B = -0.15A			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -60V; I _E =0			-10	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C =0			-10	μА
h _{FE-1}	DC Current Gain	I _C = -0.3A ; V _{CE} = -2V	100			
h _{FE-2}	DC Current Gain	I _C = -0.5A ; V _{CE} = -2V	100		400	
h _{FE-3}	DC Current Gain	I _C = -2.0A ; V _{CE} = -2V	60			

♦ h_{FE-2} Classifications

M	L K	
100-200	150-300	200-400

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2