



isc Silicon PNP Power Transistor

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
- : V_{CEO(SUS)}= -60V(Min)
- · High DC Current Gain-
- : h_{FE} = 100(Min)@ (V_{CE} = -2V, I_{C} = -2A)
- · Low Saturation Voltage-
- : $V_{CE(sat)} = -0.3V(Max)@ (I_C = -6A, I_B = -0.3A)$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

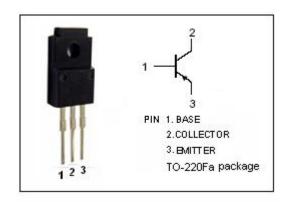


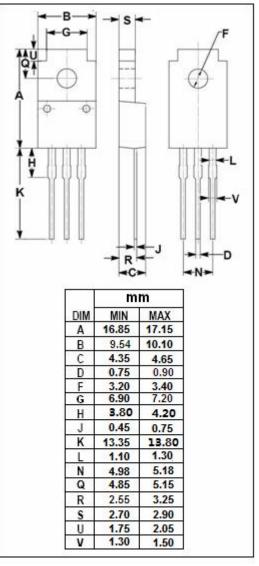
APPLICATIONS

• This type of power transistor is developed for high-speed switching and features a high hee at low VCE(sat), which is ideal for use as a driver in DC/DC converters and actuators

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-100	V	
Vceo	Collector-Emitter Voltage	-60	V	
V _{EBO}	Emitter-Base Voltage	-7.0	V	
Ic	Collector Current-Continuous	-10	Α	
Ісм	Collector Current-Pulse	-20	Α	
I _B	Base Current-Continuous	-5	Α	
P _T	Total Power Dissipation @T _C =25℃	30	W	
	Total Power Dissipation @T _a =25℃	2.0		
TJ	Junction Temperature	150	$^{\circ}$	
T _{stg}	Storage Temperature	-55~150	$^{\circ}$	







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2SA1443

ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -6.0A ; I _B = -0.6A, L= 1mH	-60			V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = -6A; I _B = -0.3A			-0.3	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = -8A; I _B = -0.4A			-0.5	V
V _{BE} (sat)-1	Base-Emitter Saturation Voltage	I _C = -6A; I _B = -0.3A			-1.2	V
V _{BE(sat)-2}	Base-Emitter Saturation Voltage	I _C = -8A; I _B = -0.4A			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -60V ; I _E =0			-10	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-10	μА
h _{FE-1}	DC Current Gain	I _C = -1A; V _{CE} = -2V	100			
h _{FE-2}	DC Current Gain	I _C = -2A; V _{CE} = -2V	100		400	
h _{FE-3}	DC Current Gain	I _C = -6A; V _{CE} = -2V	60			

♦ h_{FE-2} Classifications

M	L	K		
100-200	150-300	200-400		

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