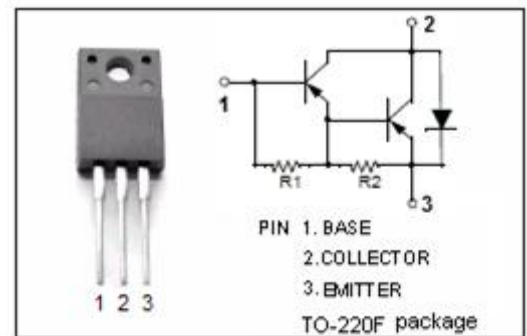


isc Silicon PNP Power Transistor
2SA1718
DESCRIPTION

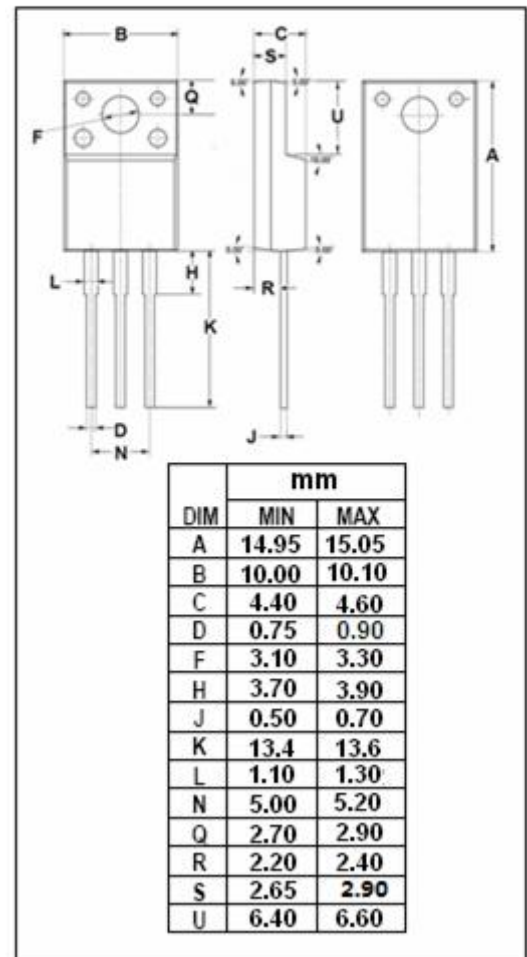
- Low Collector Saturation Voltage
: $V_{CE(sat)} = -0.5(V)(Max) @ I_C = -2A$
- High Switching Speed
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for audio and general purpose applications.


ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-100	V
V_{CEO}	Collector-Emitter Voltage	-100	V
V_{EBO}	Emitter-Base Voltage	-7	V
I_C	Collector Current-Continuous	-5	A
I_B	Base Collector Current-Continuous	-0.5	A
P_C	Total Power Dissipation @ $T_C = 25^\circ C$	20	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-55~150	$^\circ C$



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ELECTRICAL CHARACTERISTICS

T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _c = -30mA ; I _B = 0	-100			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _c = -2A; I _B = -2mA			-1.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _c = -2A; I _B = -2mA			-2.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0			-10	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -7V; I _C = 0			-5	mA
h _{FE-1}	DC Current Gain	I _c = -2A ; V _{CE} = -2V	2K		20K	
h _{FE-2}	DC Current Gain	I _c = -4A ; V _{CE} = -2V	500			

◆ h_{FE-1} Classifications

M	L	K
2K-5K	4K-10K	8K-20K

NOTICE:

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