

isc Silicon PNP Power Transistor

2SA1771

DESCRIPTION

- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= -80V(Min)
- · Low Collector Saturation Voltage-
 - : $V_{CE(sat)}$ = -0.4V(Max)@ I_C= -6A
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

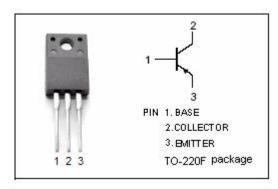


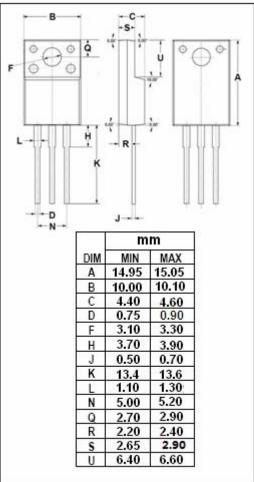
APPLICATIONS

· Designed for high current switching applications.

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{СВО}	Collector-Base Voltage	-80	V
V _{CEO}	Collector-Emitter Voltage	-80	V
V _{EBO}	Emitter-Base Voltage	-14	V
lc	Collector Current-Continuous	-12	Α
l _Β	Base Current-Continuous	-1.2	Α
Pc	Collector Power Dissipation @T _C =25°C	30	W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature	-55~150	$^{\circ}$







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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -50mA; I _B = 0	-80			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -6A; I _B = -0.3A			-0.4	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -6A; I _B = -0.3A			-1.2	V
І _{СВО}	Collector Cutoff Current	V _{CB} = -80V; I _E = 0			-10	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -14V; I _C = 0			-10	μА
h _{FE-1}	DC Current Gain	I _C = -1A; V _{CE} = -1V	100		320	
h _{FE-2}	DC Current Gain	I _C = -6A; V _{CE} = -1V	40			



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