Silicon PNP Power Transistors 2SA1788

DESCRIPTION

·With TO-247 package

·Complement to type 2SC4652

APPLICATIONS

·For audio output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

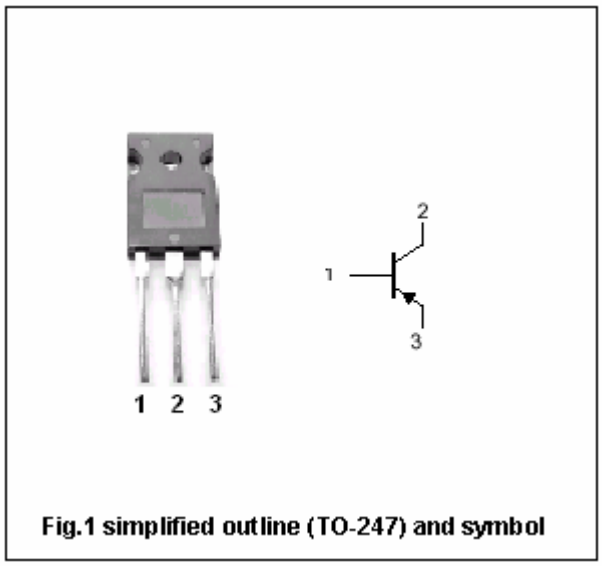

Fig.1 simplified outline (TO-247) and symbol

Absolute maximum ratings(Tc=25□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-120	V
V_{CEO}	Collector-emitter voltage	Open base	-120	V
V_{EBO}	Emitter-base voltage	Open collector	-5	V
I_C	Collector current		-8	A
P_C	Collector power dissipation	T_C=25□	80	W
T_j	Junction temperature		150	□
T_{stg}	Storage temperature		-55~150	□

CHARACTERISTICS

Tj=25□ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-emitter breakdown voltage	I_C=-25mA; I_B=0	-120			V
$V_{(BR)CBO}$	Collector-base breakdown voltage	I_C=-1mA; I_E=0	-120			V
$V_{(BR)EBO}$	Emitter-base breakdown voltage	I_E=-1mA; I_C=0	-5			V
V_{CEsat}	Collector-emitter saturation voltage	I_C=-6A;I_B=-0.6 A			-2.0	V
V_{BEsat}	Base-emitter saturation voltage	I_C=-6A;I_B=-0.6 A			-2.5	V
I_{CBO}	Collector cut-off current	V_{CB}=-120V; I_E=0			-10	μA
I_{EBO}	Emitter cut-off current	V_{EB}=-5V; I_C=0			-10	μA
h_{FE}	DC current gain	I_C=-1A ; V_{CE}=-5V	60		320	

◆ **h_{FE} classifications**

D	E	F
60-120	100-200	160-320

PACKAGE OUTLINE

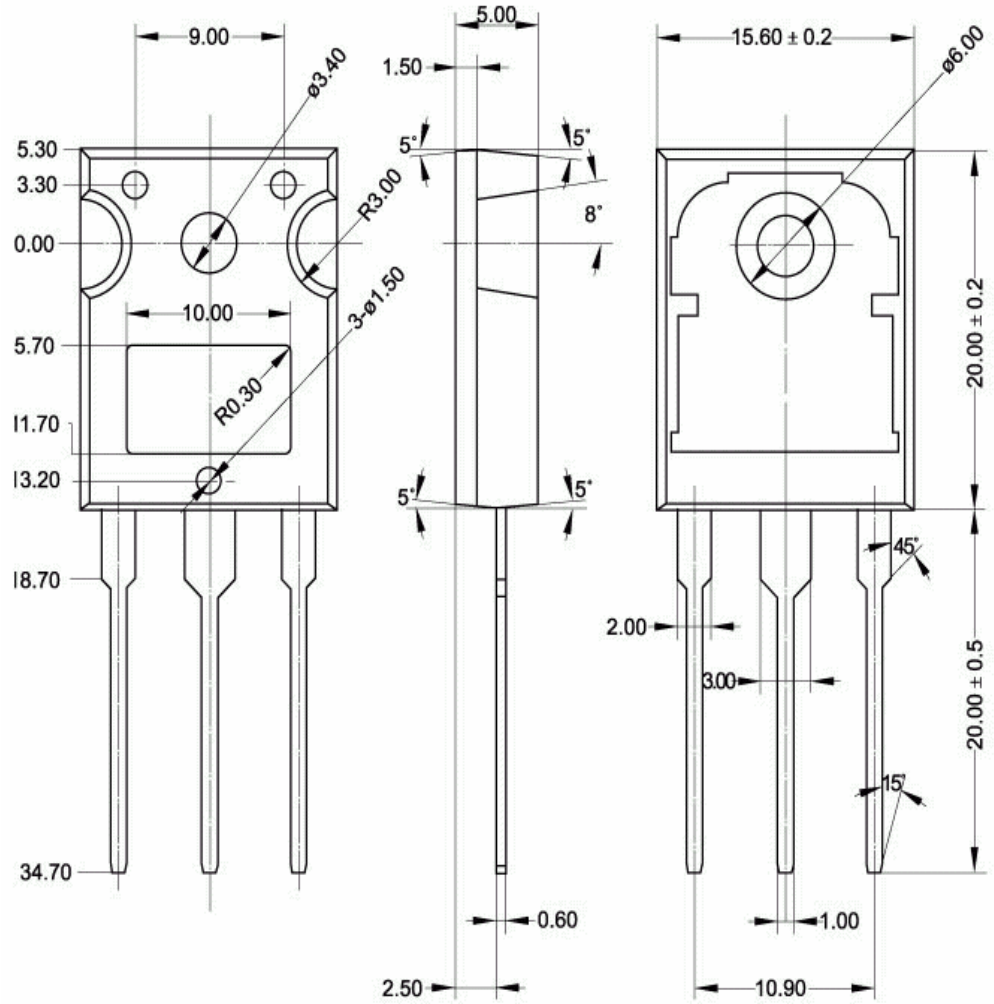

Fig.2 Outline dimensions