

isc Silicon PNP Power Transistor

2SA2004

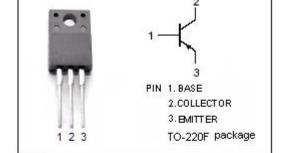
DESCRIPTION

- Silicon PNP epitaxial planner type
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



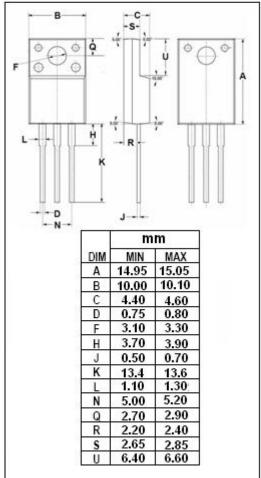
APPLICATIONS

- · Power amplifier applications
- Driver stage amplifier applications



ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-60	V	
Vceo	Collector-Emitter Voltage	-60	V	
V _{EBO}	Emitter-Base Voltage	-5	V	
I _C	Collector Current-Continuous	-8	Α	
I_{B}	Base Current-Continuous	-1	А	
Pc	Collector Power Dissipation @ Tc=25℃	20	W	
TJ	Junction Temperature	150	$^{\circ}$	
T _{stg}	Storage Temperature Range	-55~150	°C	





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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -10mA; I _B = 0	-60			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B =- 0.25A			-1.2	V
V _{BE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B =- 0.25A			-1.7	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -60V ; I _E = 0			-10	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V ; I _C = 0			-10	μА
h _{FE-1}	DC Current Gain	Ic= -0.1A; Vc== -2V	100		320	
h _{FE-2}	DC Current Gain	I _C = -5A ; V _{CE} = -2V	30			



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