

isc Silicon PNP Power Transistor
2SA505
DESCRIPTION

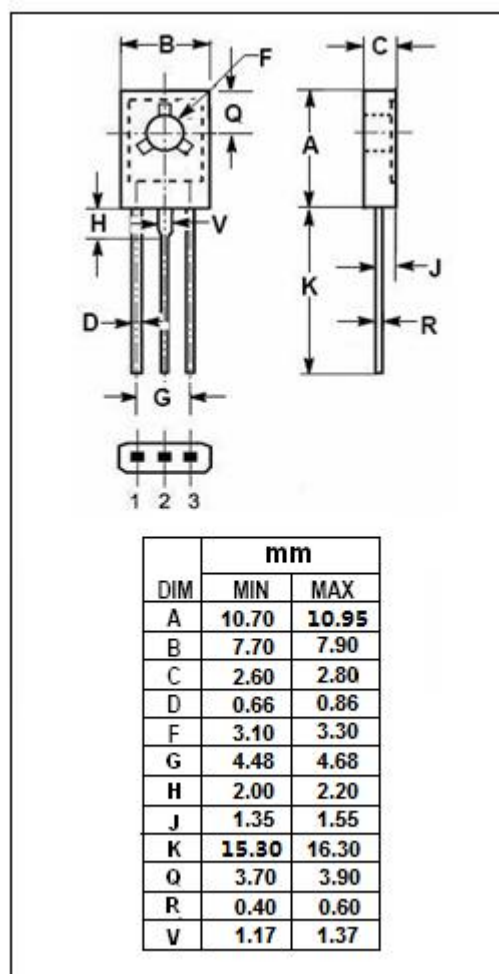
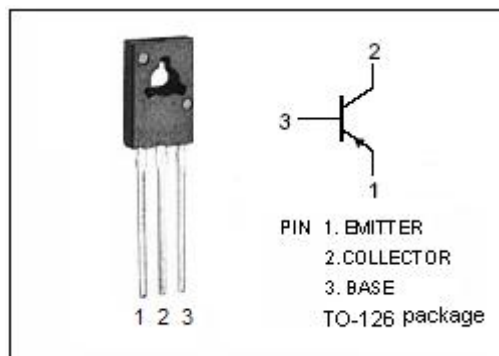
- Collector-Emitter Breakdown Voltage-
 $V_{(BR)CEO} = -50V$ (Min.)
- Collector-Emitter Saturation Voltage-
 $V_{CE(sat)} = -0.8V$ (Max.) @ $I_C = -500mA$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for medium power amplifier applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-60	V
V_{CEO}	Collector-Emitter Voltage	-50	V
V_{EBO}	Emitter-Base Voltage	-5.0	V
I_C	Collector Current-Continuous	-1	A
I_E	Emitter Current-Continuous	1	A
P_C	Collector Power Dissipation	1	W
T_J	Junction Temperature	150	°C
T_{stg}	Storage Temperature Range	-55~150	°C



isc Silicon PNP Power Transistor**2SA505****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-50			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -1mA; I _C = 0	-5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -500mA; I _B = -50mA			-0.8	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -500mA; V _{CE} = -2V			-1.3	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -30V; I _E = 0			-1.0	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5; I _C = 0			-1.0	μ A
h _{FE-1}	DC Current Gain	I _C = -50mA; V _{CE} = -2V	40		240	
h _{FE-2}	DC Current Gain	I _C = -800mA; V _{CE} = -2V	13			

◆ **h_{FE-1} Classifications**

R	O	Y
40-80	70-140	120-240

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