

isc Silicon PNP Power Transistor

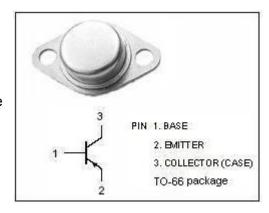
DESCRIPTION

- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= -110V(Min)
- · Low Collector Saturation Voltage
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.



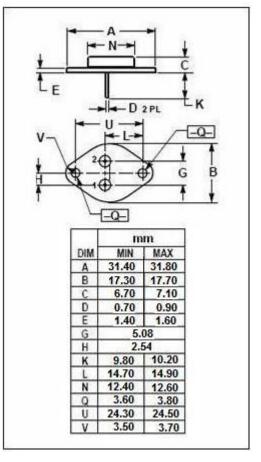
APPLICATIONS

• Designed for general purpose power amplifier applications



ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-110	V
V _{CEO}	Collector-Emitter Voltage	-110	V
V _{EBO}	Emitter-Base Voltage	-6	V
Ic	Collector Current-Continuous	-2	А
Pc	Total Power Dissipation @ T _C =25℃	23	W
TJ	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$





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2SA762

ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-110			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -1mA; I _E = 0	-110			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -2A; I _B = -0.2A			-1.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -2A; I _B = -0.2A			-2.0	V
Ісво	Collector Cutoff Current	V _{CB} = -110V; I _E = 0			-10	μА
ІЕВО	Emitter Cutoff Current	V _{EB} = -6V; I _C = 0			-10	μ A
h _{FE}	DC Current Gain	I _C = -1A; V _{CE} = -4V	50			

Notice:

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