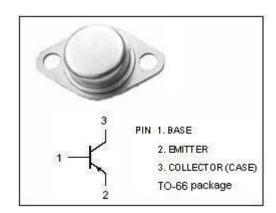


isc Silicon PNP Power Transistor

2SA764

DESCRIPTION

- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= -60V(Min)
- · Low Collector Saturation Voltage-
 - : V_{CE(sat)}= 1.5V(Max.)@ I_C= 4A
- Complement to Type 2SC1444
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation

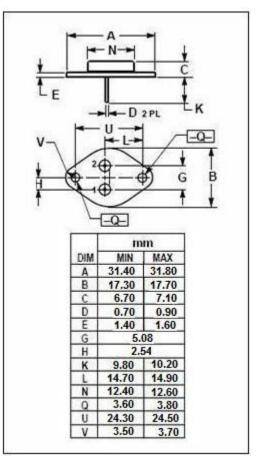


APPLICATIONS

• Designed for general purpose power amplifier applications

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-60	V
V _{CEO}	Collector-Emitter Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-6	V
Ic	Collector Current-Continuous	-6	А
Pc	Total Power Dissipation @ T _C =25℃	40	W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$





isc Silicon PNP Power Transistor

2SA764

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-60			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -1mA; I _E = 0	-60			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -4A; I _B = -0.4A			-1.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = -4A; I _B = -0.4A			-2.0	٧
I _{CBO}	Collector Cutoff Current	V _{CB} = -60V; I _E = 0			-10	μА
ІЕВО	Emitter Cutoff Current	V _{EB} = -6V; I _C = 0			-10	μА
h _{FE}	DC Current Gain	I _C = -1A; V _{CE} = -4V	50			
f⊤	Current-Gain—Bandwidth Product	I _C = -0.5A; V _{CE} = -12V		10		MHz

Notice:

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