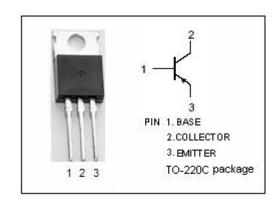


# isc Silicon PNP Power Transistor

### **DESCRIPTION**

- · Collector-Emitter Breakdown Voltage
  - : V<sub>(BR)CEO</sub>= -150V(Min)
- Complement to Type 2SC1913
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

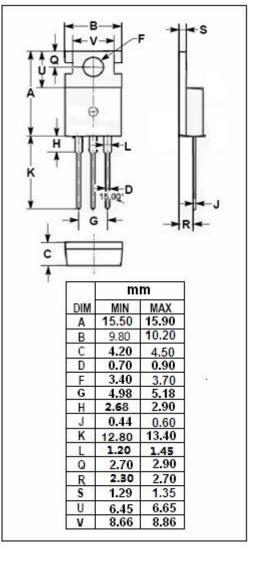


### **APPLICATIONS**

• Designed for AF high power dirver applications.

## ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>СВО</sub>	Collector-Base Voltage	-150	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-150	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
Ic	Collector Current-Continuous	-1	А
I <sub>CM</sub>	Collector Current-Peak	-1.5	А
Pc	Total Power Dissipation @ T <sub>C</sub> =25℃	15	W
TJ	Junction Temperature 150		$^{\circ}$ C
T <sub>stg</sub>	Storage Temperature Range	-55~150	${\mathbb C}$





# isc Silicon PNP Power Transistor

2SA913

### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -0.1mA; I <sub>B</sub> = 0	-150			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -10 μ A; I <sub>C</sub> = 0	-5			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	llector-Emitter Saturation Voltage I <sub>C</sub> = -500mA; I <sub>B</sub> = -50mA			-1.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = -500mA; I <sub>B</sub> = -50mA			-1.5	V
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -150mA; V <sub>CE</sub> = -10V	65		330	
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -500mA; V <sub>CE</sub> = -5V	50			
Сов	Output Capacitance	I <sub>E</sub> =0; V <sub>CB</sub> = -100V;f= 1.0MHz			15	pF
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>E</sub> = 50mA; V <sub>CE</sub> = -10V		120		MHz

### ♦ h<sub>FE-1</sub> Classifications

Р	Q	R	S
65-110	90-155	130-220	185-330

### Notice:

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