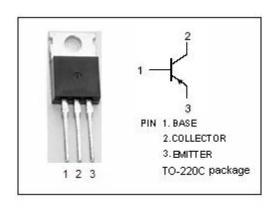


isc Silicon PNP Power Transistor

DESCRIPTION

- · Collector-Emitter Breakdown Voltage
 - : V_{(BR)CEO}= -150V(Min)
- · Good Linearity of hFE
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

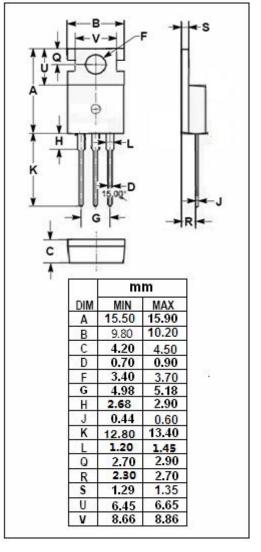


APPLICATIONS

· Designed for general purpose applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{СВО}	Collector-Base Voltage	-150	V
V _{CEO}	Collector-Emitter Voltage	-150	V
V _{EBO}	Emitter-Base Voltage	-6	V
Ic	Collector Current-Continuous	-2	А
I _B	Base Current-Continuous	-1	А
Pc	Total Power Dissipation @ T _C =25℃	30	W
TJ	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$





isc Silicon PNP Power Transistor

2SA957

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT		
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -25mA; I _B = 0	-150			V		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -0.7A; I _B = -70mA			-1.5	V		
I _{CBO}	Collector Cutoff Current	V _{CB} = -150V; I _E = 0			-100	μА		
ІЕВО	Emitter Cutoff Current	V _{EB} = -6V; I _C = 0			-1.0	mA		
h _{FE}	DC Current Gain	I _C = -0.7A; V _{CE} = -10V	40					
f⊤	Current-Gain—Bandwidth Product	I _E = 0.2A; V _{CE} = -12V		20		MHz		
Switching times								
tr	Rise Time			0.4		μ \$		
t _{stg}	Storage Time	I_{C} = -1A, R_{L} = 20 Ω , I_{B1} = - I_{B2} = -0.1A, V_{CC} = -20V		1.5		μ \$		
t _f	Fall Time			0.5		μ S		

Notice:

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