

isc Silicon PNP Power Transistor

2SAR586D

DESCRIPTION

- Suitable for middle power drivers
- Low $V_{CE(sat)}$
 $V_{CE(sat)} \leq -0.32V @ (I_C = -2A, I_B = -100mA)$
- Complementary NPN types: 2SCR586D
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

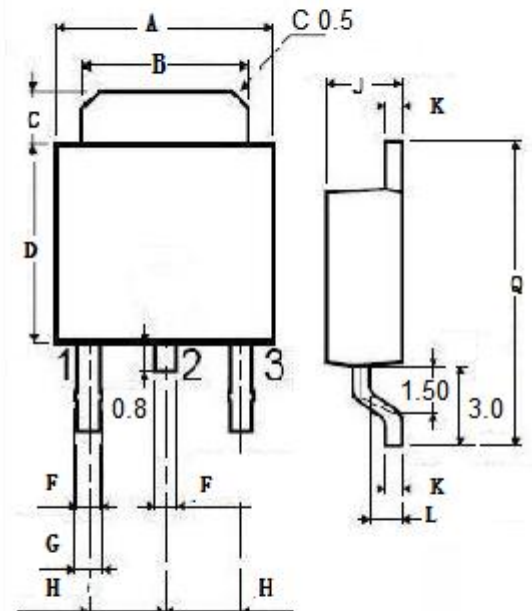
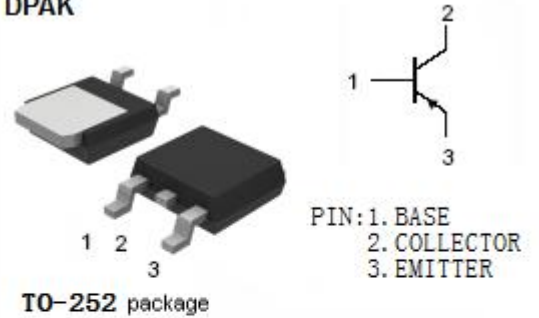
APPLICATIONS

- Low frequency amplifier

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-80	V
V_{CEO}	Collector-Emitter Voltage	-80	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current-Continuous	-5	A
I_{CM}	Collector Current-Peak	-10	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ C$	10	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-55~150	$^\circ C$

DPAK



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
F	0.65	
G	0.75	
H	2.10	2.50
J	2.10	2.40
K	0.40	0.60
L	0.90	1.10
Q	9.90	10.1

isc Silicon PNP Power Transistor**2SAR586D****ELECTRICAL CHARACTERISTICS** $T_c=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
BV_{CBO}	Collector-Base breakdown voltage	$I_C=-100\mu\text{A}$	-80			V
BV_{CEO}	Collector-Emitter breakdown voltage	$I_C=-1\text{mA}$	-80			V
BV_{EBO}	Emitter-Base breakdown voltage	$I_E=-100\mu\text{A}$	-6			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -2\text{A}; I_B = -100\text{mA}$			-0.32	V
I_{CBO}	Collector Cutoff Current	$V_{CB} = -80\text{V}; I_E = 0$			-1.0	μA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -4\text{V}; I_C = 0$			-1.0	μA
h_{FE}	DC Current Gain	$I_C = -0.5\text{A}; V_{CE} = -3\text{V}$	120		390	
C_{OB}	Output Capacitance	$I_E = 0; V_{CB} = -10\text{V}; f = 1.0\text{MHz}$		100		pF
f_T^{NOTE}	Current-Gain—Bandwidth Product	$I_C = -0.5\text{A}; V_{CE} = -10\text{V}, f = 100\text{MHz}$		200		MHz

NOTE:Pulsed